

NOTES:

1. CHIPS POLAR AND GND PINS ARE ALL STANDARD EXCEPT 854B (+5V PIN 8, GND PIN 1) AND 4164 (+5V PIN 8, GND PIN 16). ALL RESISTORS ARE 1/4W 5% EXCEPT AS NOTED ON SCHEMATIC.
2. B E52 AND E53 ARE FUTURE OPTION.

STARTING ADDRESS		SWITCH SETTINGS FOR E38									
DECIMAL (K)	OCTAL	PA17	PA16	PA15	PA14	PA13	PA12	PA11	PA10	PA9	PA8
0	00000000										
8	00000000										
16	00100000										
24	00100000										
32	00200000										
40	00200000										
48	00300000										
56	00300000										
64	00400000										
72	00400000										
80	00500000										
88	00500000										
96	00600000										
104	00600000										
112	00700000										
120	00700000										

STARTING ADDRESS		SWITCH SETTINGS FOR E38									
DECIMAL (K)	OCTAL	PA21	PA20	PA19	PA18	PA17	PA16	PA15	PA14	PA13	PA12
128-248	01000000-01700000										
256-376	02000000-02700000										
384-504	03000000-03700000										
512-632	04000000-04700000										
640-760	05000000-05700000										
768-888	06000000-06700000										
896-1016	07000000-07700000										
1024-1144	08000000-08700000										
1152-1272	09000000-09700000										
1280-1400	10000000-10700000										
1408-1528	11000000-11700000										
1536-1656	12000000-12700000										
1664-1784	13000000-13700000										
1792-1912	14000000-14700000										
1920-2040	15000000-15700000										
2048-2168	16000000-16700000										
2176-2296	17000000-17700000										

CSR STARTING ADDRESS		SWITCH SETTINGS FOR E28									
DECIMAL	OCTAL	CS003	CS002	CS001	CS000	CS003	CS002	CS001	CS000	CS003	CS002
17772100											
17772102											
17772104											
17772106											
17772110											
17772112											
17772114											
17772116											
17772120											
17772122											
17772124											
17772126											
17772130											
17772132											
17772134											
17772136											

POWER DISTRIBUTION

MS11-P  
MEMORY

DATE: 01-08-83  
CHK'D: [Signature]  
PRG: JANELOS.MSP/MS01.DPL/05-JAN-83 15:42  
FIRST USED ON OPTION/MODEL: MS11-P B-DD-M8743-BA

SIZE: K  
CODE: CS  
NUMBER: M8743-BA-MS01

REV: B

8

7

6

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4

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1

MS11-P

MS01 PU1 MS01 PU4 MS01 PU5 MS01 PU7

MS01 PU2 MS01 PU3

MS01 PU6

MS01 PU8

MS01 PU9

MS01 PU10

MS01 PU11

MS01 PU12

MS01 PU13

MS01 PU14

MS01 PU15

MS01 PU16

MS01 PU17

MS01 PU18

MS01 PU19

MS01 PU20

MS01 PU21

MS01 PU22

MS01 PU23

MS01 PU24

MS01 PU25

MS01 PU26

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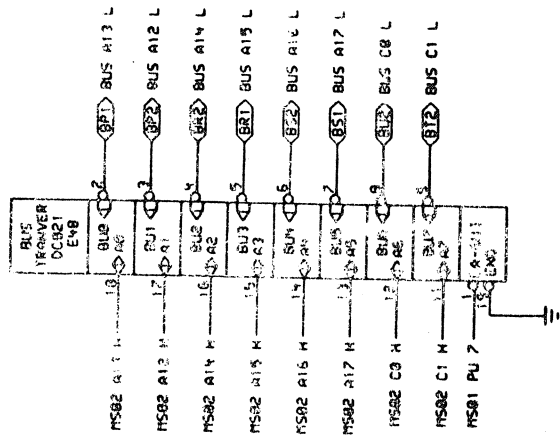
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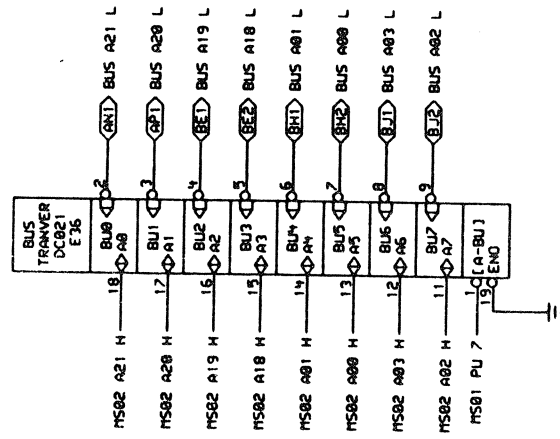
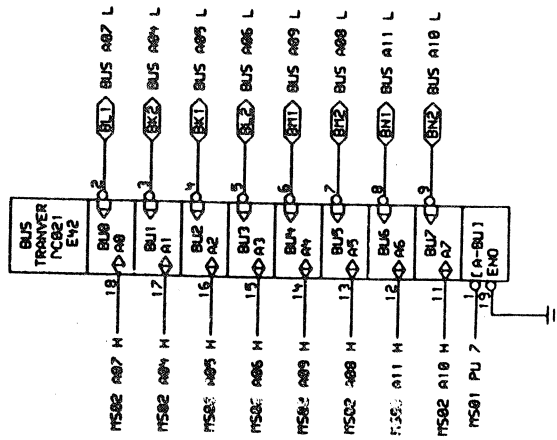
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MS01 PU656

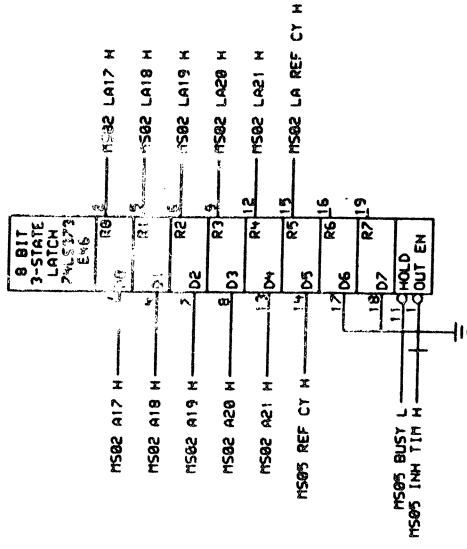
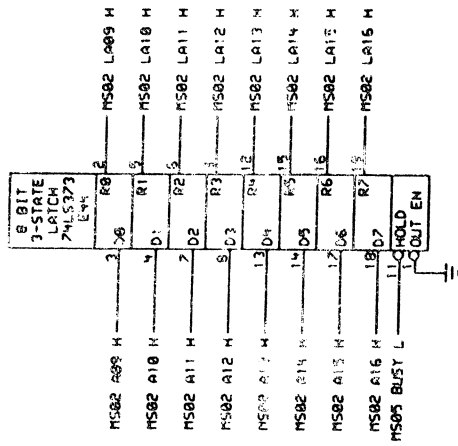
MS0

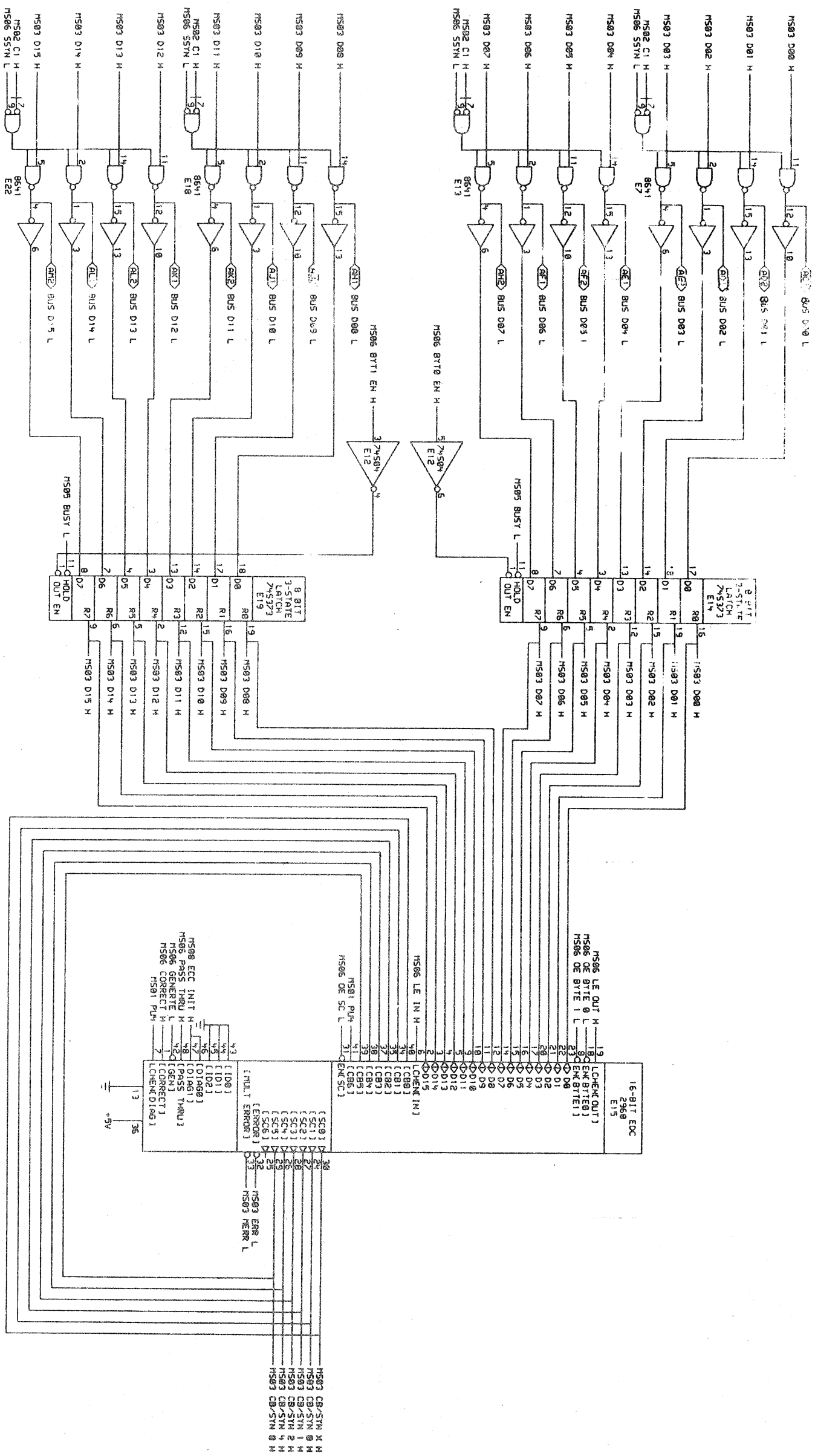


## ADDRESS BUS RECEIVERS



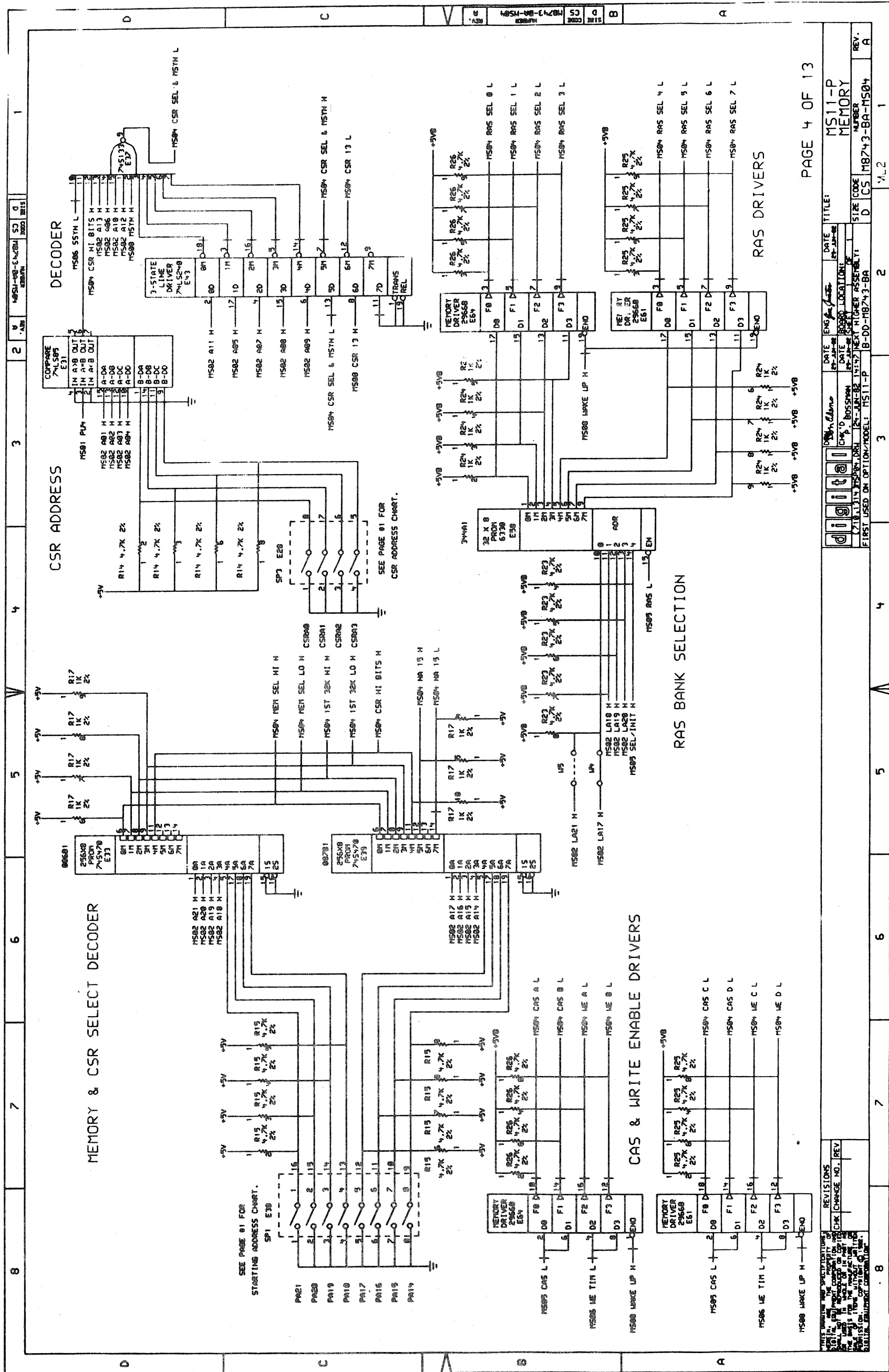
## ADDRESS LATCHES



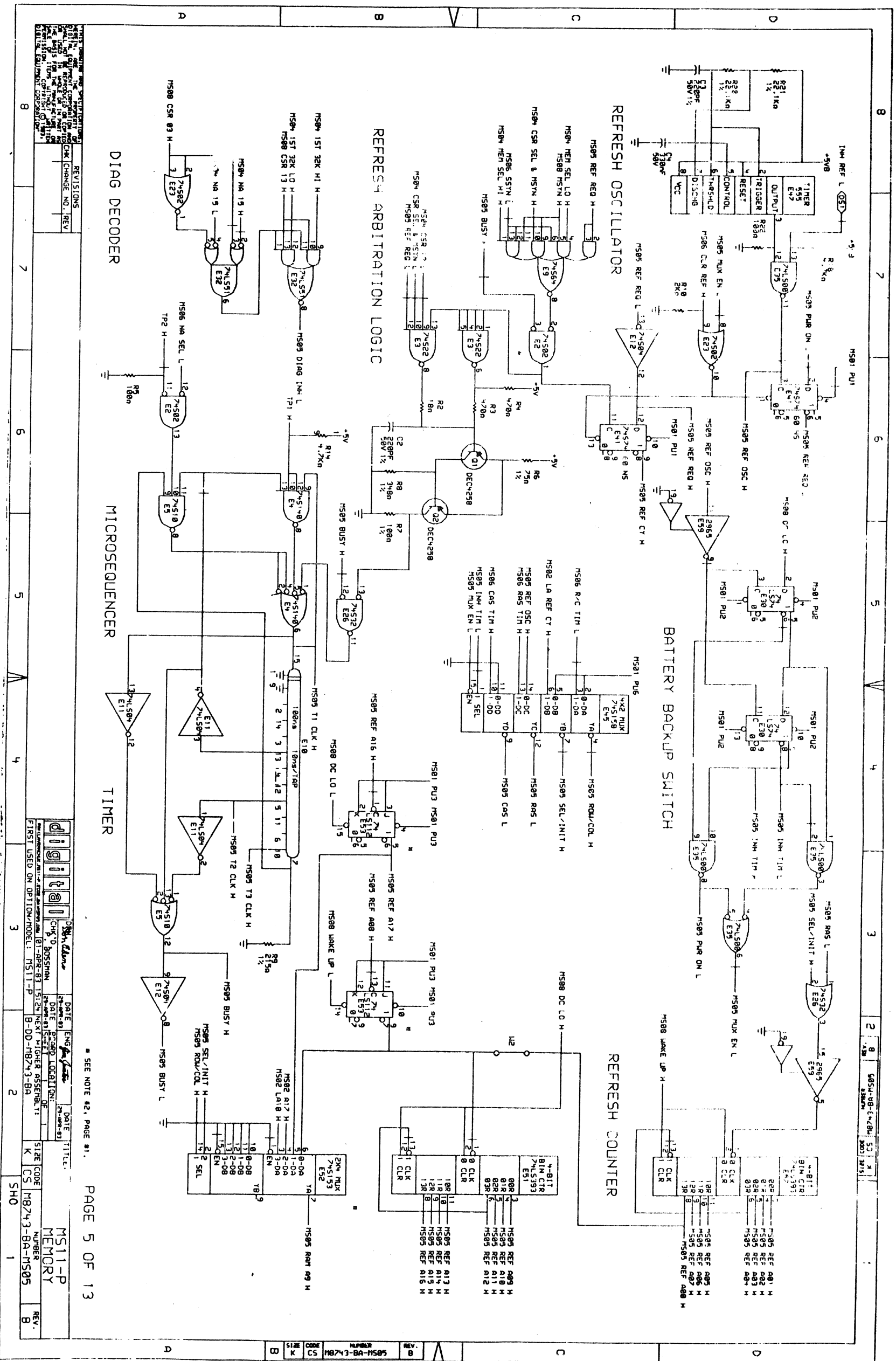


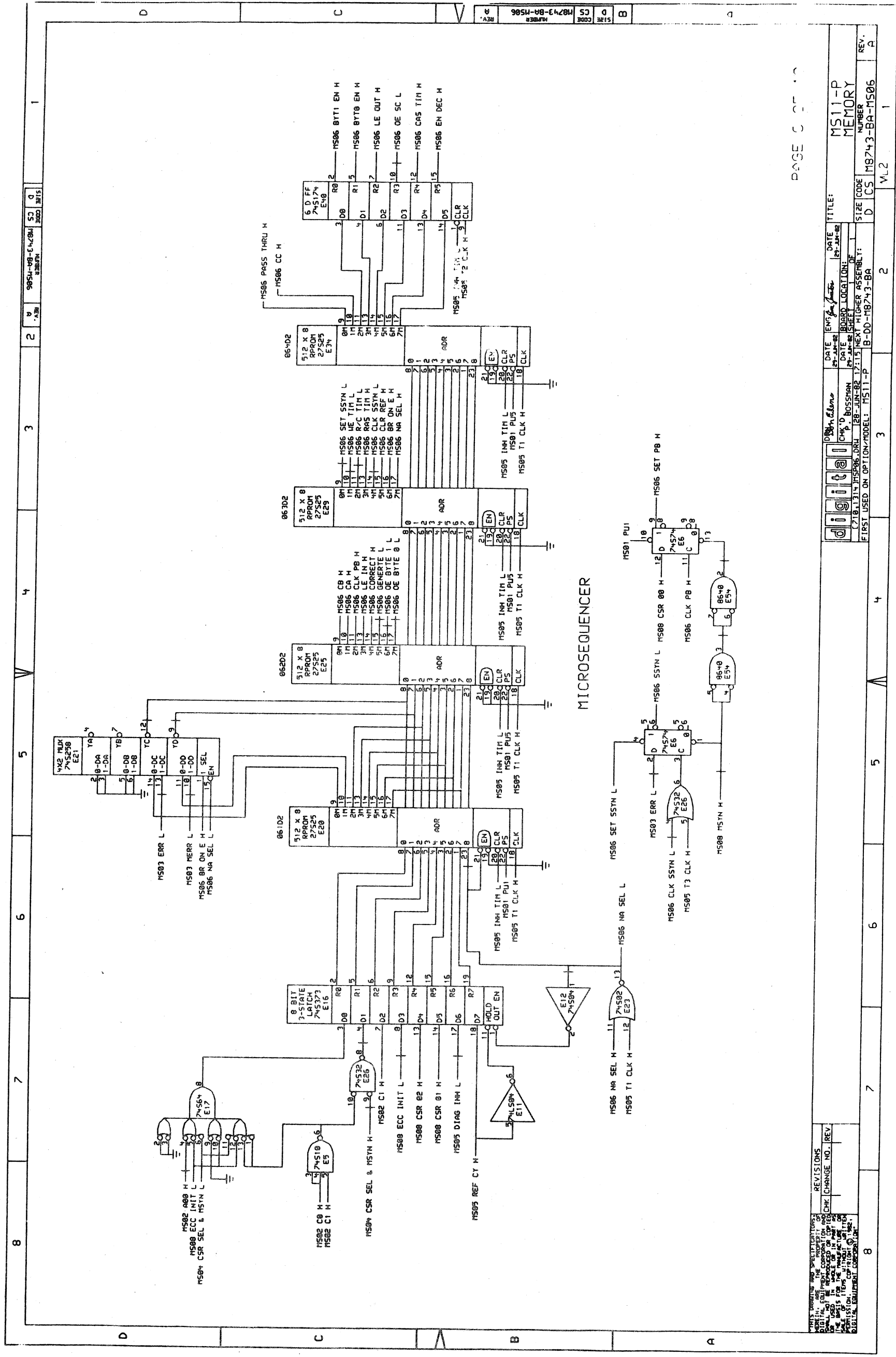
## EDC LOGIC

DIGITAL		DATE		DATE		TITLE:	
MS11-P		27-APR-82		27-APR-82		MS11-P	
CHG'D BY BROSCHOW		DATE		REMOVED LOCATION:		MEMORY	
C7181314 JMS03 DATA		24-APR-82		11:33		MOTUO NUMBER	
ELECT USED ON OPTION MODEL: MS11-P		P. NO. 0920-2		P. NO. 0920-2		REV.	





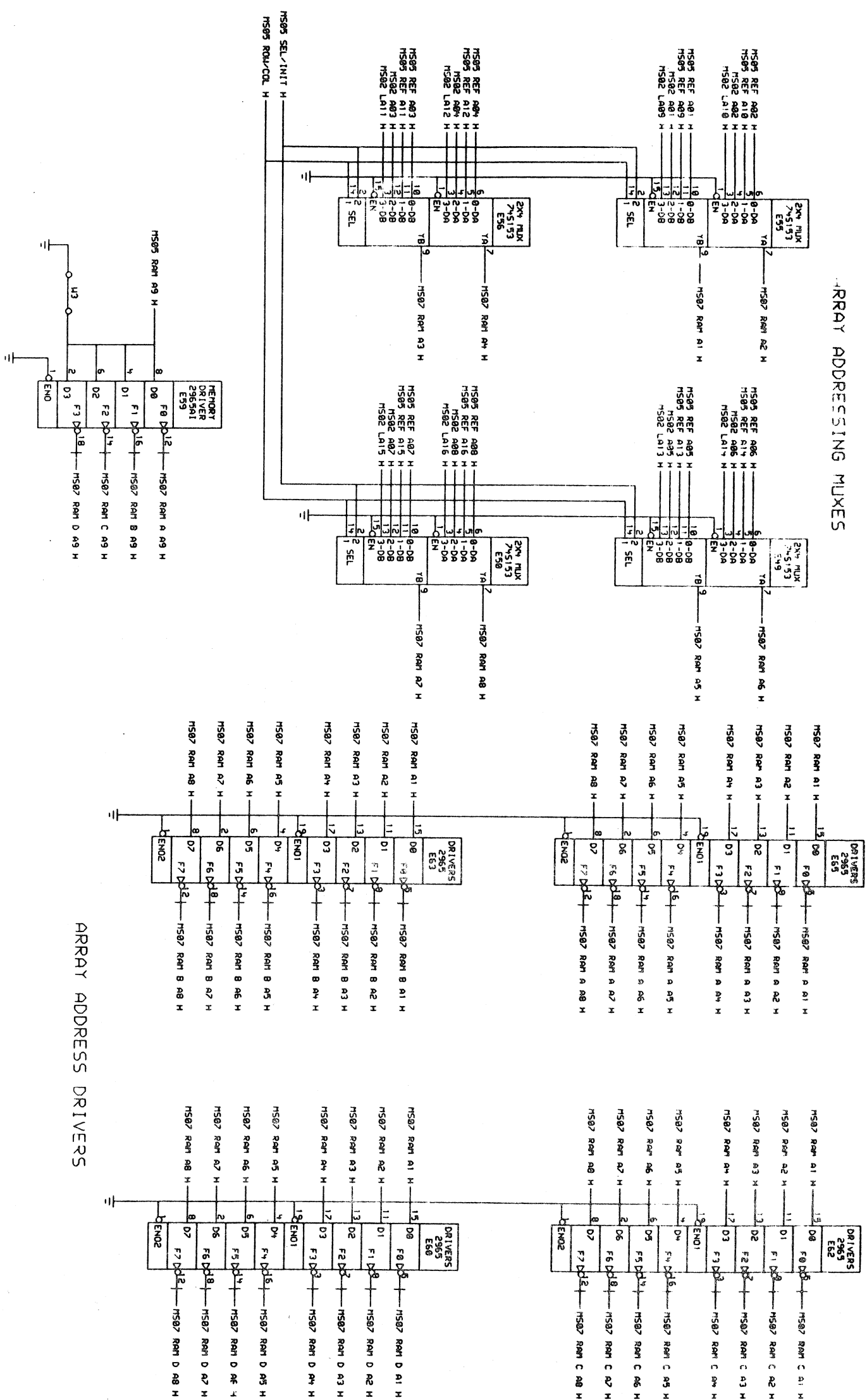




PAGE 001

REVISIONS		DATE		TITLE	
1	INITIAL	28-JAN-82	28-JAN-82	MS11-P	MEMORY
2	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
3	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
4	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
5	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
6	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
7	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY
8	CHG	28-JAN-82	28-JAN-82	MS11-P	MEMORY

# TRRAY ADDRESSING MIXES



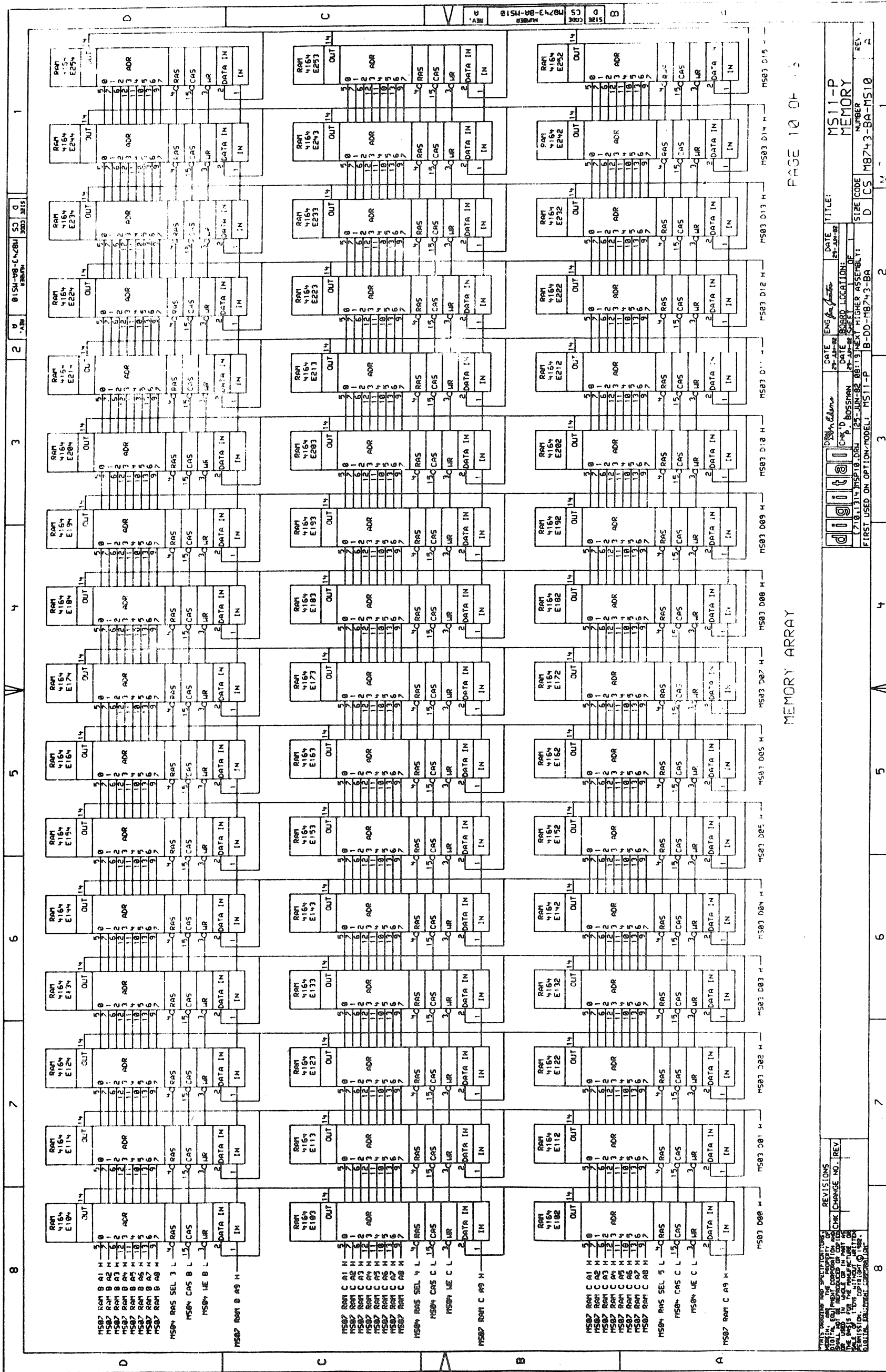
# ARRAY ADDRESS DRIVERS

[illegible]





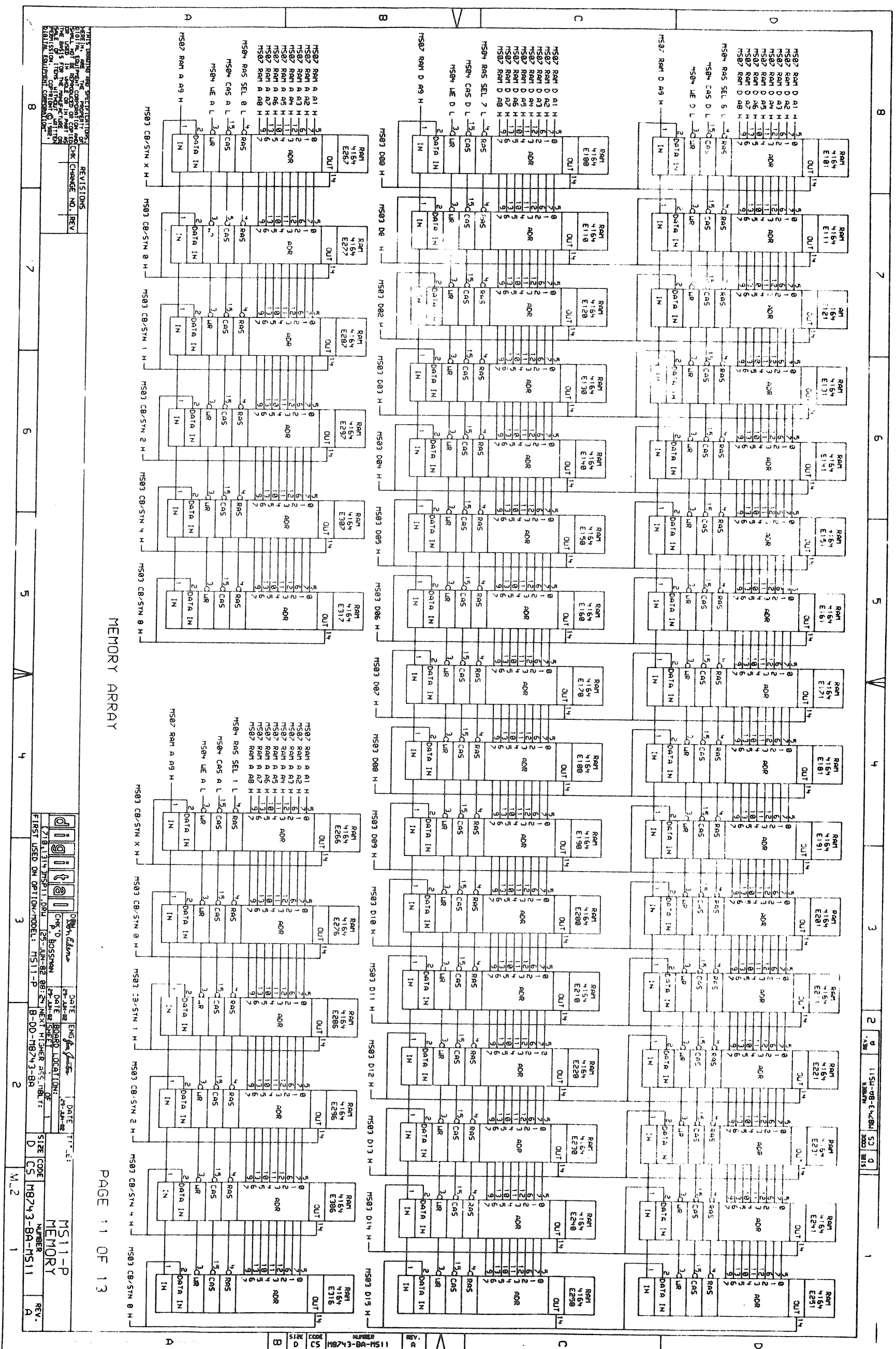




MEMORY ARRAY

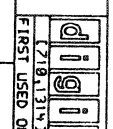
<b>REVISIONS</b> DATE: 12-11-82 BY: P. BOSSMAN CHECKED: 12-11-82 APPROVED: 12-11-82 REVISION NO. 1		<b>MS11-P</b> <b>MEMORY</b> NUMBER: M8743-BA-MS10 REV. 1	
<b>DATE</b> : 12-11-82 <b>BY</b> : P. BOSSMAN <b>CHECKED</b> : 12-11-82 <b>APPROVED</b> : 12-11-82		<b>SIZE CODE</b> : D <b>CODE</b> : CS <b>MODEL</b> : M8743-BA	
<b>DATE</b> : 12-11-82 <b>BY</b> : P. BOSSMAN <b>CHECKED</b> : 12-11-82 <b>APPROVED</b> : 12-11-82		<b>SIZE CODE</b> : D <b>CODE</b> : CS <b>MODEL</b> : M8743-BA	





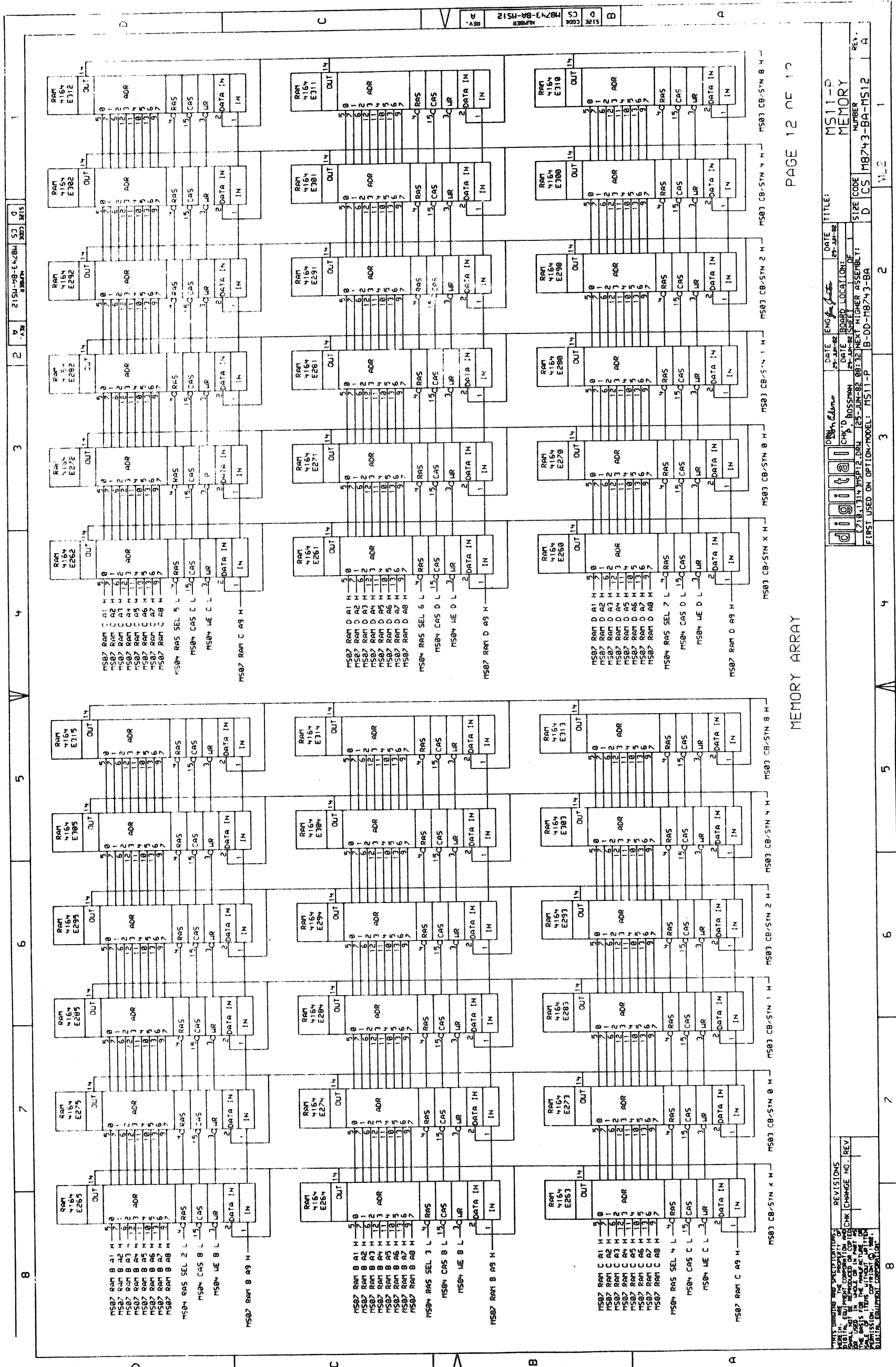
MEMORY ARRAY

REVISIONS			
NO.	DATE	BY	REV.
1	08-01-88	MS11-P	1
2	08-01-88	MS11-P	2
3	08-01-88	MS11-P	3
4	08-01-88	MS11-P	4
5	08-01-88	MS11-P	5
6	08-01-88	MS11-P	6
7	08-01-88	MS11-P	7
8	08-01-88	MS11-P	8

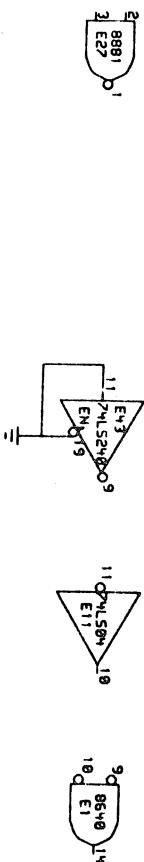
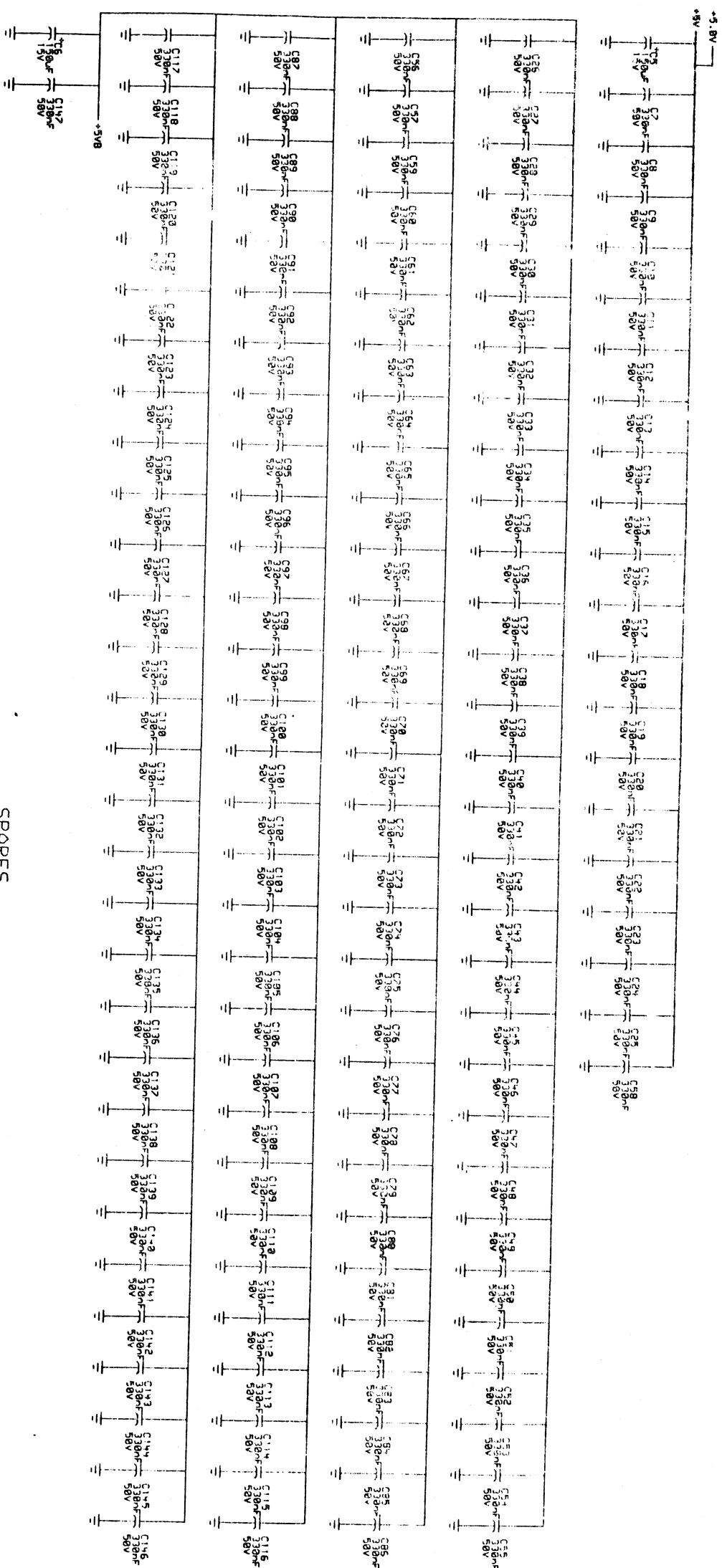


MS11-P  
MEMORY  
NUMBER  
REV. A

MS11-P



REV. NO.		DATE		BY		CHK		REV. NO.		DATE		BY		CHK		REV. NO.	
1		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
2		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
3		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
4		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
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6		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
7		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	
8		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82		21-01-82	



SPARES

[illegible]