

CHAPTER 6
PROGRAMMING

The TK50 Tape Drive Subsystem runs with Digital's Tape Mass Storage Control Protocol (TMSCP).

LEGAL NOTICE: TMSCP and STI protocols and documentation are the proprietary information of Digital Equipment Corporation. UNIBUS/Q-BUS/BI-BUS port drivers and documentation for MSCP/TMSCP products are also proprietary information of Digital Equipment Corporation.

The standard base address for the subsystem is 774500 (octal). This is the location of the Initialization and Polling (IP) register. The Status and Address (SA) register is at location 774502 (octal).

Interrupt vectors are software settable. The standard interrupt vector for the first TK50 Tape Drive Subsystem is 260.

The addresses and interrupt vectors for TK50s on your system depend on your system configuration. If you have more than one TMSCP device (that is, TU81, TK50) on your system, you will need to use the appropriate software tools to determine the addresses and interrupt vectors for each device. In VMS systems, use the SYSGEN utility; its use is described below. In PDP-11 systems, the appropriate tool is the Float diagnostic, which is run under the XXDP+ diagnostic supervisor. Refer to the PDP-11 Architecture Handbook (EB-23657-18), Appendix A (Assignment of Bus Addresses and Vectors). Refer also to the XXDP+ Operating Manual.