

## NEW NUMBER = 0BKTB

ABSTRACT = Keys test.

This program checks the operation of each access key for each of the four Unibus cycles (or combinations of cycles) which may reference an address thru segmentation. These cycles are DATI, DATO (no DATIP), DATIP=DATO and DATIP, DATPB. Each of these cases is tested with and without memory management enable set, thus eight cases are tested for each key, SR0, SR2 and the corresponding PDR's, and the proper execution or prevention of execution of the instruction are checked in each case.

REQUIREMENTS =

PDP-11/40 with K711=D option

STORAGE = Program requires 4K of memory, starting at location 0.

LOADING = Absolute Loader.

EXECUTION TIME = Each pass takes approximately 10 seconds with core memory.

STARTING PROCEDURE = Load address 200

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 of UP ... HALT ON ERROR  
 SW14 = 1 of UP ... SCOPE LOOP  
 SW13 = 1 of UP ... INHIBIT PRINTOUT  
 SW11 = 1 of UP ... INHIBIT ITERATIONS  
 SW10 = 1 of UP ... HALT AT END OF CURRENT TEST WITH NEXT  
 TEST NUMBER IN DATA LIGHTS.