

MAINDEC=11

NEW NUMBER = DCKBF to DCKB0

ABSTRACT =

This is the last 10 of 15 tests that incrementally test and isolate simple malfunctions in the PDP-11 option. The tests should be run in the indicated alphabetic sequence. There are additional tests for more complex malfunctions. All tests are executed in kernel mode only except for test DCKB0. All these tests are needed for the 11/45. For the 11/40 stack limits, if you have a KJ option. For ASH,ASHC,MUL,DIV, if you have the KE11F option.

REQUIREMENTS =

PDP=11/45, 11/40

STORAGE = The programs use all of a 4KW memory with the exception of 17500 to 17776 (which is reserved for the boot and absolute loader).

LOADING = Absolute loader.

EXECUTION TIME = All tests take approximately 1 minute each on an 11 with core memory

STARTING PROCEDURE = Load address 200. Press start. The program will loop, and ring bell on completion. Pass count may be monitored in the display register (11/45 only), and is stored in address 1000.

PRINTOUTS = No

SWITCH REGISTER OPTIONS = Yes

SW(28) = 1 Load PDP-11/45 micro break register with value in SW(20-27). (at start of test only)

SW(29) = 1 Use alternate register set (R10-R15) test DCKB1=DCKBL only. (PDP=11/45 only)