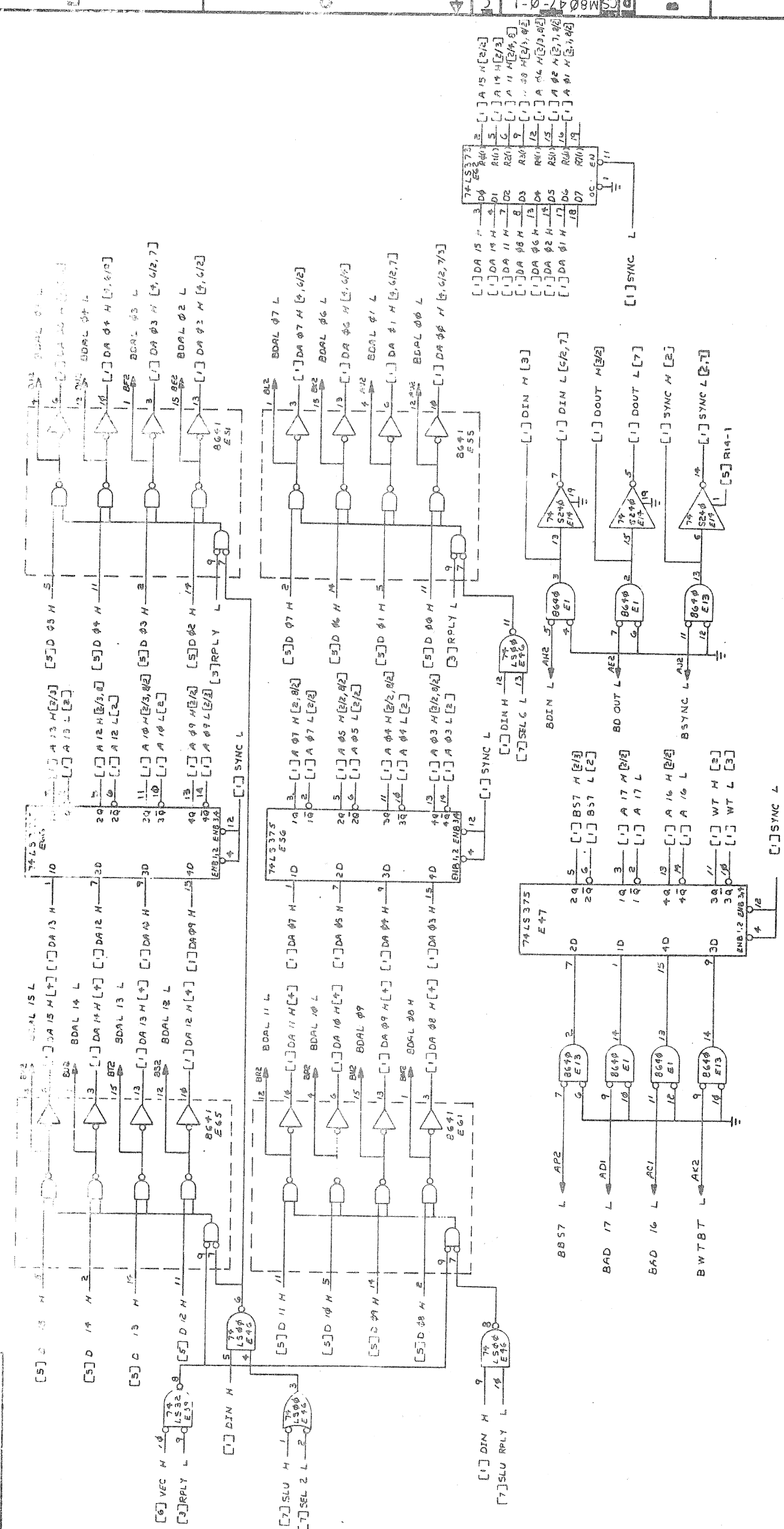
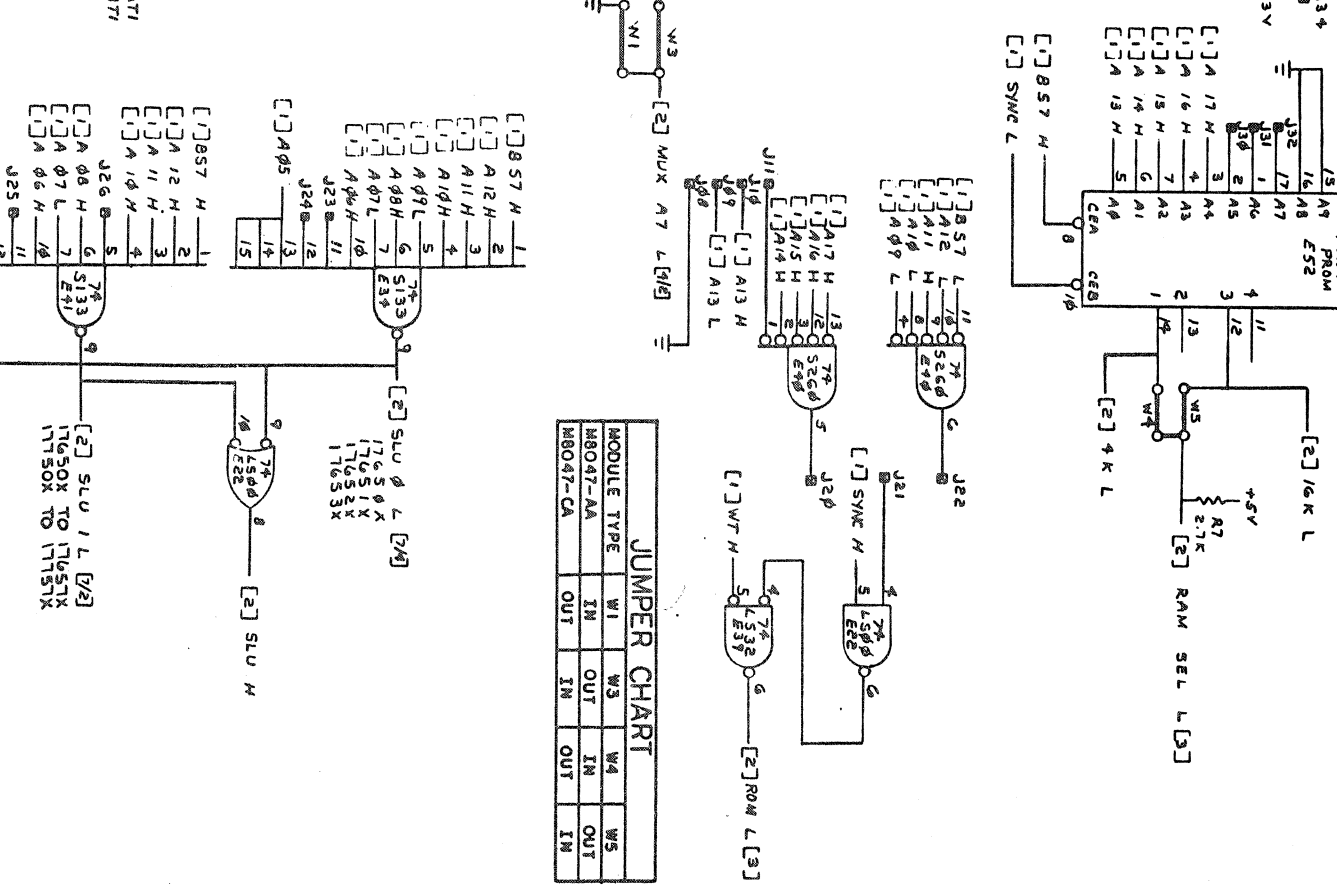
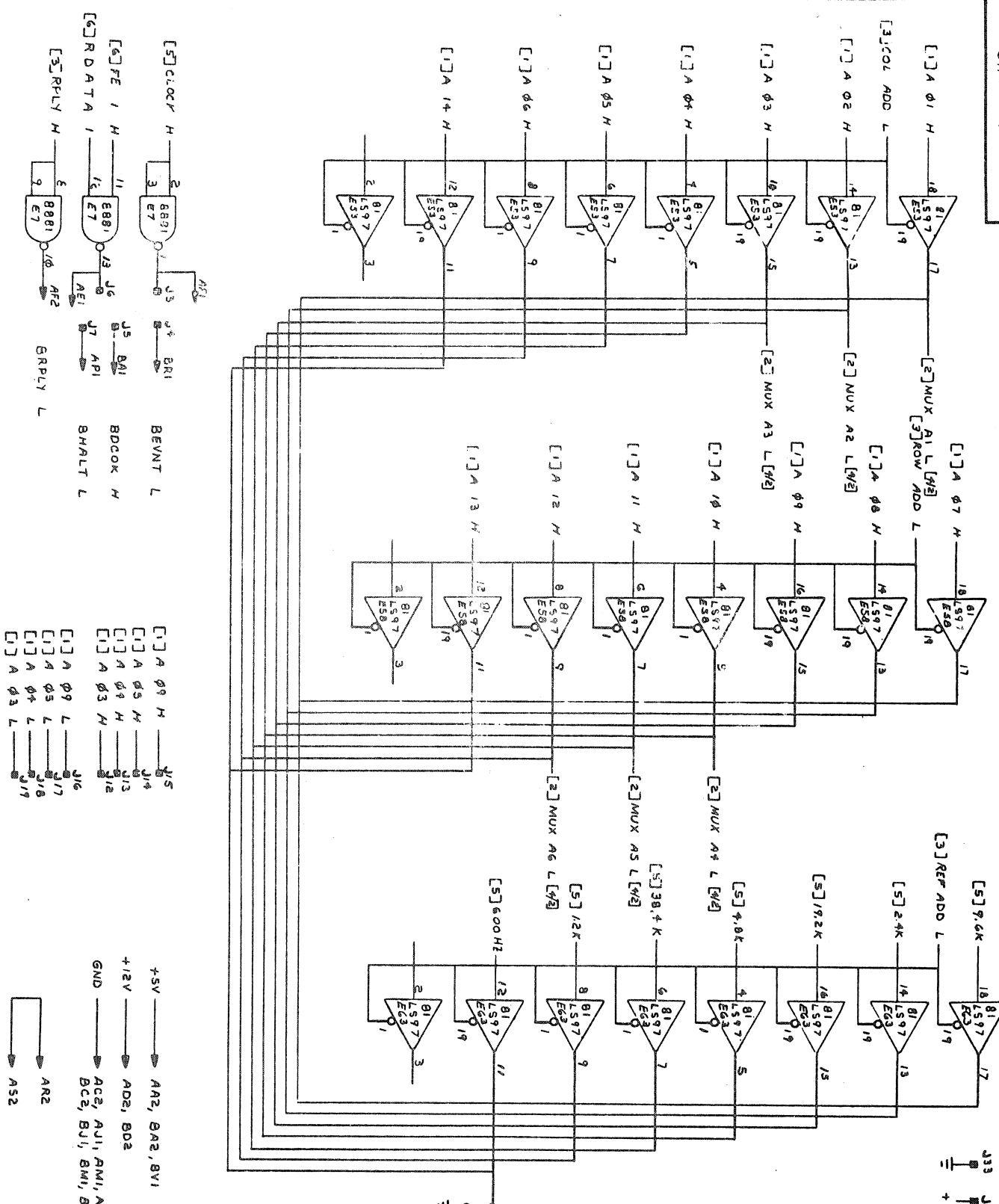


REVISIONS
REV. NO. 1
DATE 10/1/77
BY J. L. HARRIS
DESCRIPTION
LSI-11 MEM AND I/O



DATE	10/1/77	DESIGNED BY	J. L. HARRIS	FILE NO.	LSI-11 MEM AND I/O
REV. NO.	1	CHECKED BY	J. L. HARRIS	DATE	10/1/77
DESCRIPTION	LSI-11 MEM AND I/O				
SCALE	1:1				
SHEET	1	OF	8	DATE	10/1/77
REVISIONS					
REV. NO.	1	DATE	10/1/77	BY	J. L. HARRIS
DESCRIPTION					
LSI-11 MEM AND I/O					

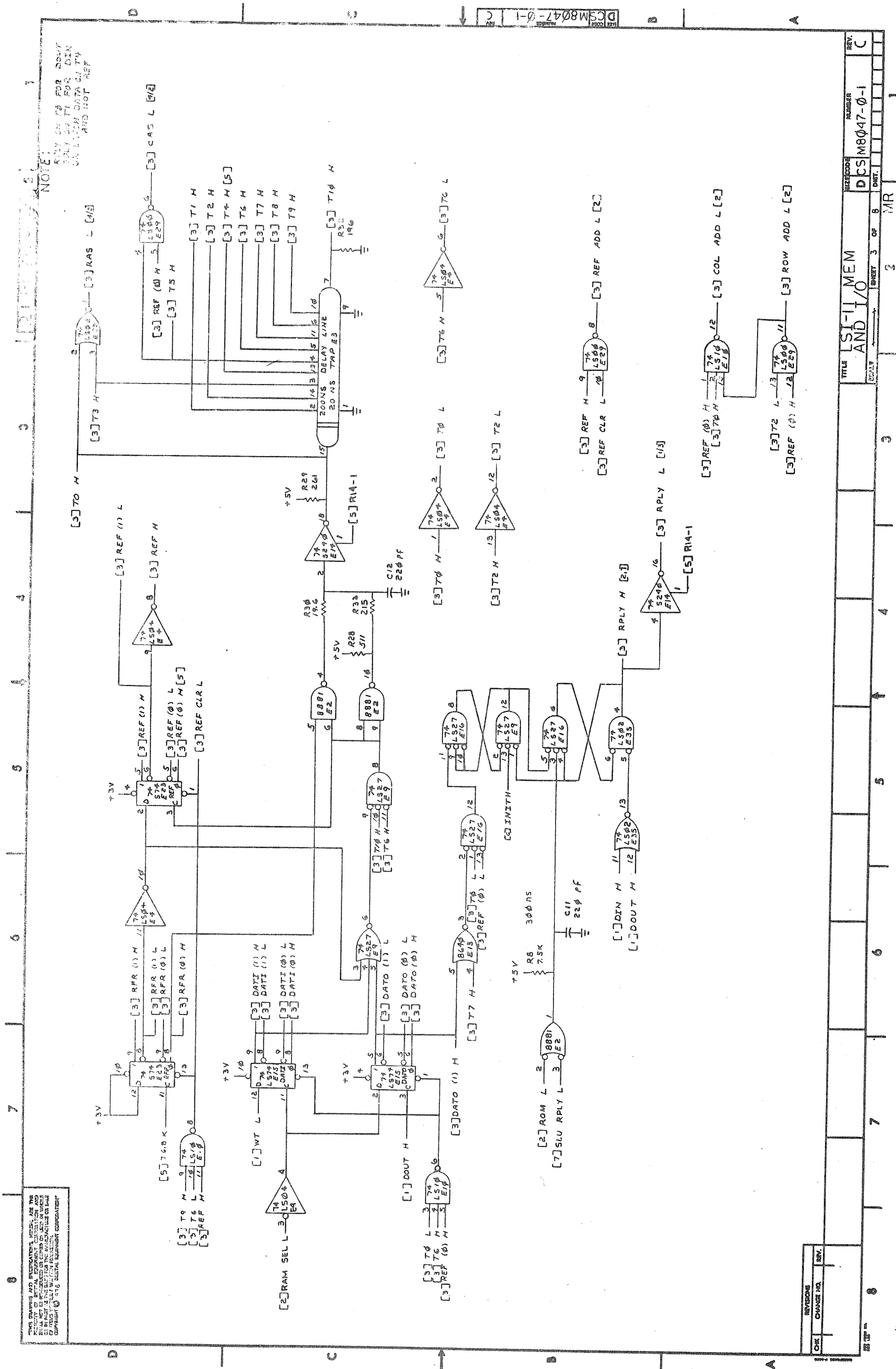
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JUMPER CHART				
MODULE TYPE	W1	W3	W4	W5
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M8047-CA	OUT	IN	OUT	IN

LSI-11 MEM AND I/O

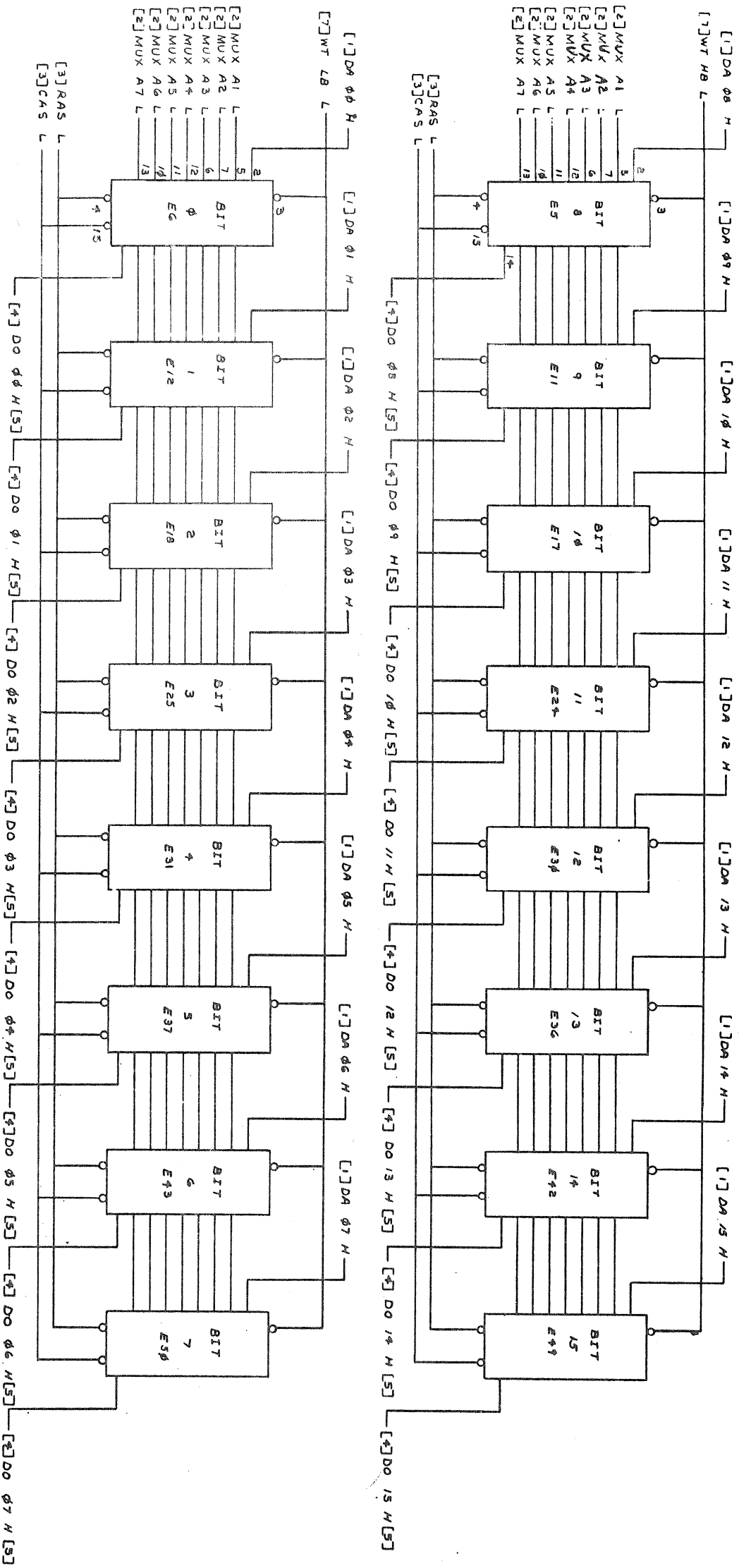
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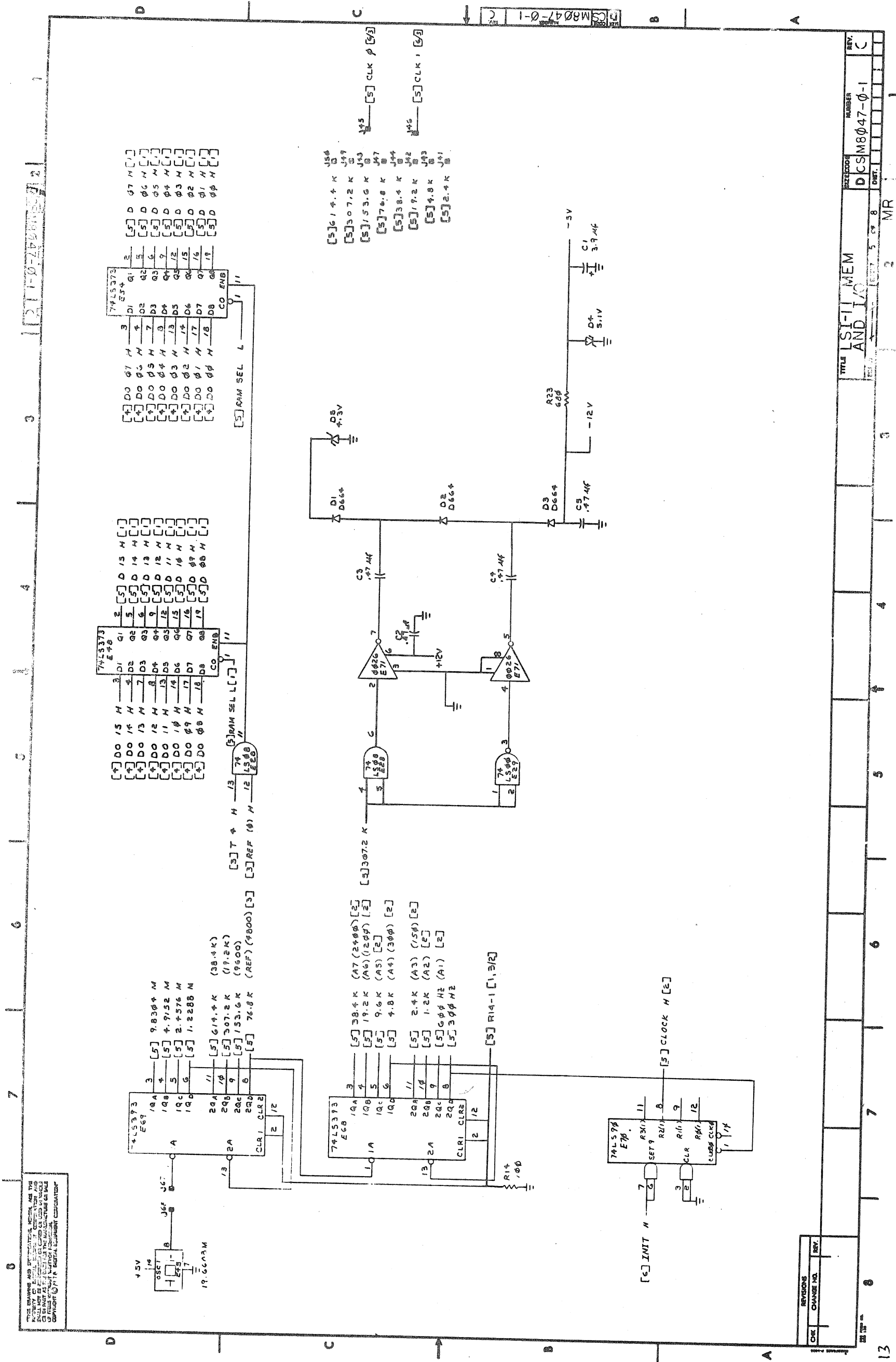
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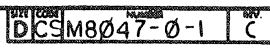


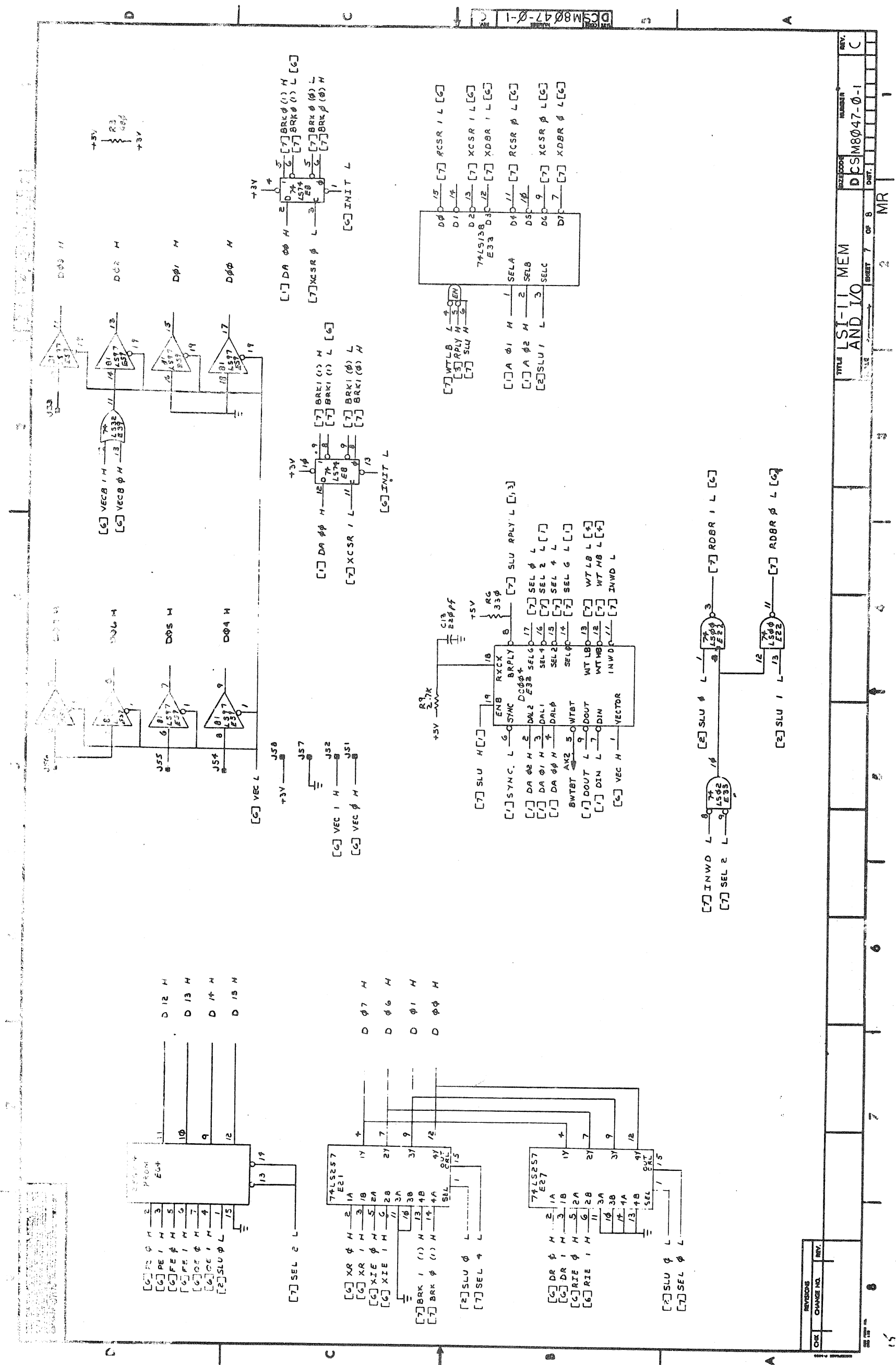
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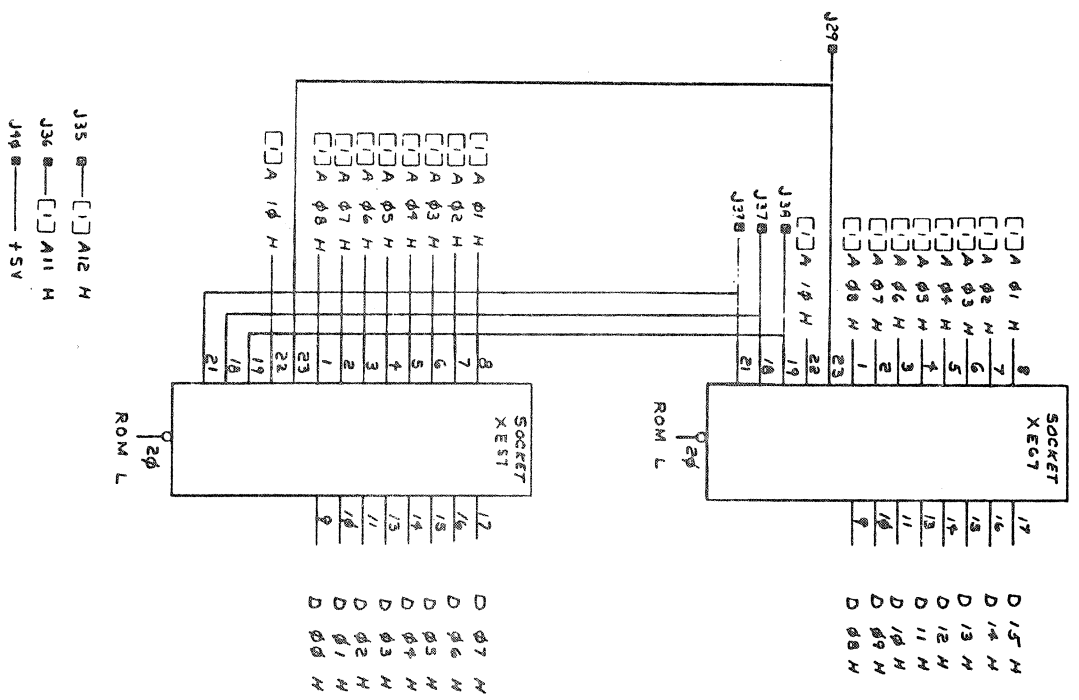
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TITLE	
LSI-11 MEM	
AND I/O	
SCALE	SHEET 8 OF 8
MR	2
PART	
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REV.	
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2 1-0-4508W S30 2