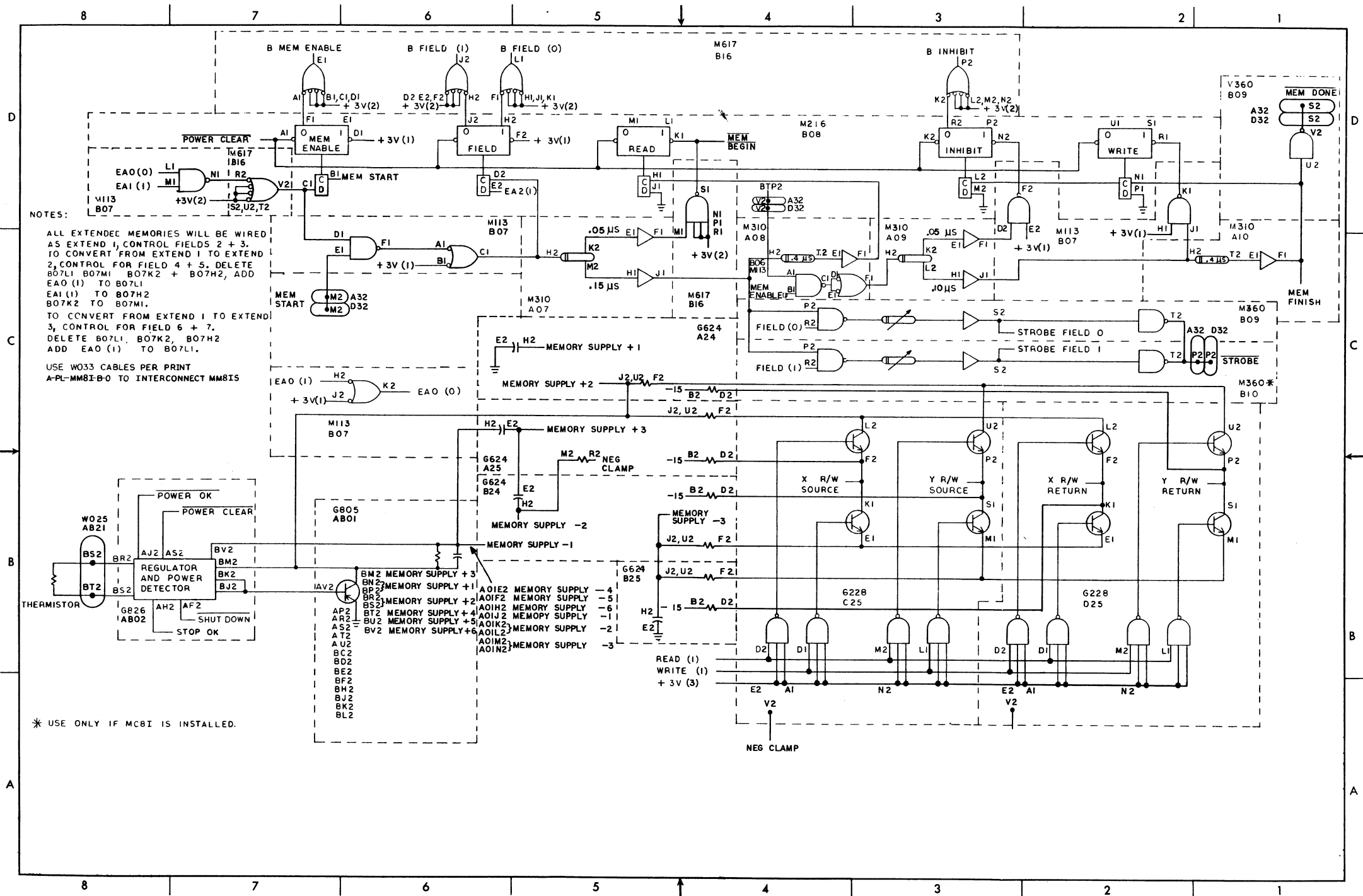
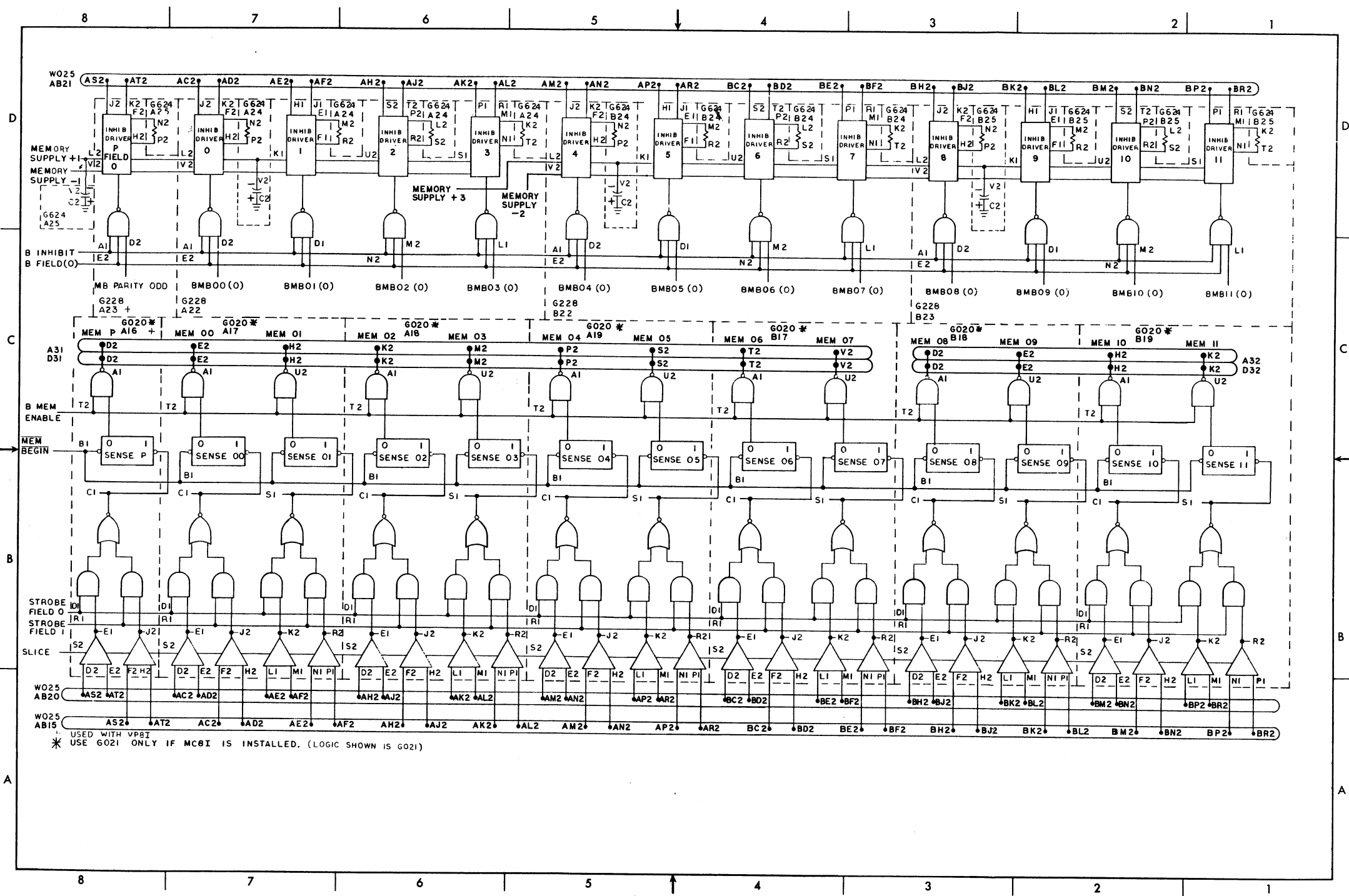


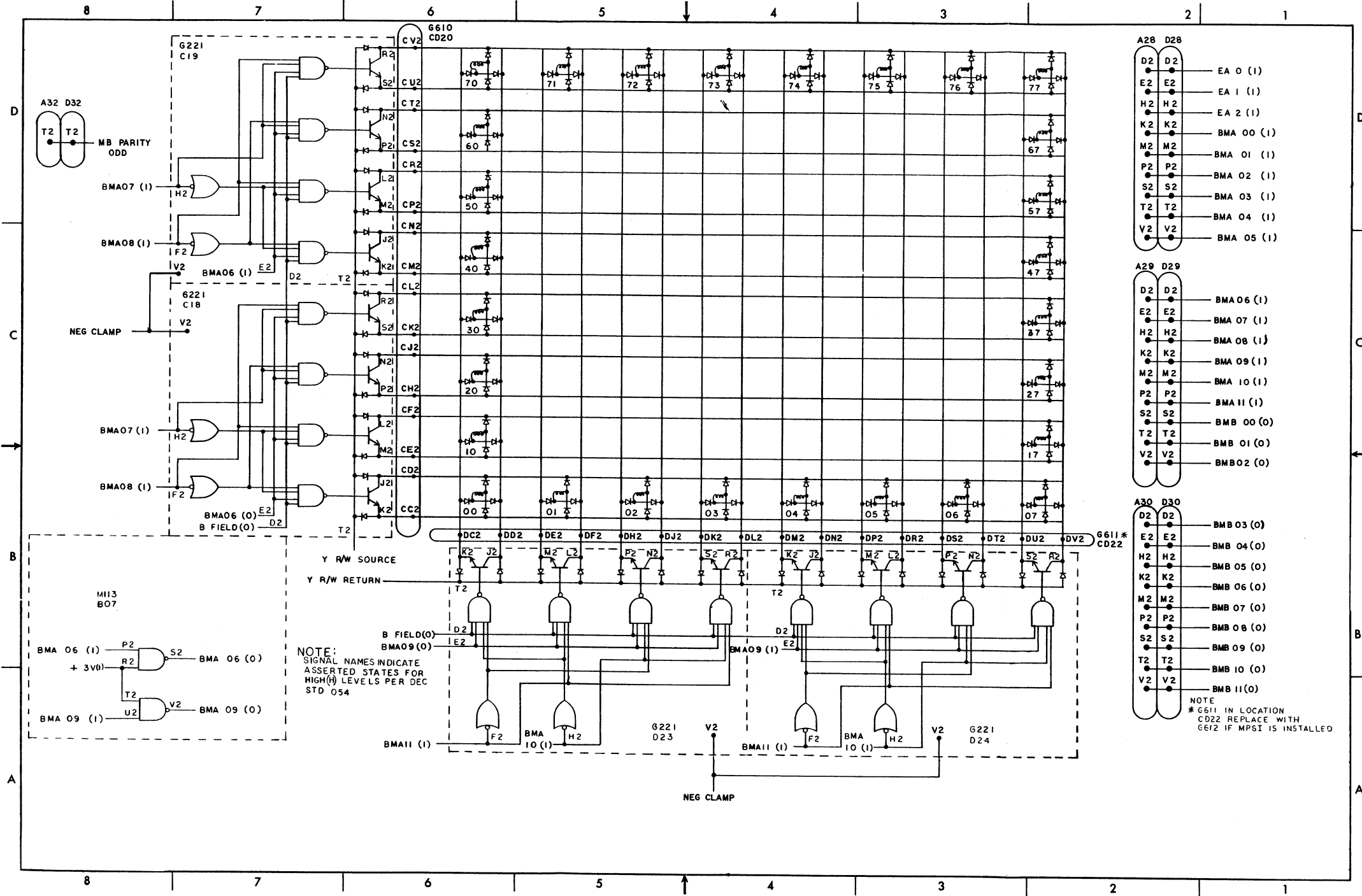
D-BS-MC8I-0-4 Y Axis Selection



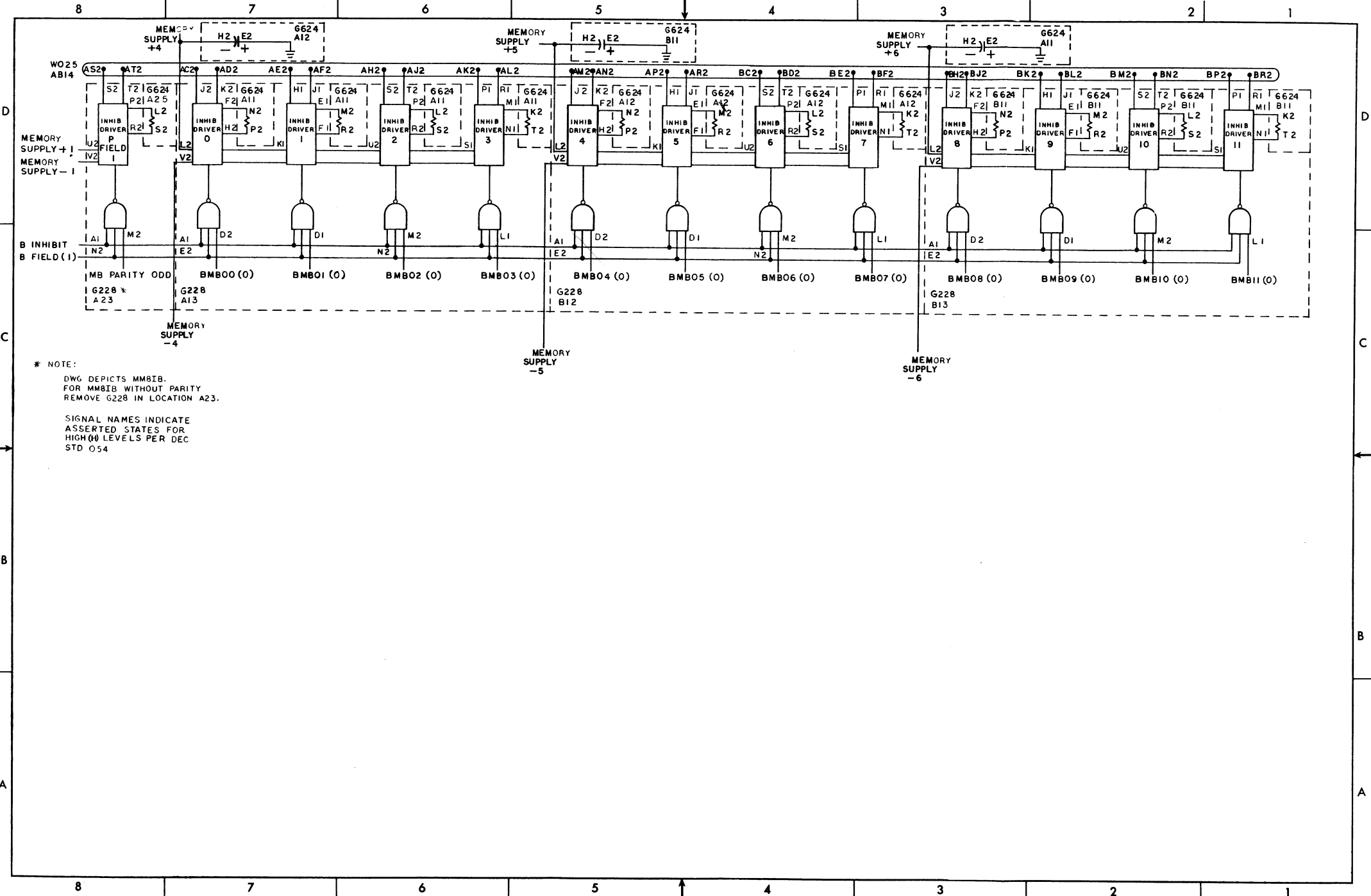






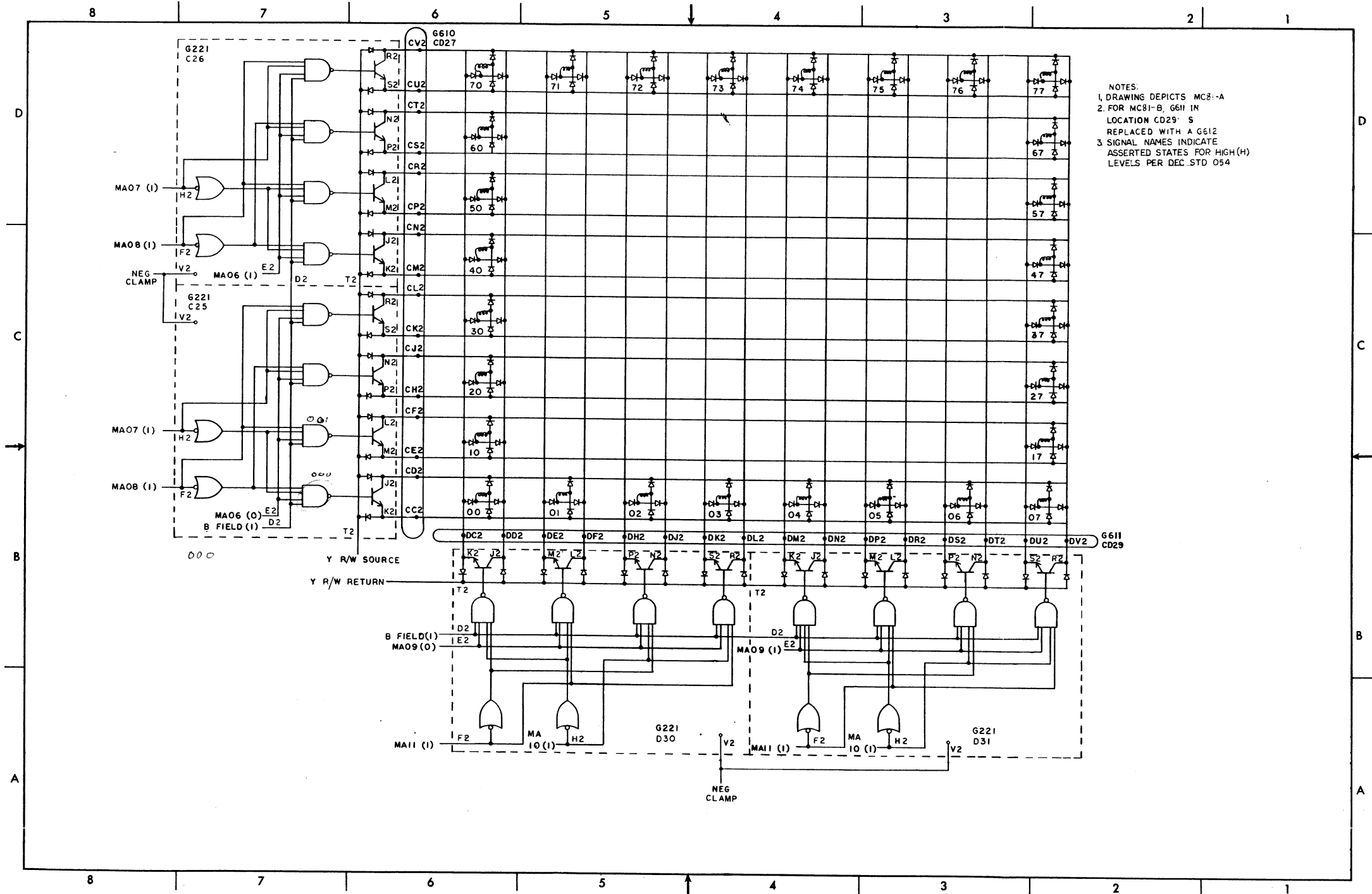


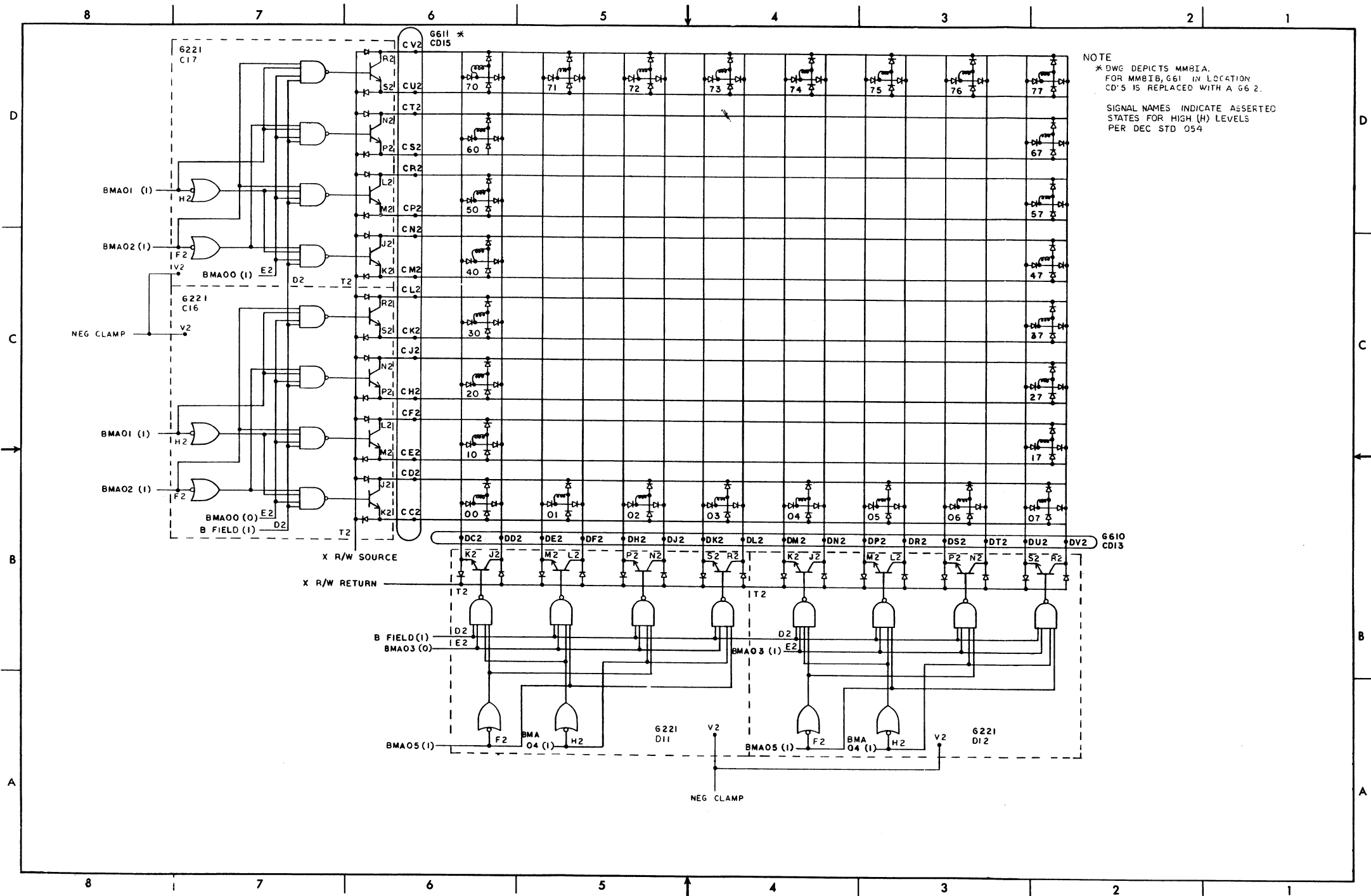
D-B5-MM81-A-4 Y Axis Selection, Field 0



\* NOTE:  
 DWG DEPICTS MM8IB.  
 FOR MM8IB WITHOUT PARITY  
 REMOVE G228 IN LOCATION A23.  
 SIGNAL NAMES INDICATE  
 ASSERTED STATES FOR  
 HIGH(0) LEVELS PER DEC  
 STD 054

D-BS-MM8I-B-1 Inhibit Drivers, Field 1





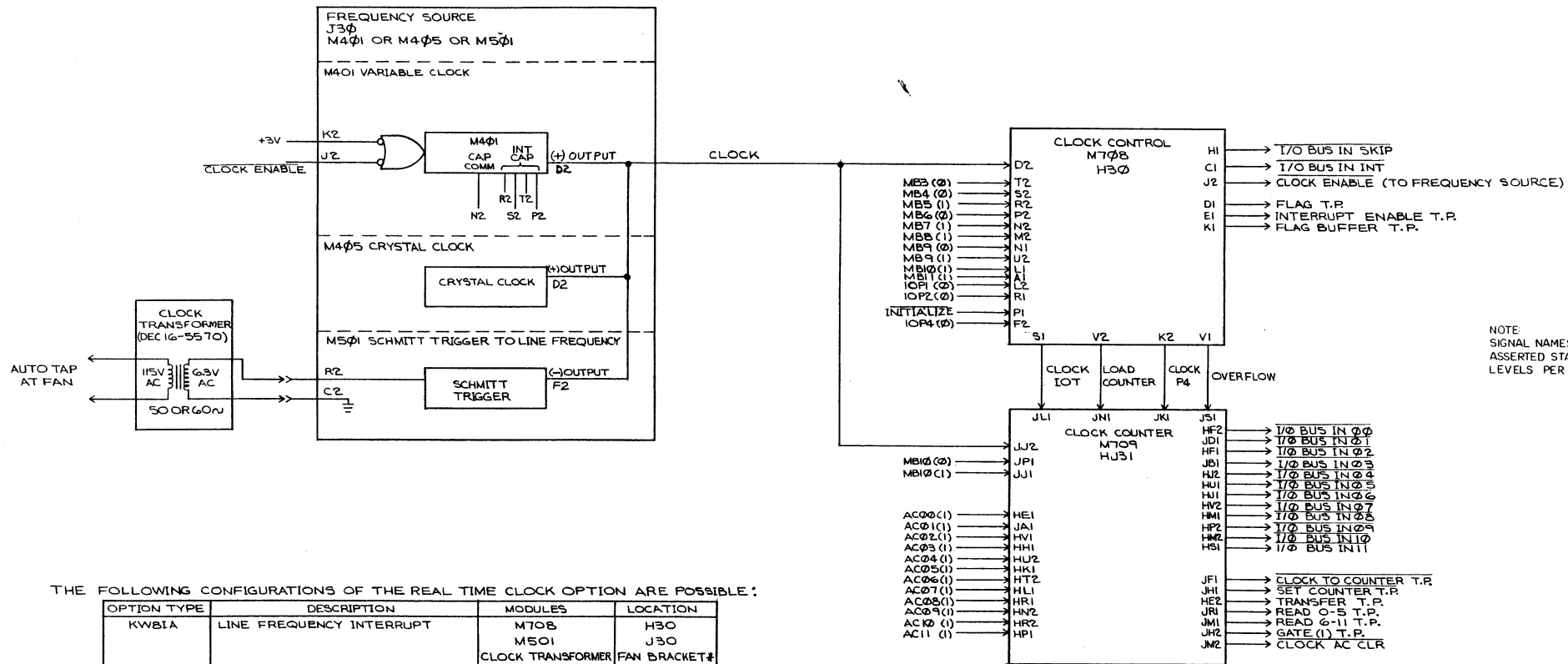








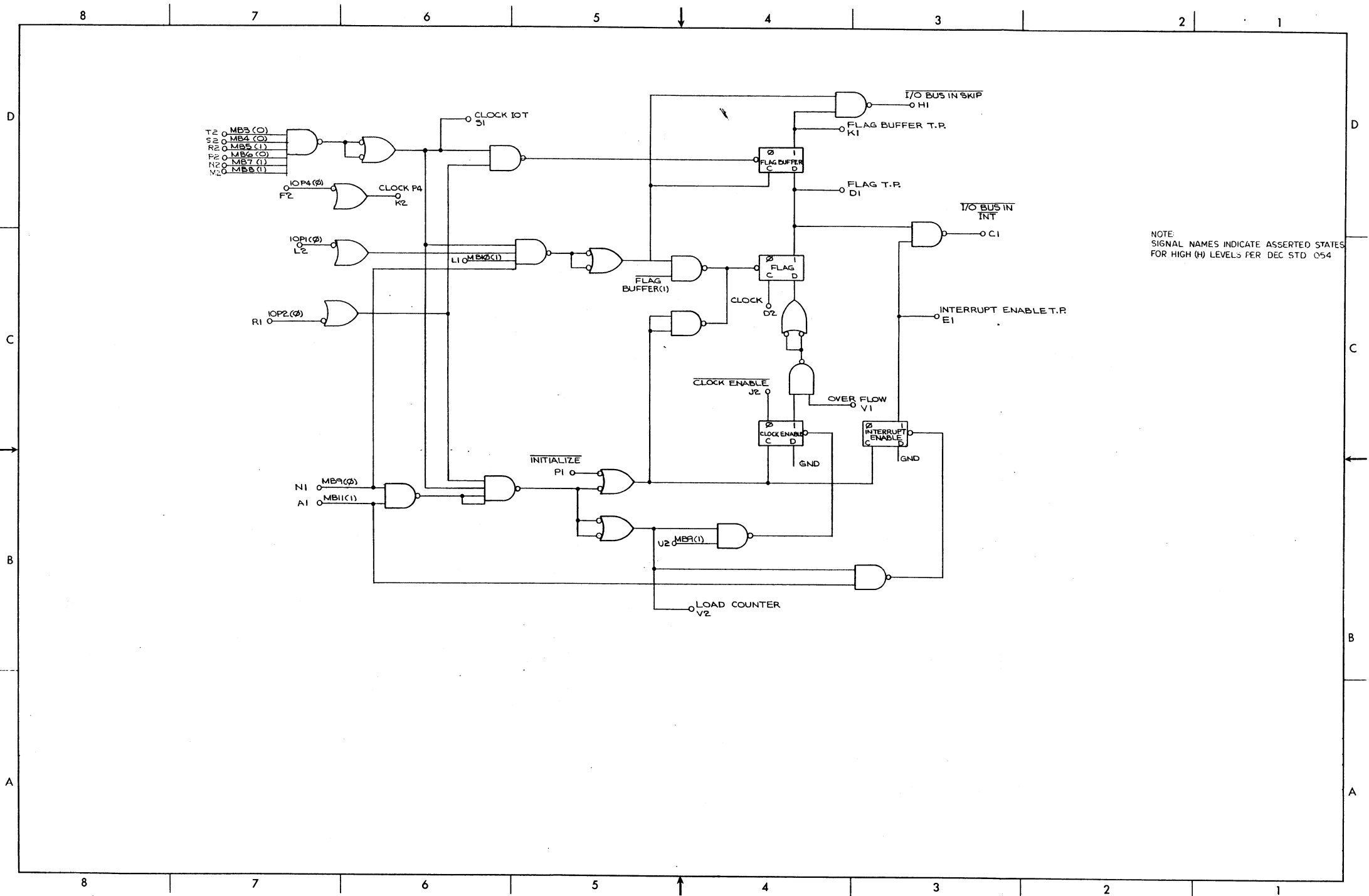




THE FOLLOWING CONFIGURATIONS OF THE REAL TIME CLOCK OPTION ARE POSSIBLE:

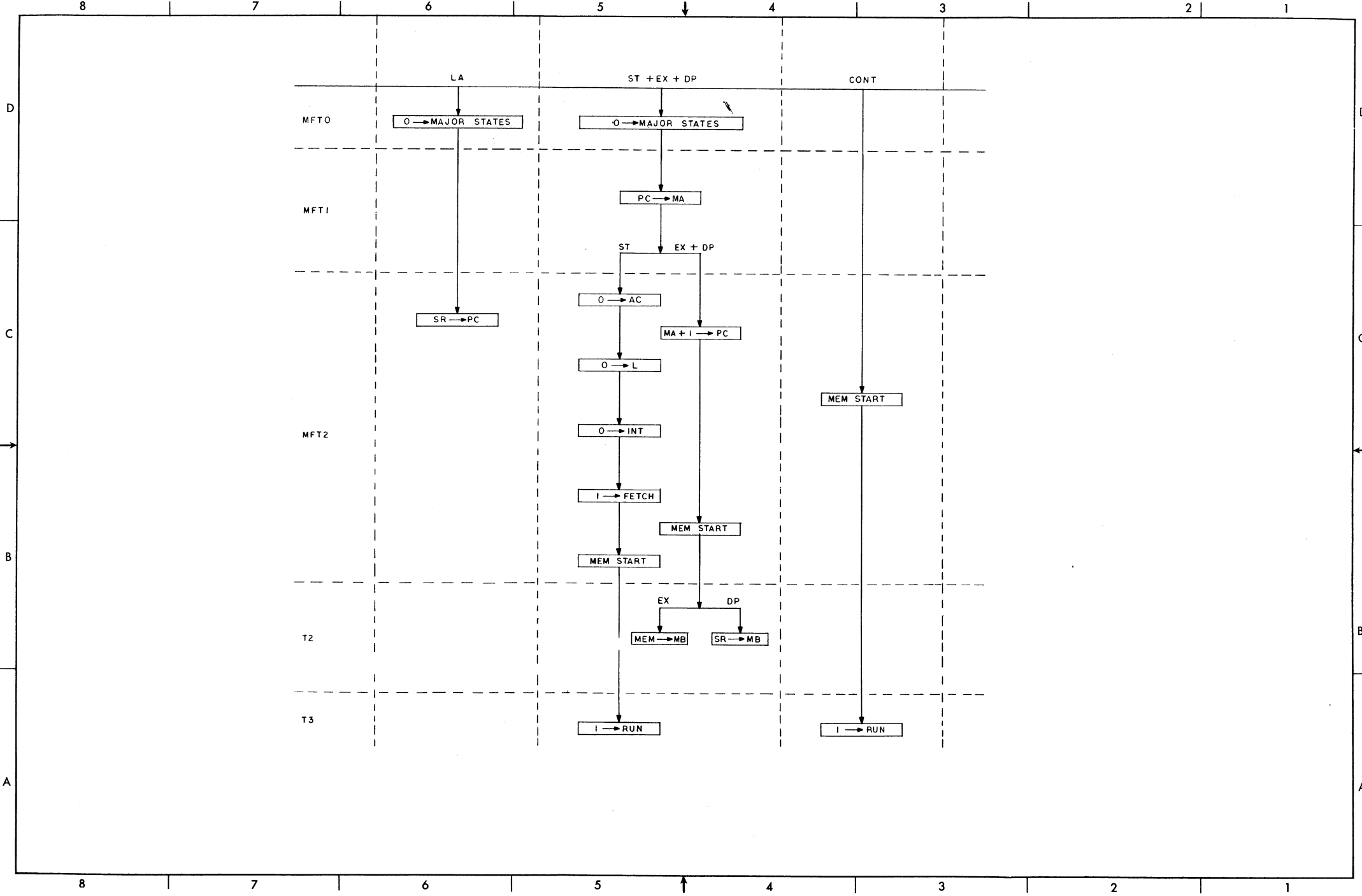
OPTION TYPE	DESCRIPTION	MODULES	LOCATION
KWBIA	LINE FREQUENCY INTERRUPT	M708 M501 CLOCK TRANSFORMER	H30 J30 FAN BRACKET*
KWBIB	VARIABLE CLOCK INTERRUPT	M708 M401	H30 J30
KWBIC	CRYSTAL CLOCK INTERRUPT	M708 M405	H30 J30
KWBID	KWBIA WITH PRESET AND READOUT	M708 M709 M501 CLOCK TRANSFORMER	H30 HJ31 J30 FAN BRACKET*
KWBI E	KWBIB WITH PRESET AND READOUT	M708 M709 M401	H30 HJ31 J30
KWBIF	KWBIC WITH PRESET AND READOUT	M708 M709 M405	H30 HJ31 J30

\* MTG & WIRING INFORMATION ON PRINT \* D-UA-KWBI-0-0

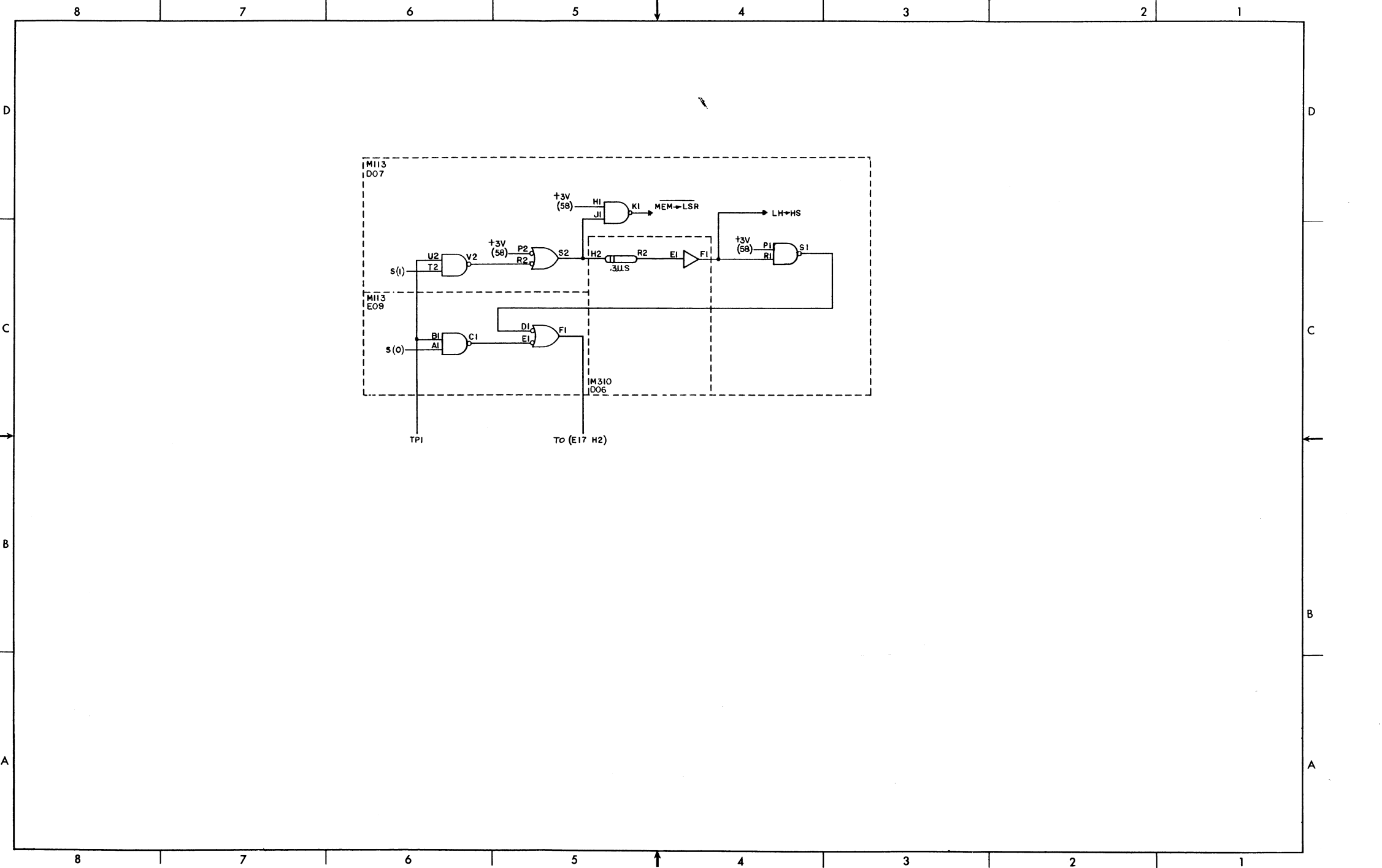








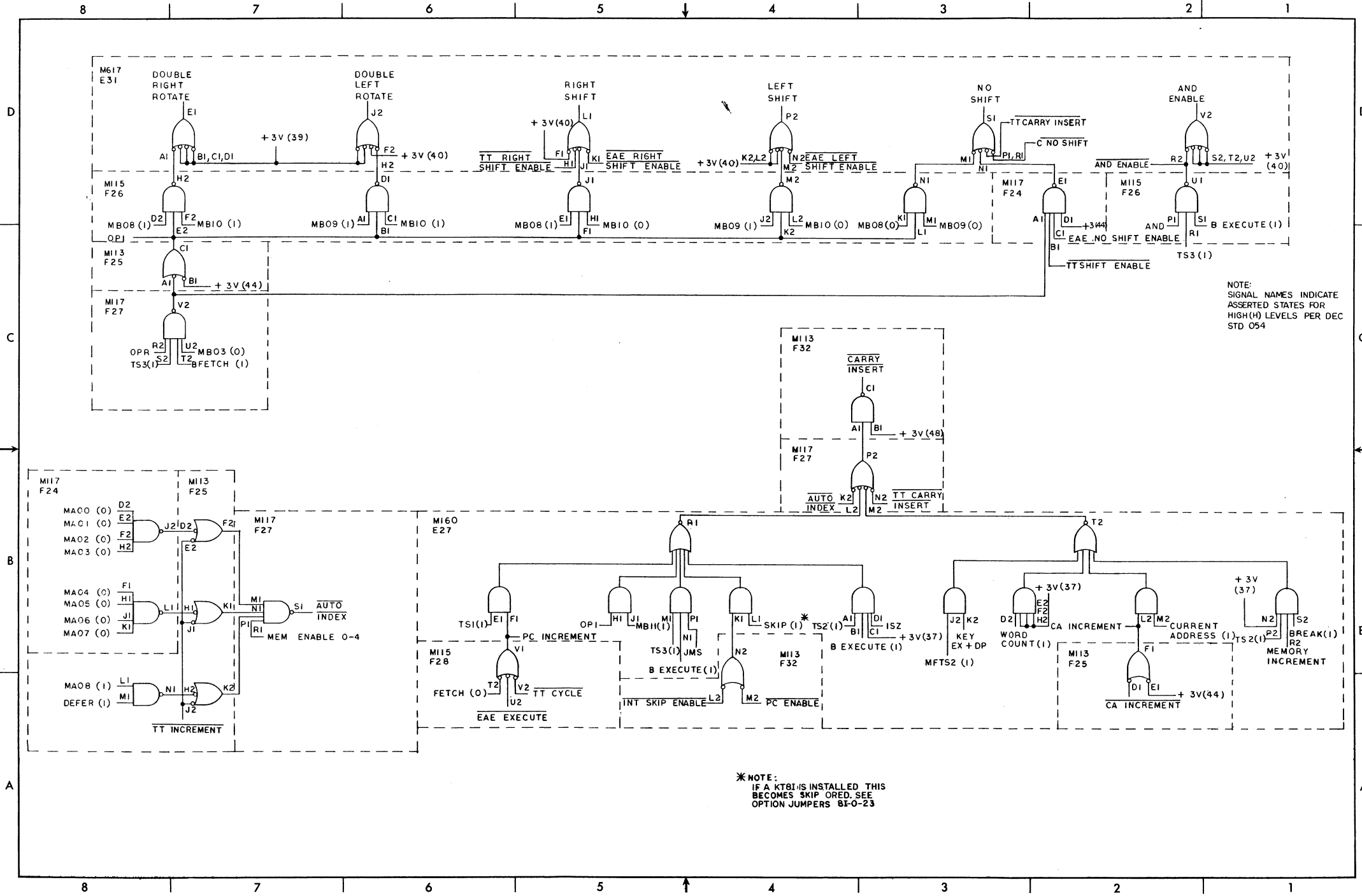






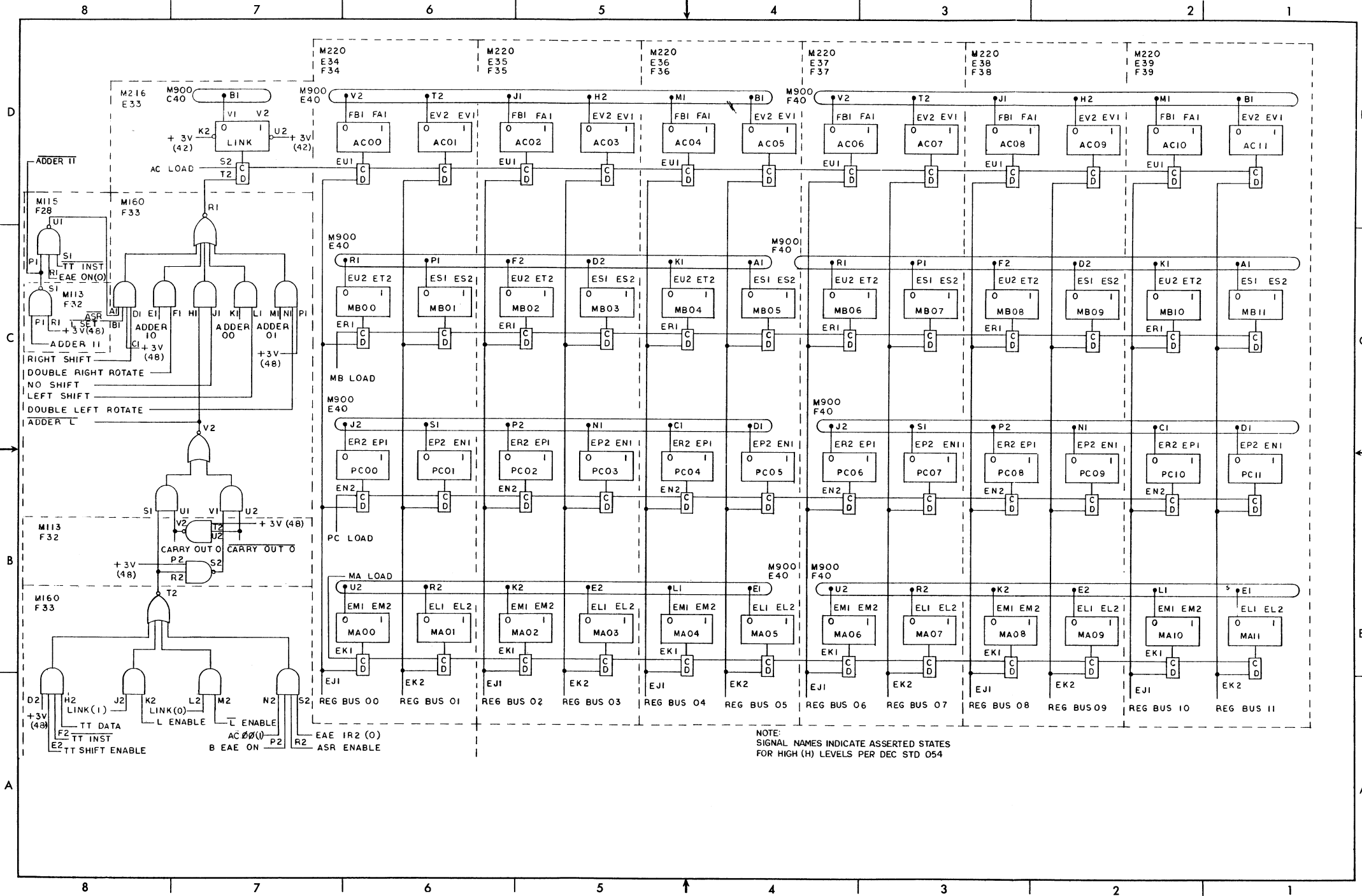






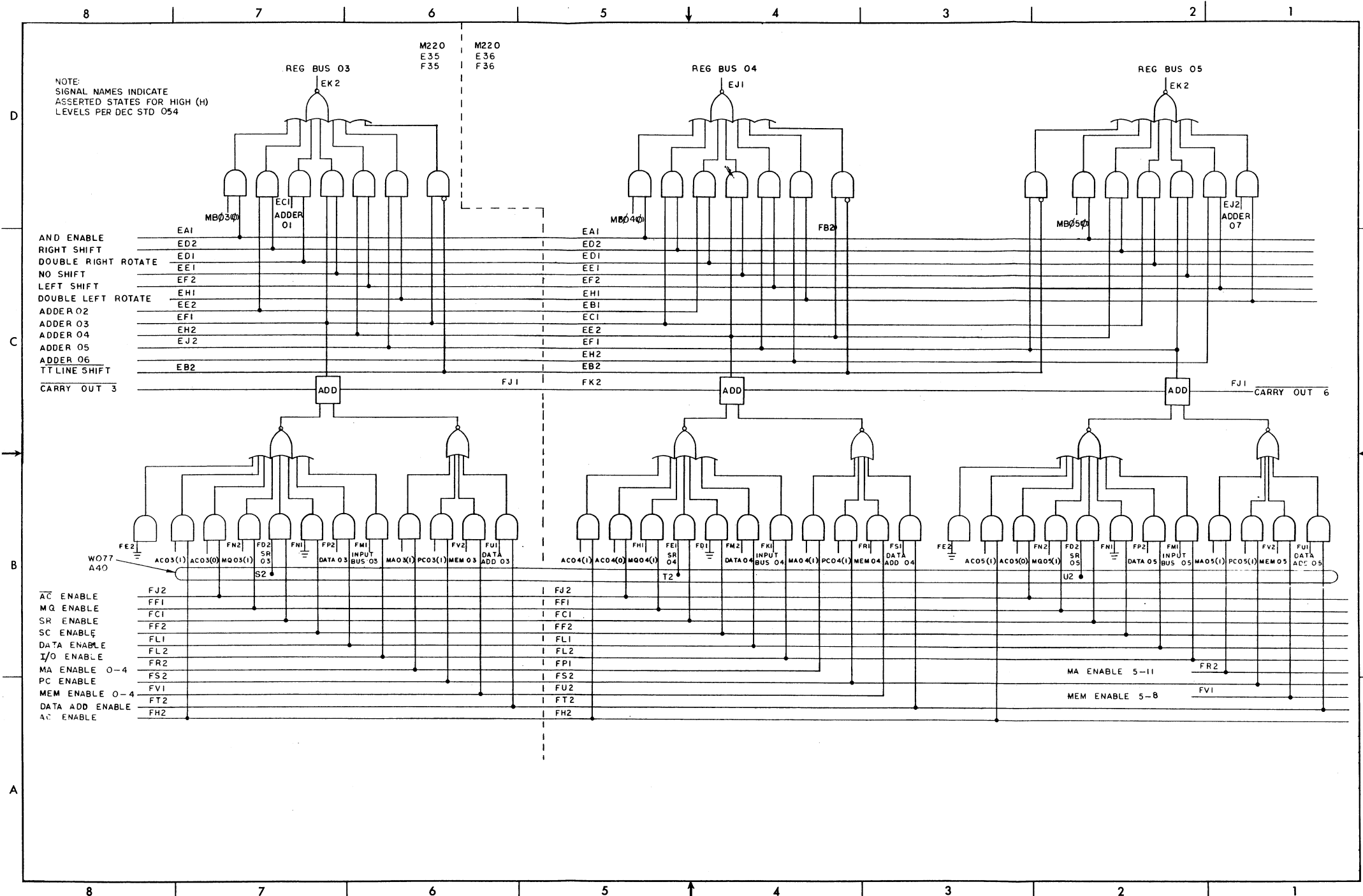






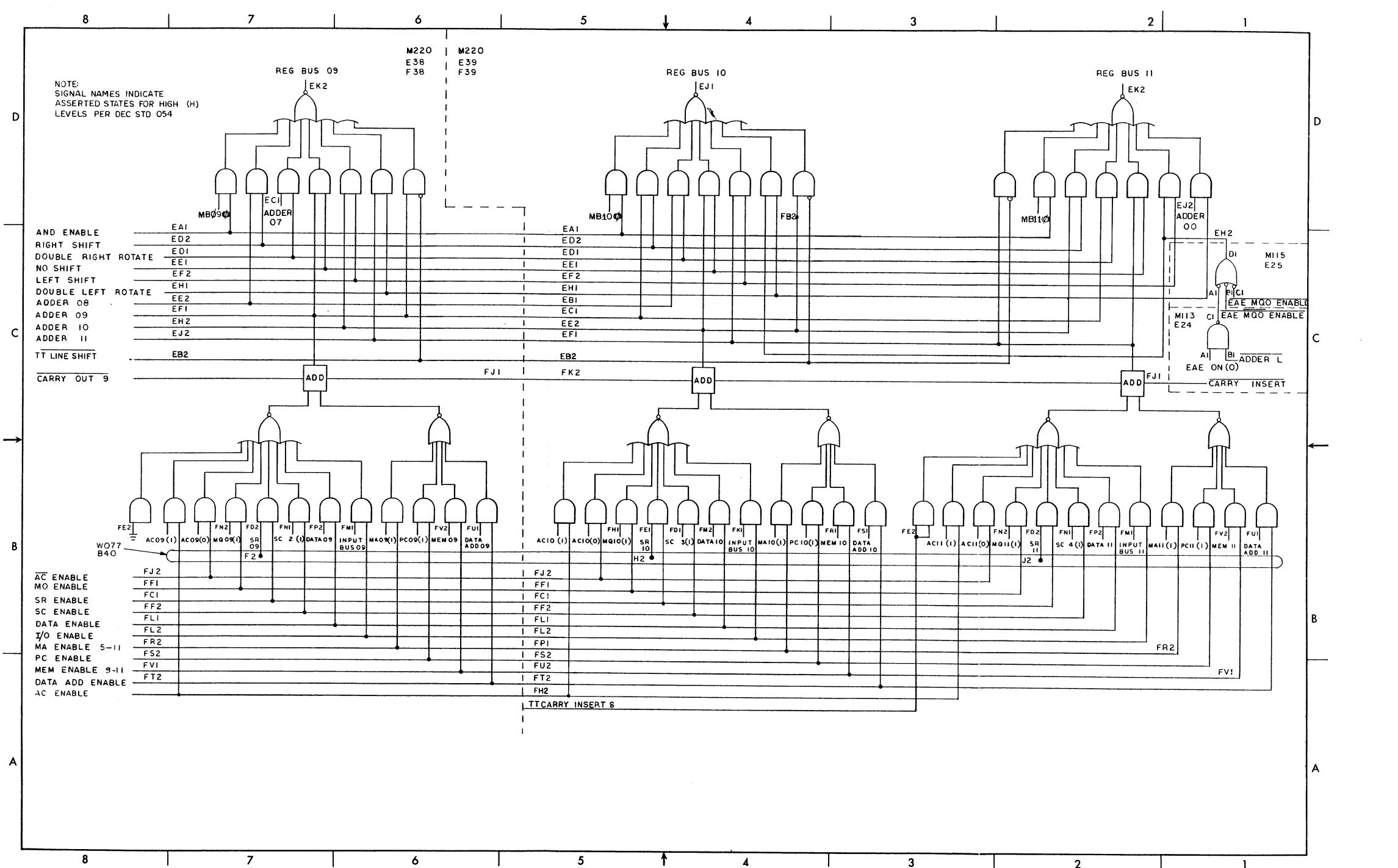
D-BS-8I-0-8 Major Registers

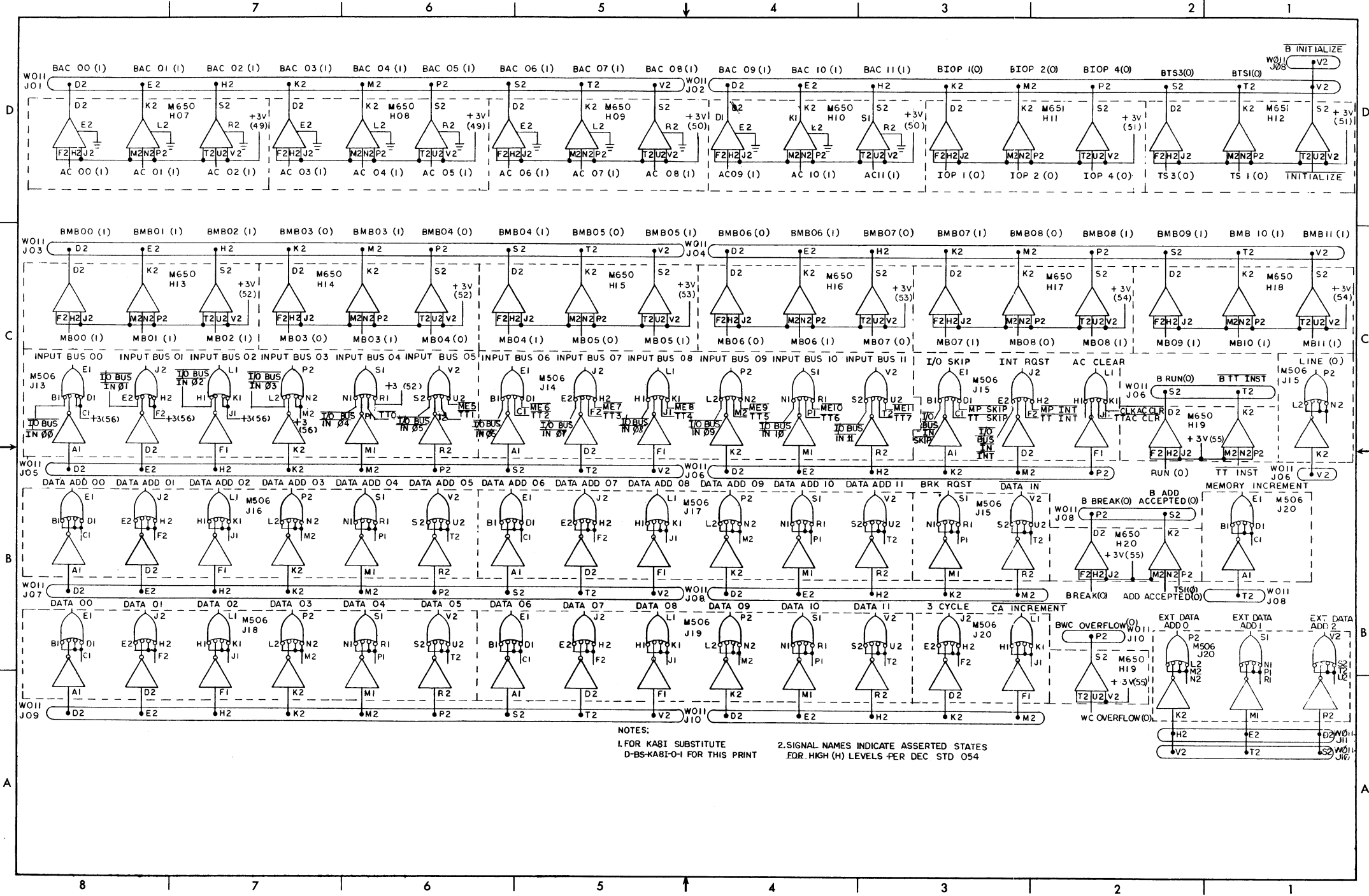




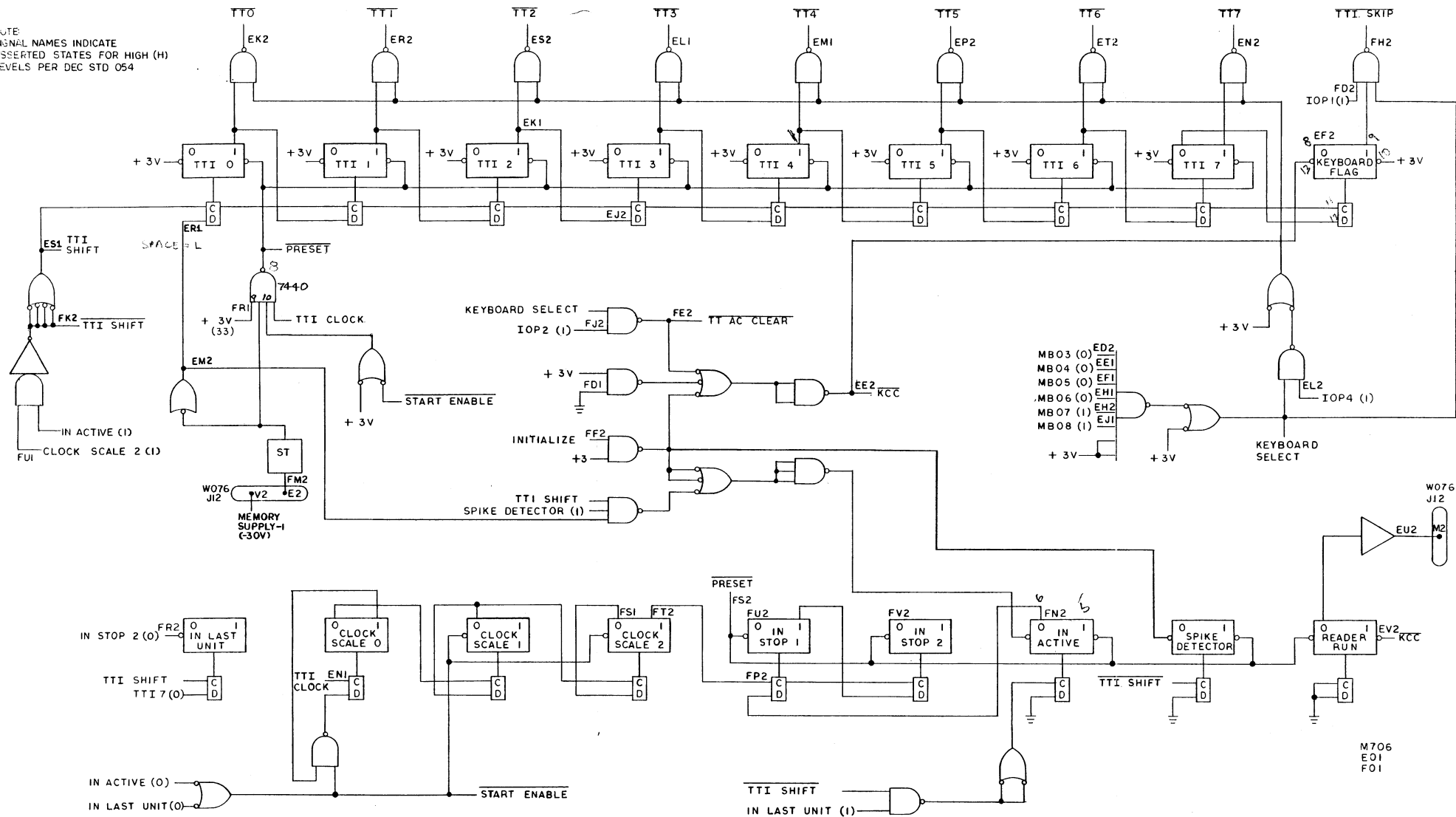




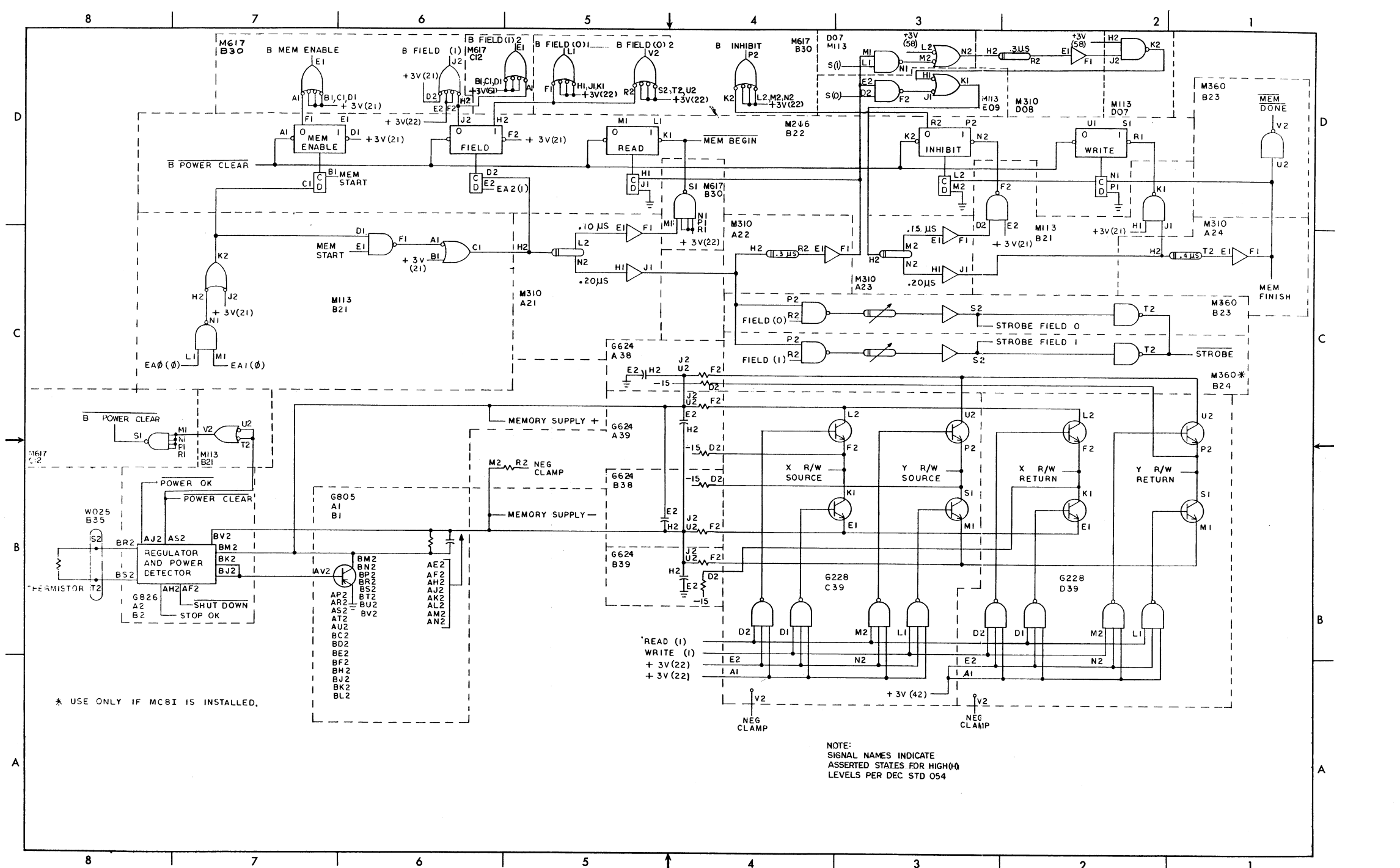


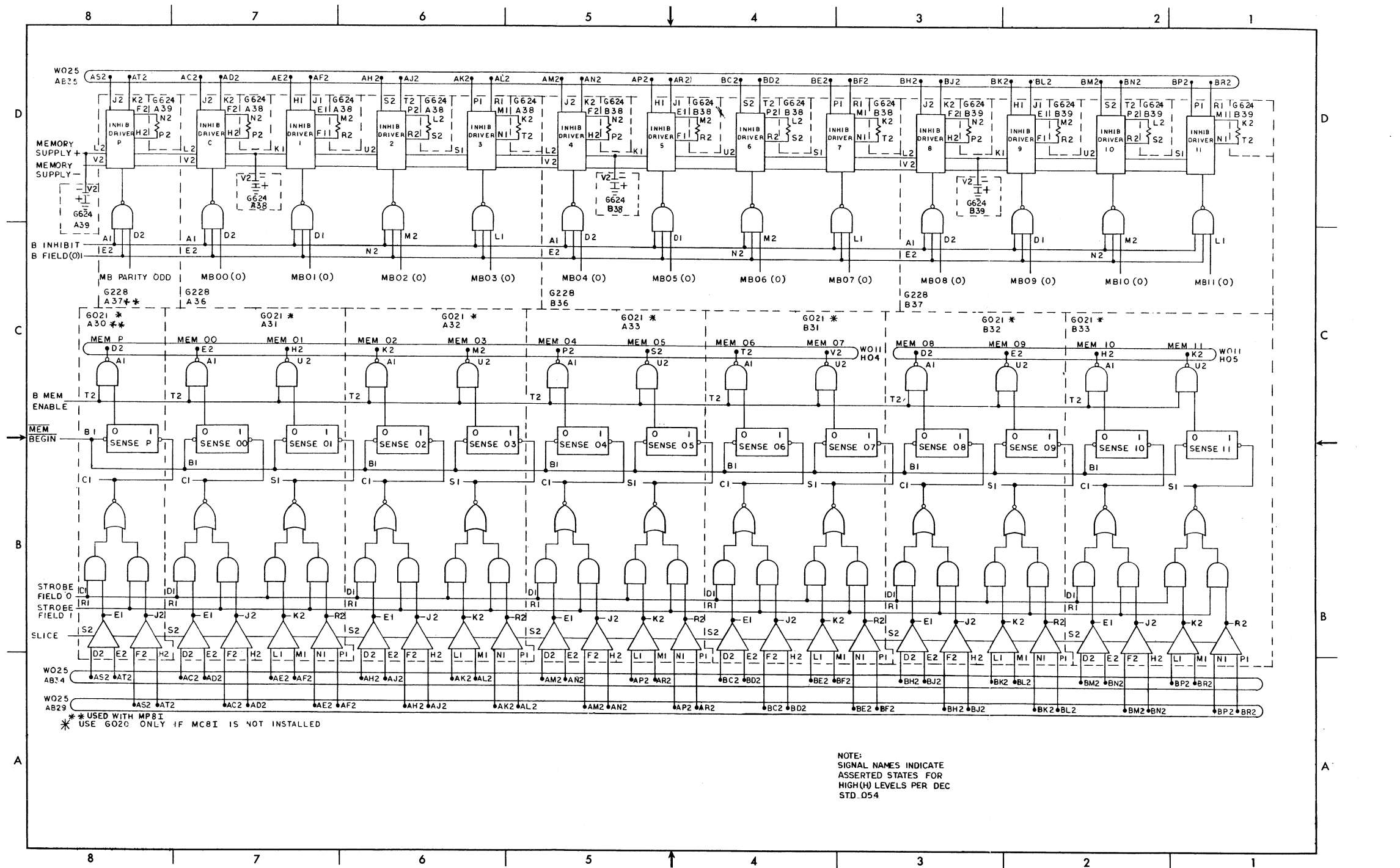


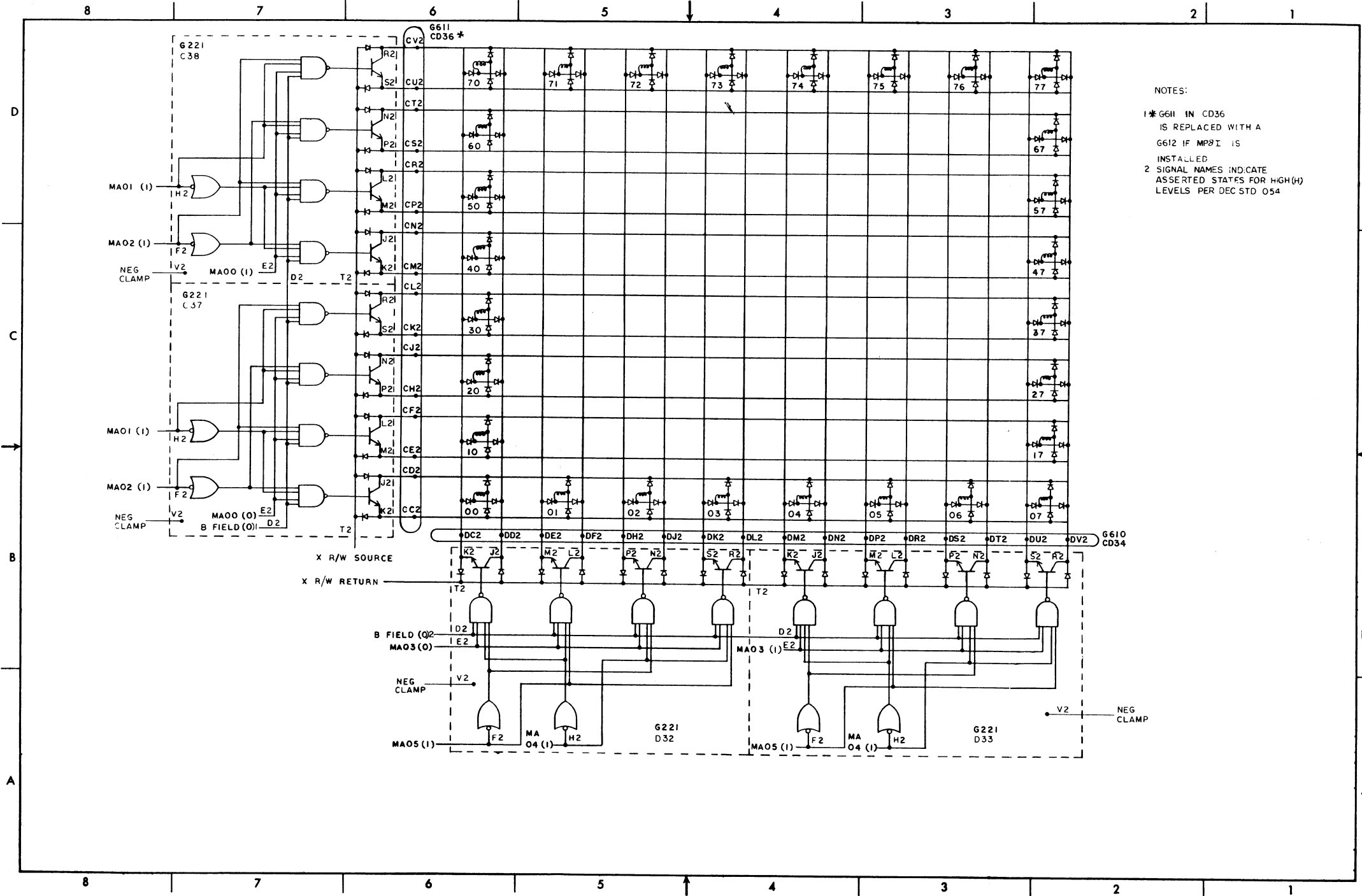
NOTES:  
 1. FOR KABI SUBSTITUTE  
 D-B5-KABI-0-1 FOR THIS PRINT  
 2. SIGNAL NAMES INDICATE ASSERTED STATES  
 FOR HIGH (H) LEVELS PER DEC STD 054



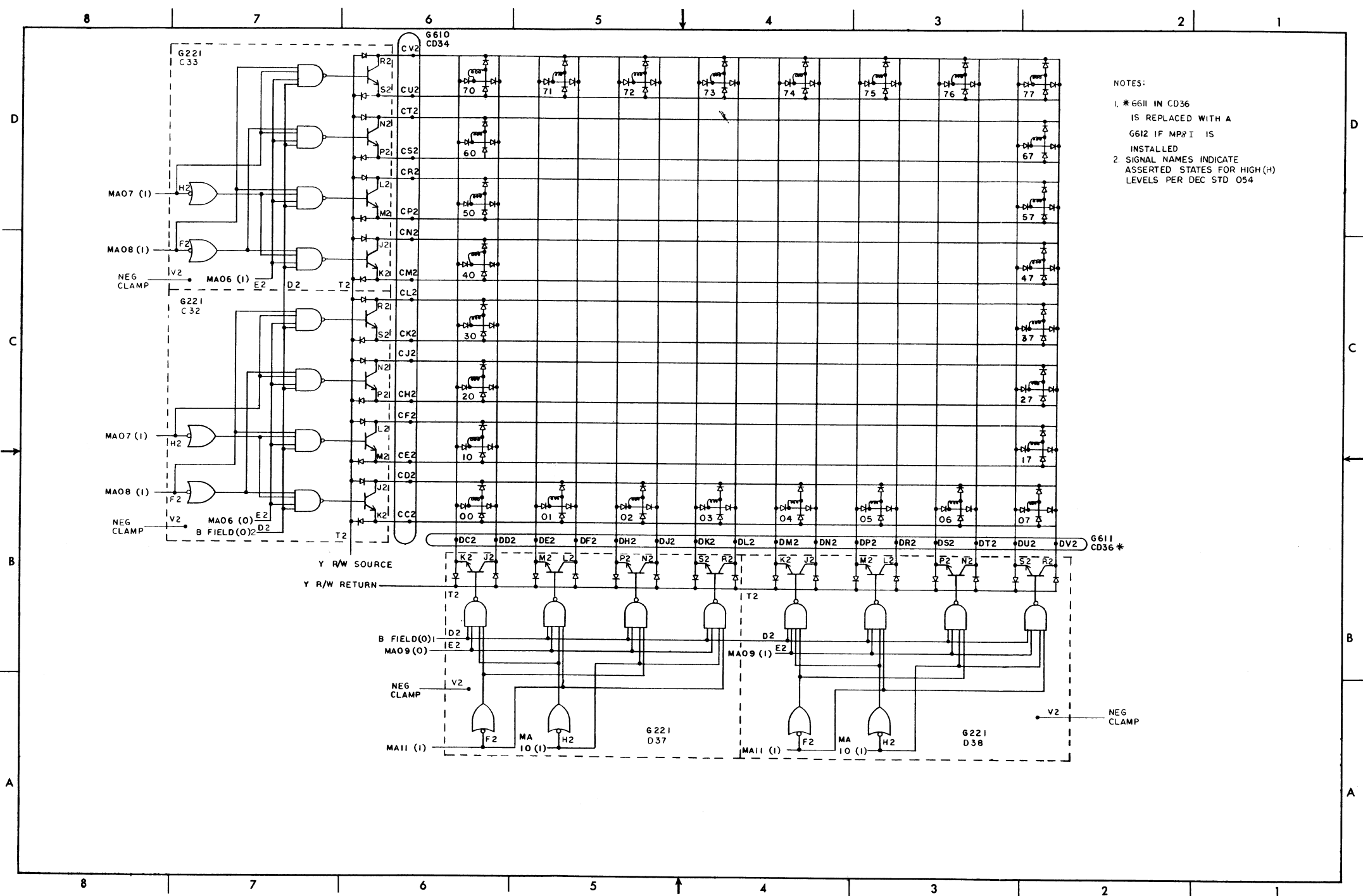




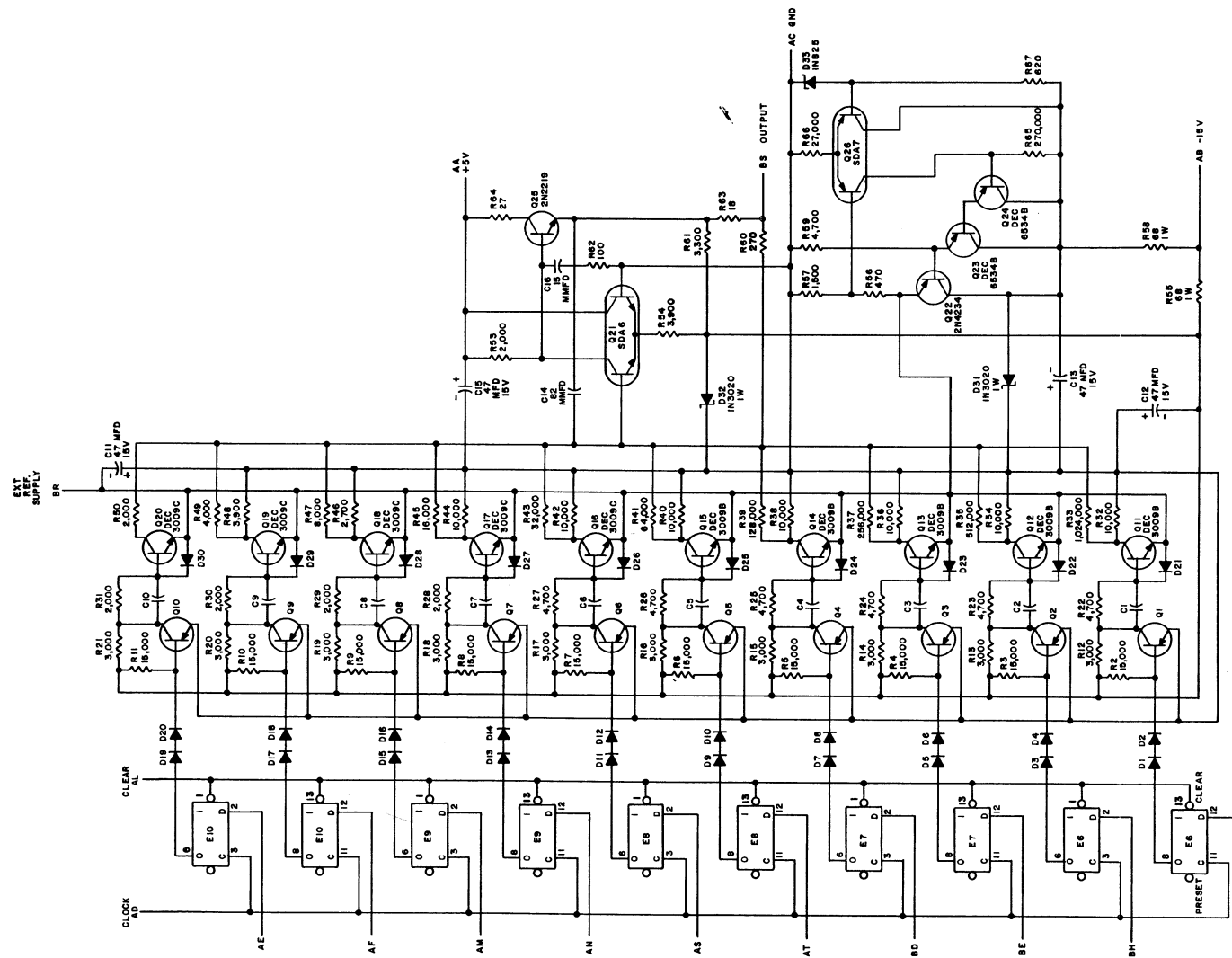




- NOTES:
- 1 \* G611 IN CD36 IS REPLACED WITH A G612 IF MP81 IS INSTALLED
  - 2 SIGNAL NAMES INDICATE ASSERTED STATES FOR HIGH(H) LEVELS PER DEC STD 054







UNLESS OTHERWISE INDICATED:  
 TRANSISTORS ARE DEC2894-3B  
 DIODES ARE 1N4148  
 RESISTORS ARE 1/4W, 5%  
 CAPACITORS ARE 47 MMFD  
 C1'S ARE DEC747AN AC GND  
 PIN 14 ON EACH IC = AA +5V  
 R33 IS 1/2W, 1%, 50% PPM  
 R32 IS 1/2W, 1%, 50% PPM  
 R37, R39, R41 & R43 ARE 1/4W, 1%, 25% PPM  
 R45, R47, R49 & R50 ARE .3W, .01%, 5% PPM  
 Q16 THRU Q20 ARE SELECTED SILVER DOT



