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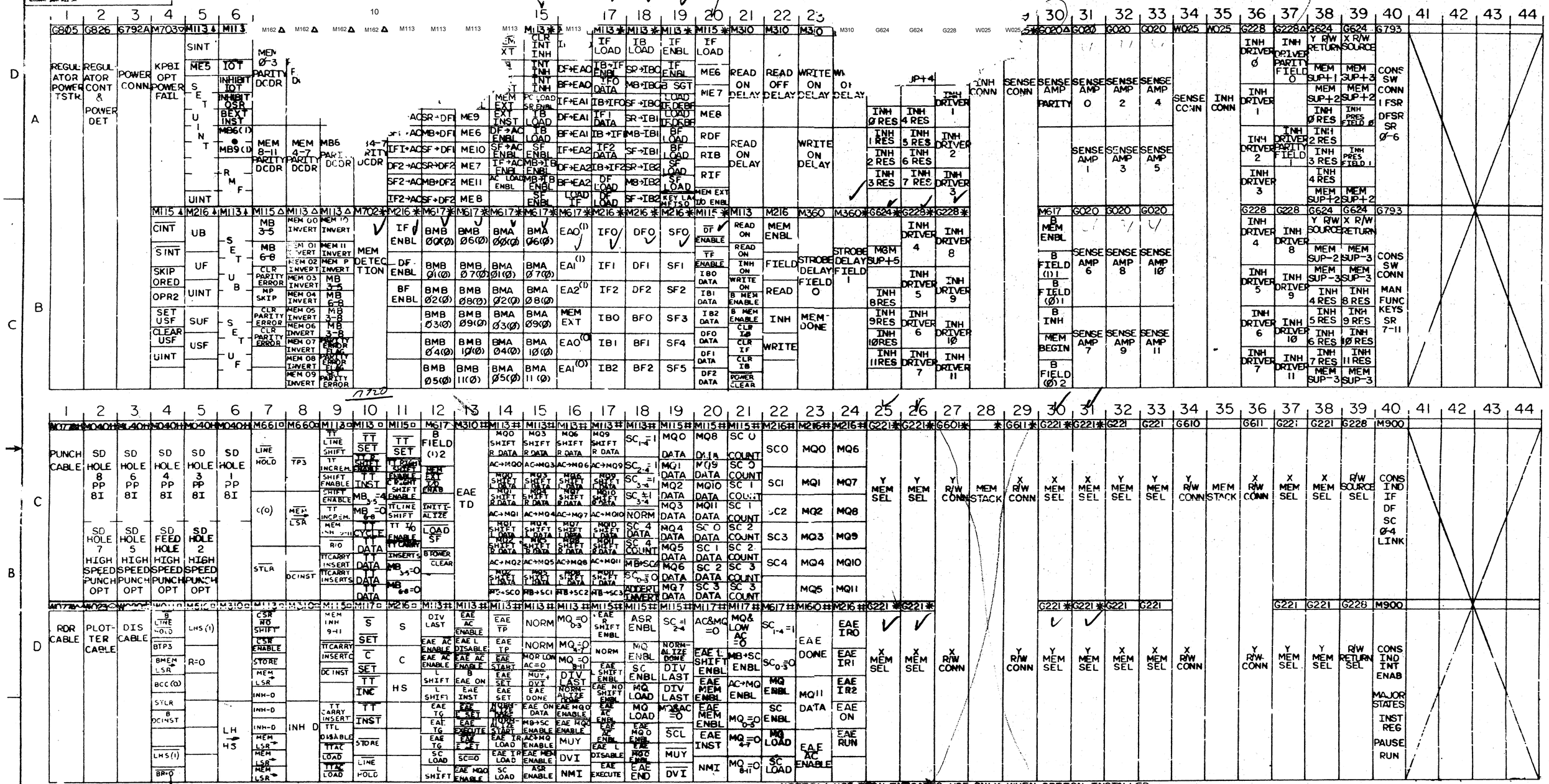

NOTES: 1. NOTATION INDICATES USE ONLY WHEN OPTION INSTALLED

⊕ MC8I	⊥ DL8I	⊗ VP8I	♦ KT8I				
⊕ MK8I	⊥ PD8I	⊗ VC8I					
⊕ MP8I	⊥ PC8I	⊗ KP8I					

2. * FOR MC8I REPLACE THE G020'S IN LOC A30, A31, A32 A33 B31, B32 AND B33 WITH G021'S

3. * Δ FOR MP8I AND MC8I9 THE USUAL MEMORY STACK DEC#30-05256-1 IS REPLACED BY DEC#30-05256-2 WITH G112'S AT LOC CD36 AND CD29 RESPECTIVELY.

REV	CHANGE NO.	CHK	
H	10		
J	12		
K	13		
L	81-00009	OLOUGHLIN	2-23-68
M	81-00013	OLOUGHLIN	9/3/68
N	81-00015	J.E. OLOUGHLIN	10/4/67
P	81-00020	OLOUGHLIN	11/11/67
R	00021	OLOUGHLIN	12/22/67
S	00024	OLOUGHLIN	12/27/67
T	81-00025	J. OLOUGHLIN	1/3/69
U	81-00032	M. ARSENAULT	2/14/69
V	81-00034	ARSENAULT	4/21/69
W	81-00042	ARSENAULT	3/19/69
Y	00046	M. ARSENAULT	2/14/69
Z	81-00050	ARSENAULT	5/12/69

REVISIONS

DEC FORM 187 DRD 105

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

		DATE
DRN.	R. BERNIER	8/9/67
CHK'D.	P. DAIGNEAULT	8/16/67
ENG.	E. DECASTRO	10/10/67
PROJ. ENG.	E. DECASTRO	10/10/67
PROD.	B. VAILLANCOURT	8/21/67

FIRST USED ON E-UA-8I-0-0

SCALE

SHEET 1 OF 2

digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS

TITLE MODULE UTILIZATION

SIZE	CODE	NUMBER	REV
D	MU	8I-0-17	AC

DIST.

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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
	M706	M707							MI13	M700	MI13	MI13	MI60	MI15	MI13	M310	M310	M216	M216	M216	MI15	MI13	M215	MI13	MI15	MI60	M160	MI60	MI60	MI60	M617	M617	M216	M220	M220	M220	M220	M220	M220	M900				
	TTY RECVR	TTY TRANS							SD	MFTS0	KEY LA	PAUSE	DATA	MEM START	MEM START	.3µS	.15µS	TS 1	TS 2	IR0	TAD	AND	FETCH	EAE LEFT SHIFT	EAE LEFT SHIFT	PC LOAD	CARRY INSERT	L ENABLE	MEM ENABLE	MA ENABLE	DOUBLE RIGHT ROTATE	MA LOAD	ADD ACCPT	AC Ø0	AC Ø2	AC Ø4	AC Ø6	AC Ø8	AC 10	CONS IND				
									SD	MFTP0	KEY ST	PAUSE		PC ENABLE	MEM START			MEM IDLE	TS 3	IR 1	AND	TAD		JMP DEFER 1	CLEAR INT ENABLE						DOUBLE LEFT ROTATE	PC LOAD	WC OVFOL							AC Ø-5			
									INH		KEY DP	CLEAR PAUSE		CLR	TP 4						ISZ	ISZ	DEFER	IR 0(I) IR 1(I)	WC+CA +TT SET						RIGHT SHIFT	MB LOAD	INT SYNC	AC Ø1	AC Ø3	AC Ø5	AC Ø7	AC Ø9	AC 11	MB Ø-5			
									INH		KEY EX+DP	IO END		IB IF DP	TP 4						JMS	JMS		SPECIAL CYCLE																			
									MEM ENAB 9-4	MFTS1	KEY STOP+SS	KEY LA EX+DP		KEY ST+EX +DP	TP 1						DCA	DCA	EXECUTE	TP 4+ INT OK																					
E	TTI Ø-7	TPR FLAG							MEM ENAB 9-4	MFTP1	KEY SI	SLOW CYCLE		SET TS 4	SET TS3			TS 4	IOP 1	BREAK SYNC	JMS	LOAD IR	WORD COUNT	AC Ø(I) MB Ø5(I)	D SET																			
									IOP 1(I)		KEY EX+DP	RESTART		SET TS 4	SET TS4		.35µS				IOT	JMP	CUR ADD.	HT AC=Ø	SKIP																			
	TTI SKIP	TTO SKIP							IOP2(I)	MFTS2	KEY IOP	TT SKIP		I/O STROBE	I/O START			SKIP	IOP 2	I/O ON	JMP	JMP		MID AC=Ø	SKIP																			
									IOP4(I)	MFTP2	KEY CONT	DATA TO WC			I/O START						OPR	OPR		LOW AC=Ø	LOAD SKIP																			
	IN ACTIVE	OUT ACTIVE							IOP IOT N		BREAK 1 MEM INC	TT INT		MFTS3	MFTS3				IOP 4		OPR	IOT	BREAK	SKIP	LOAD SKIP																			
			M452					∓M660	MI19		MI17	M310	M310	M310	M310	M310	M310	M310			MI17	MI13	MI13	MI13	MI13	MI15	MI17	MI15	MI13	M617	M617	MI13	M216	M220	M220	M220	M220	M220	M220	M900				
								B MEM START	OUR BACK PLANE HAS NO WIRES TO THIS CARD		IOT	I/O	.4µS	.4µS	.4µS	.4µS	.4µS	.4µS	.4µS		SLOW CYCLE	CLEAR ADD ACC	LOAD WC	MEMO	SKIP	NO SHIFT	OP 1	DOUBLE LEFT ROTATE	MEM ENABLE 5-11	MEM ENABLE Ø-4	CARRY INSERT	CONS IND											
											IOP										KEY LA ST EX OP CONT	CLEAR ADD ACC	LOAD IN SYNC	F SET	CA INCR																			
											I+2+4	STROBE	IOP 2	IOP 2	IOP 4	IOP 4	I/O	I/O			SET IOP 1	SET FETCH	LOAD INT DEL	SET TS	E SET	AUTO INDEX	I/O	MA ENABLE 0-4	MEM ENABLE 5-11	MEM ENABLE Ø-4		PC ENABLE											
											I PC ENABLE	ANS					END	END			LOAD INT ENABLE	SET IOP 2	LOAD INT ENB	E SET	E SET	AUTO INDEX		INT SKIP	MEM ENABLE 5-11	MEM ENABLE Ø-4	OP2	OP2	DATA ENABLE											
																						SET IOP 4	LOAD INT ENB	D SET		AUTO INDEX		PC ENABLE				AC CLEAR											
											INT INH										TT SKIP	SLOW CYCLE	CLEAR INT ENB	BREAK OK	BREAK OK	AC_0-3 =Ø	CARRY INSERT	DATA ENABLE	IO ENABLE	DATA ADD ENABLE												
																						TPC ENABLE	LOAD INT ENB	WC SET	WC SET			NO SHIFT		L ENABLE													
																					TT INT	SLOW CYCLE	LOAD INT ENB	B SET	F SET	AC_4-7 =Ø	AUTO INDEX	PC LOAD	SR ENABLE	SR ENABLE	SR ENABLE	B FETCH (I)	ADDER IB → LINK											
																						INITIAL	PROCESS OR IOT	SET B				AND ENABLE		TT INST			DATA → LINK											
											LOAD INT ENB	INITIAL		PROCESS OR IOT	PROCESS OR IOT											AC_8-11 =Ø		AC LOAD	OPI		KEY LA MFTS Ø		B EXECUTE (I)	CARRY → LINK											
																									LOAD SKIP				PC INCR	L ENABLE														
	WØ11 ∓	WØ11 ∓	WØ11 ∓	WØ11 ∓	WØ11 ∓		M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ	M651 ψ			A701 ∓	A707 ∓	A607 ∓	M705 ∿	M715 ∿	M710 ∓	M704 ∿	M708 ▼	M709 ⊗	M716 ↓		W971 ↓										
	EXTD MEM CONN	EXTD MEM CONN	EXTD MEM CONN	EXTD MEM CONN	EXTD MEM CONN		BAC Ø	BAC 3	BAC 6	BAC 9	IOP 1	TS3	MB0(I)	MB3(Ø)	MB4(I)	MB6(Ø)	MB7(I)	MB9(I)	RUN	BREAK																								
	MCB MB 3-5	BMA 6-11	MCB MB 6-11	MEM P	MEM 8-11																		VC8I OPT	VC8I OPT	VC8I OPT	PR8I OPT	PR8I OPT	PP8I OPT	VP8I OPT	KW8I OPT	KW8I OPT	CR8I OPT		CR8I OPT										
H	BMA Ø-5	MCB MB Ø-2	EA Ø-2	MEM Ø-7	MEM START STROBE MEM DONE MB PARITY ODD		BAC 1	BAC 4	BAC 7	BAC 1Ø	IOP 2	TS1	MB1(I)	MB3(I)	MB5(Ø)	MB6(I)	MB8(Ø)	MB1Ø(I)	TT INST	ADD. ACCPT			DIS CONT	DIS D/A	DIS D/A	RDR CONT	RDR CLOCK	PUNCH CONT	PLOTTER CONT	CLOCK CONT	CLOCK CTR	CARD RDR CONT 1		CARD RDR CABLE										
							BAC 2	BAC 5	BAC 8	BAC 11	IOP 4	INIT	MB2(I)	MB4(Ø)	MB5(I)	MB7(Ø)	MB8(I)	MB11(I)	WC OVFLO																									
	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ11	WØ76	M506 ψ	M506 ψ	M506 ψ	M506 ψ	M506 ψ	M506 ψ	M506 ψ	M506 ψ												M714 ↓											
	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	IN-OUT CONN	TTY CONN	IBØ	IB6	I/O SKIP	DATA ADD. Ø	DATA ADD. 6	DATA Ø	DATA 6	MEM INCR										KW8I OPT		CR8I OPT										
	BAC Ø-8	BAC 9-11 IOP 1,2&4 TS3 TS1 INIT	MB Ø-5	MB 6-11	IB Ø-8	IB 9-11 SKIP INT AC CLEAR RUN TT INST LINE	DATA ADD. Ø-8	DATA ADD. 9-11 BRK REST DATA DIR. BREAK ADD. ACC. MEM INCR	DATA Ø-8	DATA 9-11 3CYC CA NOR WC OVFLO	EXTD DATA ADD. Ø-2		IB1	IB7	INT RQST	DATA ADD. 1	DATA ADD. 7	DATA 1	DATA 7	3 CYCLE										FREQ SOURCE		CARD RDR CONT 2										
													IB2	IB8	AC CLEAR	DATA ADD. 2	DATA ADD. 8	DATA 2	DATA 8	$\overline{CA}$ INCR																						
													IB3	IB9	LINE	DATA ADD. 3	DATA ADD. 9	DATA 3	DATA 9	EXTD DATA ADD. Ø																						
													IB4	IB1Ø	BRK RQST	DATA ADD. 4	DATA ADD. 1Ø	DATA 4	DATA 1Ø	EXTD DATA ADD. 1																						
													IB5	IB11	DATA DIR	DATA ADD. 5	DATA ADD. 11	DATA 5	DATA 11	EXTD DATA ADD. 2																						
J	01	02	03	04	05	06	07	08	09	010																																		

4. M660 MODULE IN SLOT F08 TO BE PUT IN WITH THE FIRST MM 8I A ONLY.

NOTES: 1. NOTATION INDICATES USE ONLY WHEN OPTION INTALLED

#	VC8I	◇	VP8I	⋈	KW8I D,E		
∿	PR8I	T	MM 8I	↓	CR8I		
∓	PP8I	⧗	KW8I				

2. ⧗ FREQ SOURCE AT LOC J30 IS AS FOLLOWS:

M501	KW8IA OR D	M401	KW8IB OR E
M405	KW8IC OR F		

3. ψ FOR KA8I REPLACE M506'S IN LOC J13 TO J20 WITH M516'S; REPLACE M651'S IN LOC H07 TO H20 WITH M661'S; EXCEPT USE M660'S IN LOC H11 AND H12

REVISIONS				
CHK	CHANGE NO.	REV.		
	8I-00053	AA	T. Quillen 5-21-69	ARSENAULT
	8I-00054	AB	T. Quillen 6/3/69	ARSENAULT
	00059	AC		M. ARSENAULT

UNLESS OTHERWISE SPECIFIED	DRN.	DATE	[redacted] EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY ✓ REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D.	DATE	TITLE MODULE UTILIZATION
	ENG.	DATE	
	PROJ. ENG.	DATE	
	PROD.	DATE	
MATERIAL	FIRST USED ON		
FINISH	SCALE		SIZE D CODE MU NUMBER 8I-0-17 REV AC
	SHEET 2 OF 2		DWG.

SIZE D CODE MU NUMBER 8I-0-17 REV AC