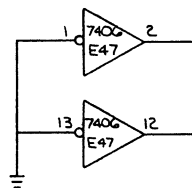
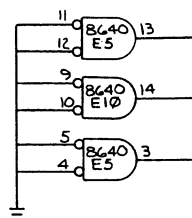


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NOTES:

- UNLESS OTHERWISE SPECIFIED:
RESISTORS ARE $\frac{1}{4}W, \pm 5\%$
CAPACITORS ARE 50V
DIODES ARE D672
- ALL RECEPTACLES ARE INDICATED WITH "J" NUMBERS.

ADDRESS	MEMORY BANK SELECTED
1/1	0 TO 16K 1ST 16K
2/2	16K TO 32K 2ND 16K
3/3	32K TO 48K 3RD 16K
4/4	48K TO 64K 4TH 16K

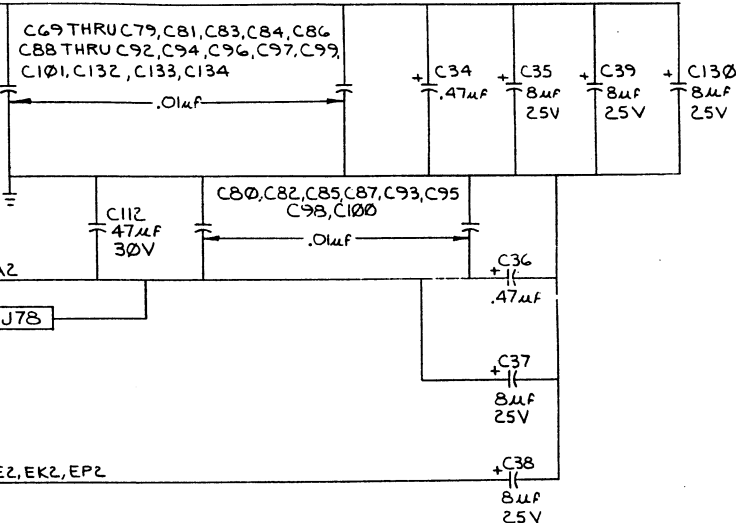


SPARES

+5V AA2, BA2

J77

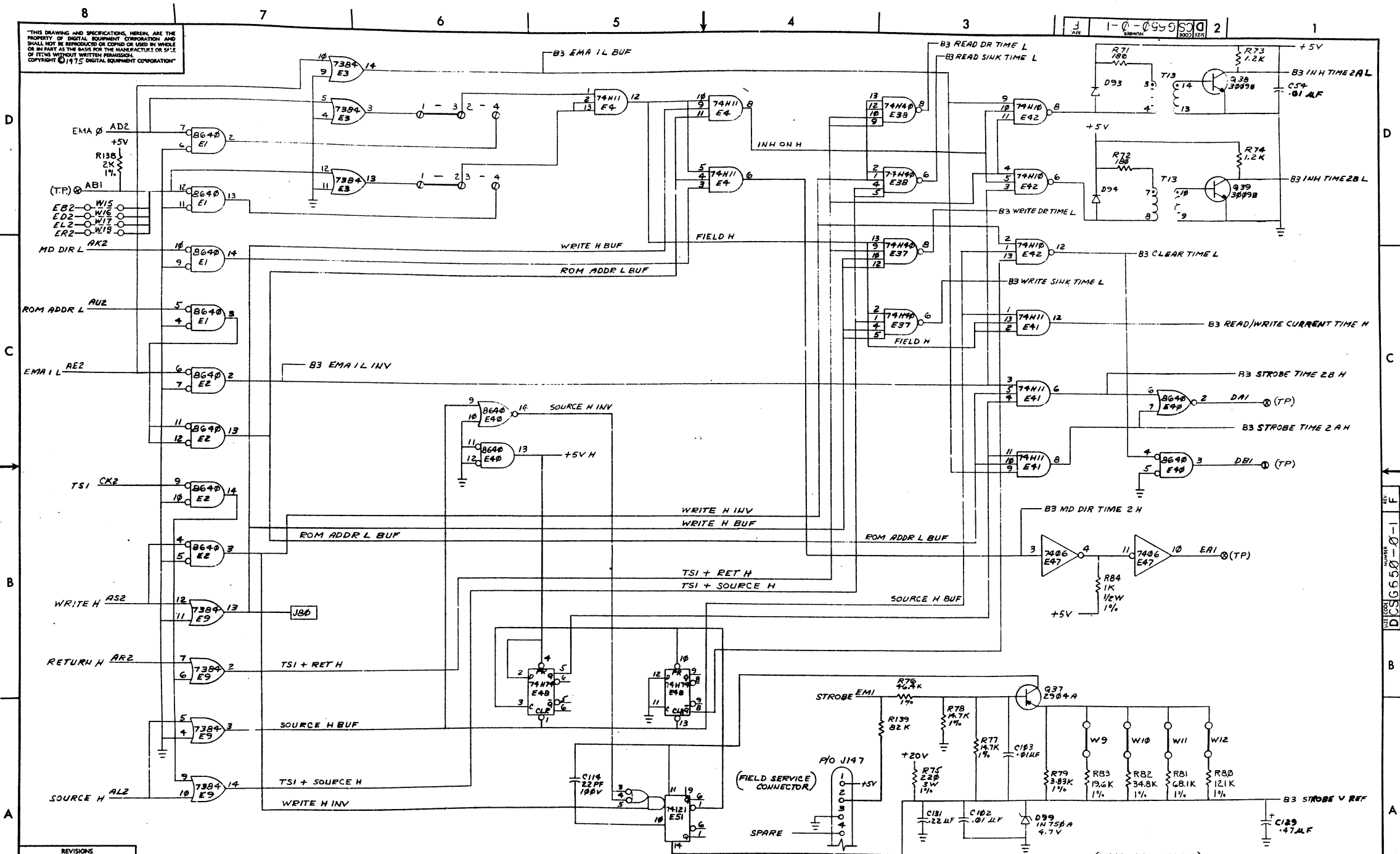
AC2 AF1 AF2 AN1 AN2 AT1 AT2
BC1 BC2 BF1 BF2 BN2 BT1 BT2
CC1 CC2 CF1 CF2 CN1 CN2 CT1 CT2 EC2 EF2
DC2 DF1 DF2 DN1 DN2 DT1 DT2 EN2 ET2



75325	8	9	16
7520	8	9	16
8640	1	8	
7442	8	16	
7274-1	4	7	
7384	1	8	
IC TYPE	-5V	0V	+5V
0V AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPT WHERE STATED ABOVE			
IC PIN LOCATIONS			

FIRST USED ON OPTION MODEL		QTY		REF. DESIGNATION	
MM8-AB					
ETCH BOARD REV.		D			
DATE 10-25-76		DATE 9-7-75		DATE 9-12-75	
DESIGNED BY: KARAKJIAN		DESIGNED BY: R. MEVLY		DESIGNED BY: C. GRANGER	
DRAWN BY: C. GRANGER		DRAWN BY: C. GRANGER		DRAWN BY: C. GRANGER	
CHECKED BY: C. GRANGER		CHECKED BY: C. GRANGER		CHECKED BY: C. GRANGER	
REVIEWED BY: C. GRANGER		REVIEWED BY: C. GRANGER		REVIEWED BY: C. GRANGER	
DEC NO.		EIA NO.		DEC NO.	
SEMICONDUCTOR CONVERSION CHART					
SCALE		OF 11			
SHEET					
TITLE		16K 12 BIT (BI) BASE BOARD			
SIZE CODE		D CS		NUMBER	
REV.		F			

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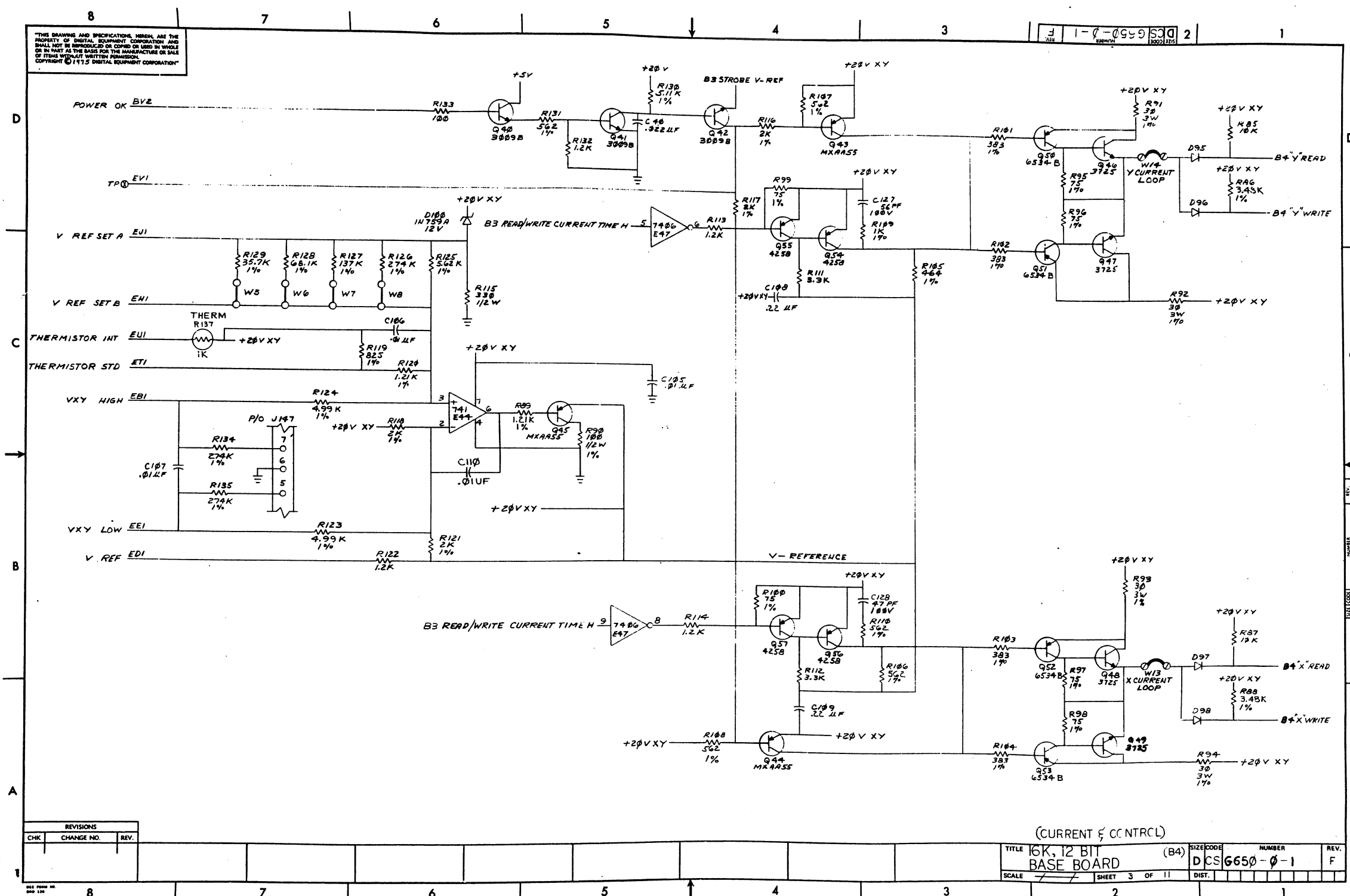
CHK	CHANGE NO.	REV.

(MM8-AB TIMING)

TITLE	16K, 12 BIT BASE BOARD (B3)	SIZE CODE	NUMBER	REV.
SCALE	1/2"	SHEET	2 OF 11	DIST.

DCS 6650-0-1

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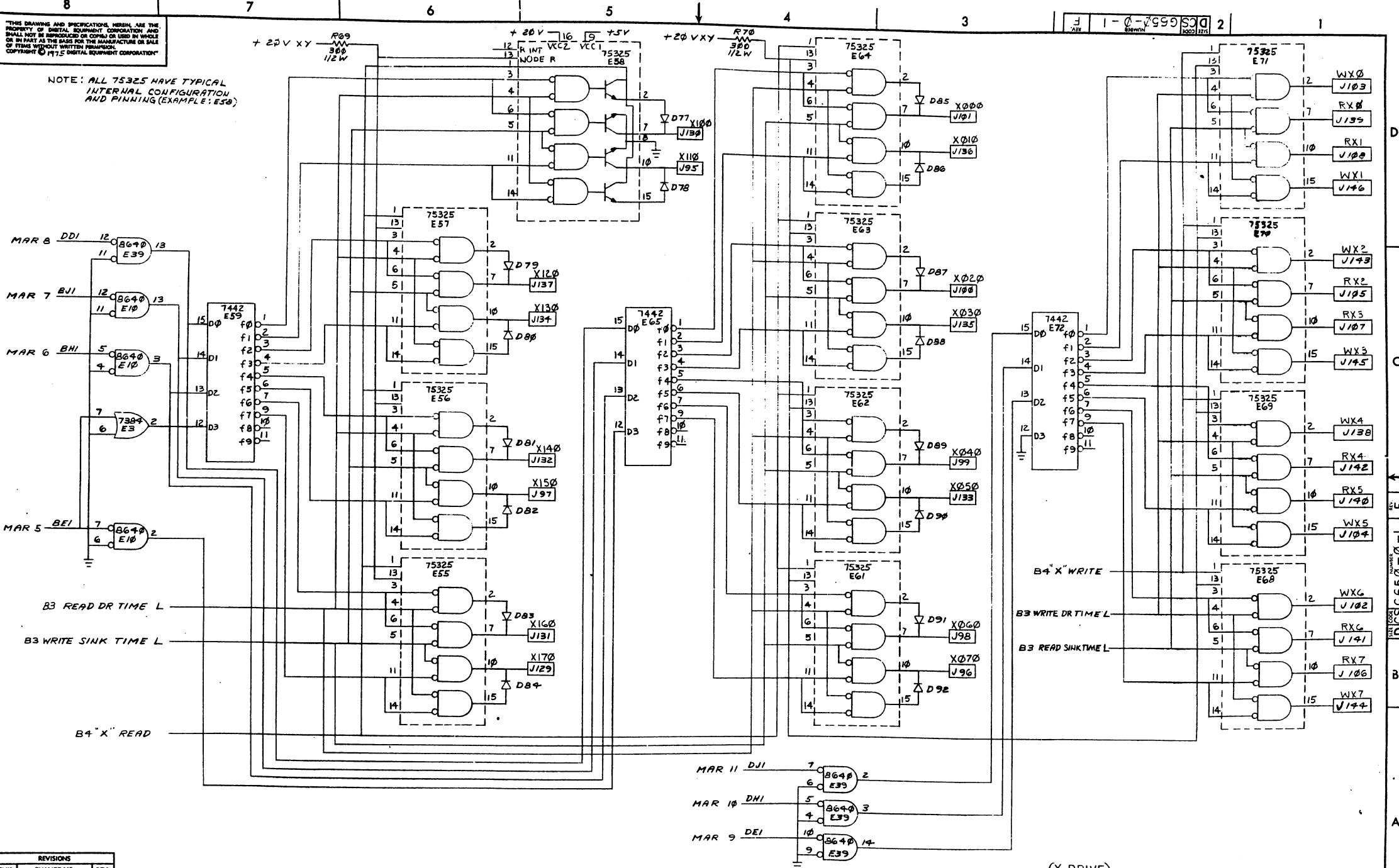
REVISIONS		
CHK	CHANGE NO.	REV.

(CURRENT & CONTRL)

TITLE	16K, 12 BIT BASE BOARD (B4)	SIZE CODE	DCS 6650-0-1	NUMBER		REV.	F
SCALE		SHEET	3 OF 11	DIST.			

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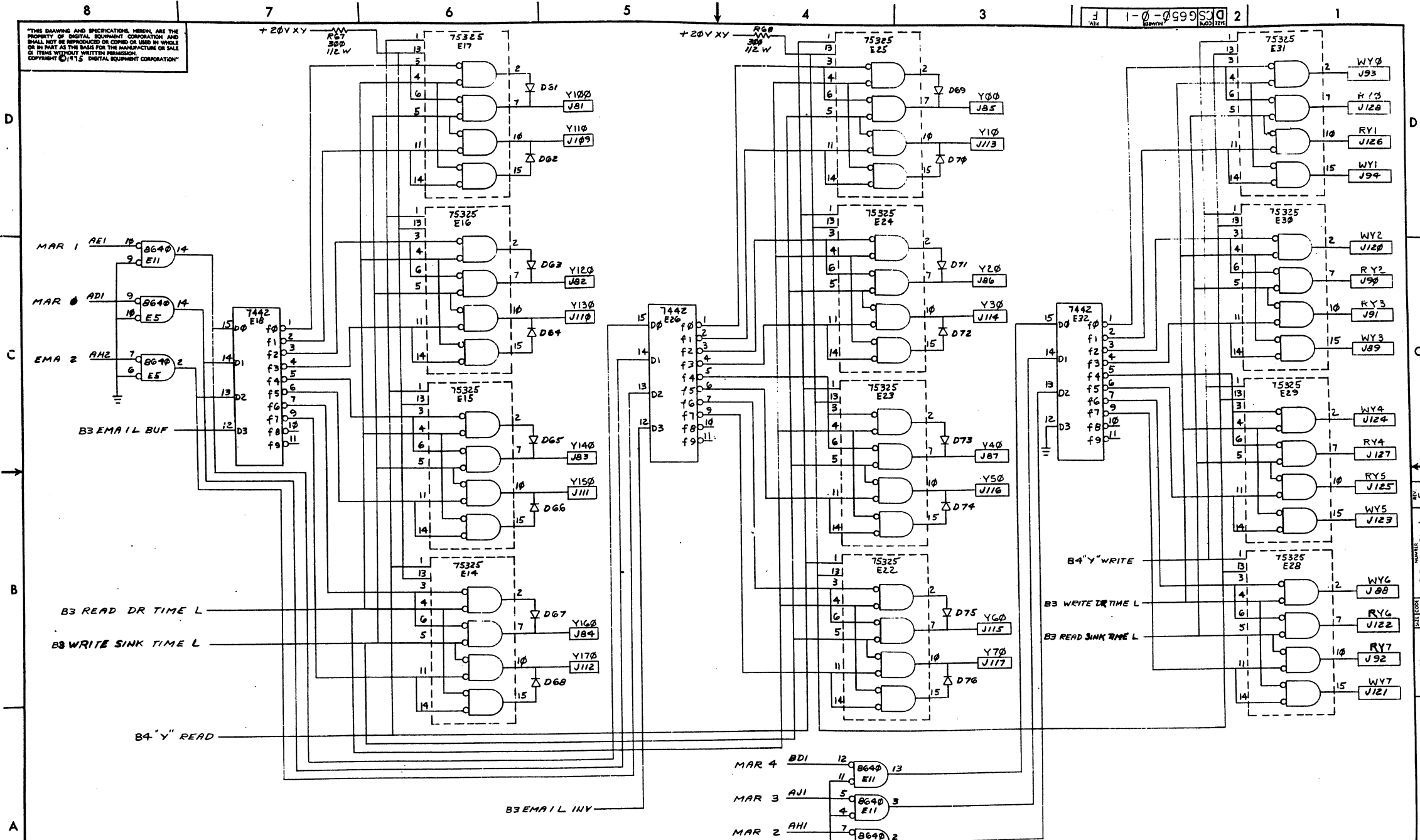
NOTE: ALL 75325 HAVE TYPICAL INTERNAL CONFIGURATION AND PINNING (EXAMPLE: E58)



REVISIONS		
CHK	CHANGE NO.	REV.

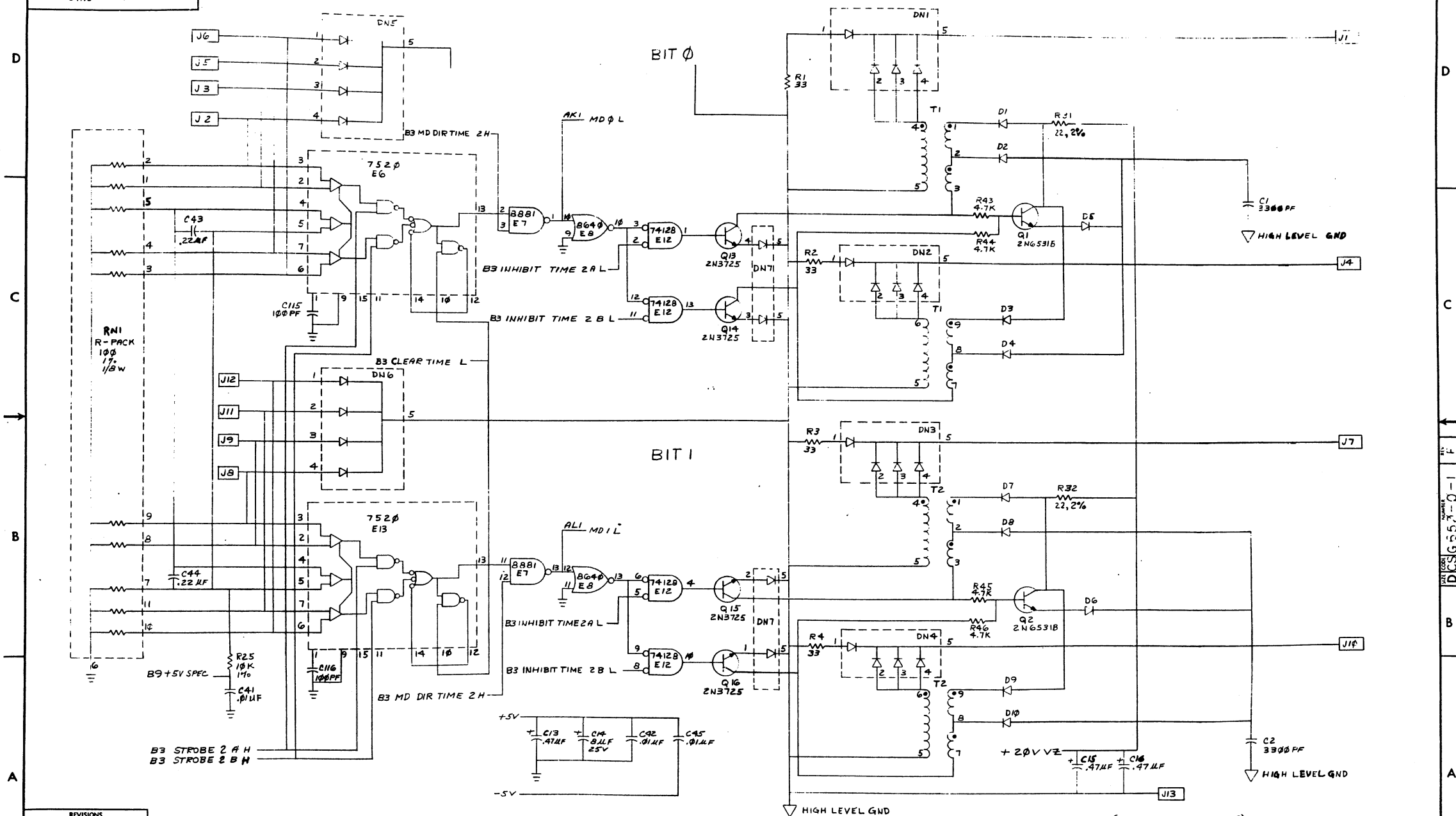
TITLE		(B5) SIZE CODE	NUMBER	REV.
6K.12 BIT BASE BOARD		D	CS G650-0-1	F
SCALE	SHEET	4	OF	11

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REVISIONS		
CHK	CHANGE NO.	REV.

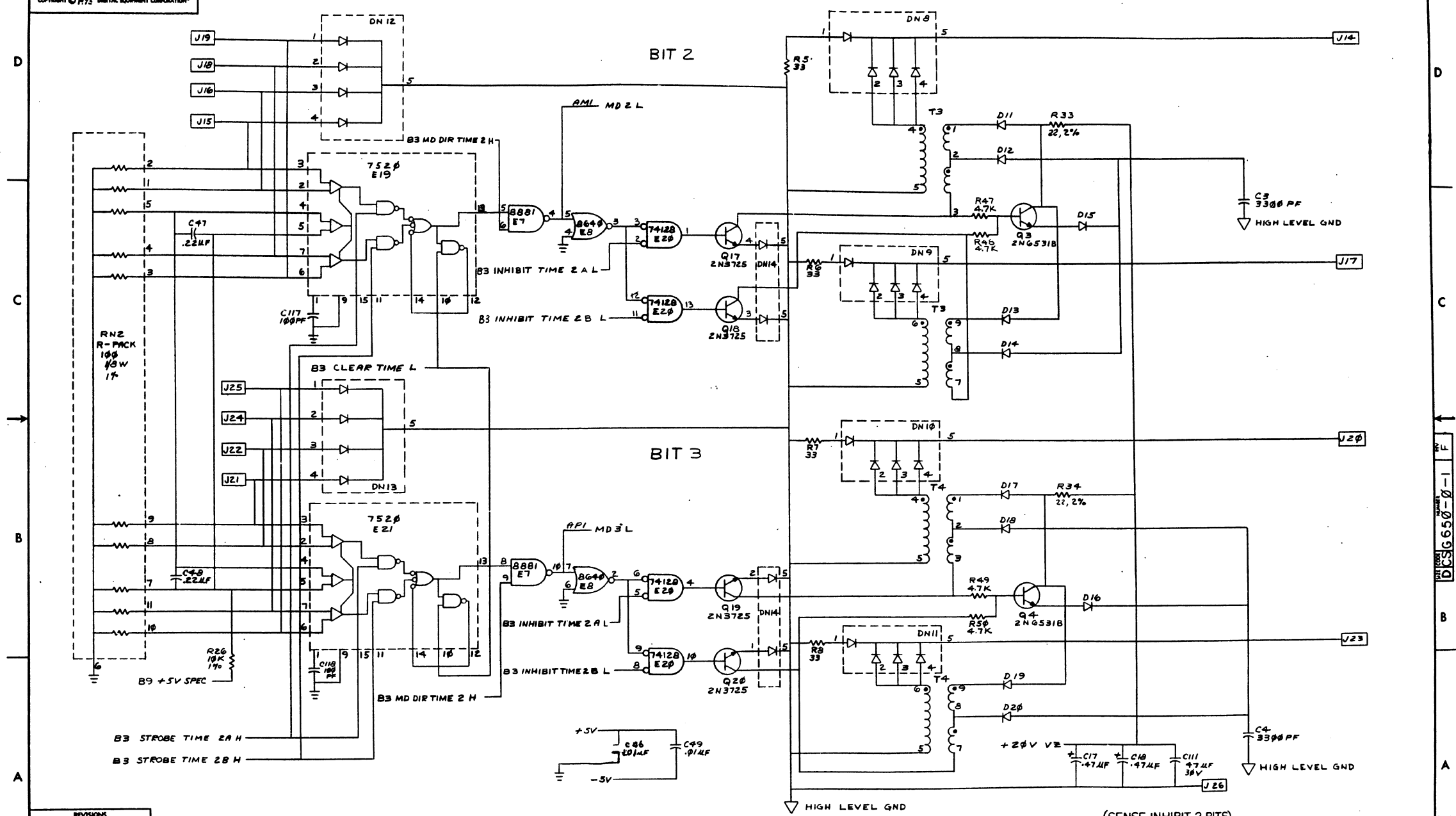
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE 16K.12 BIT BASE BOARD		B7	SIZE CODE	NUMBER	REV.
DCS G-650-0-1					F
SCALE	SHEET 6 OF 11	DIST.			

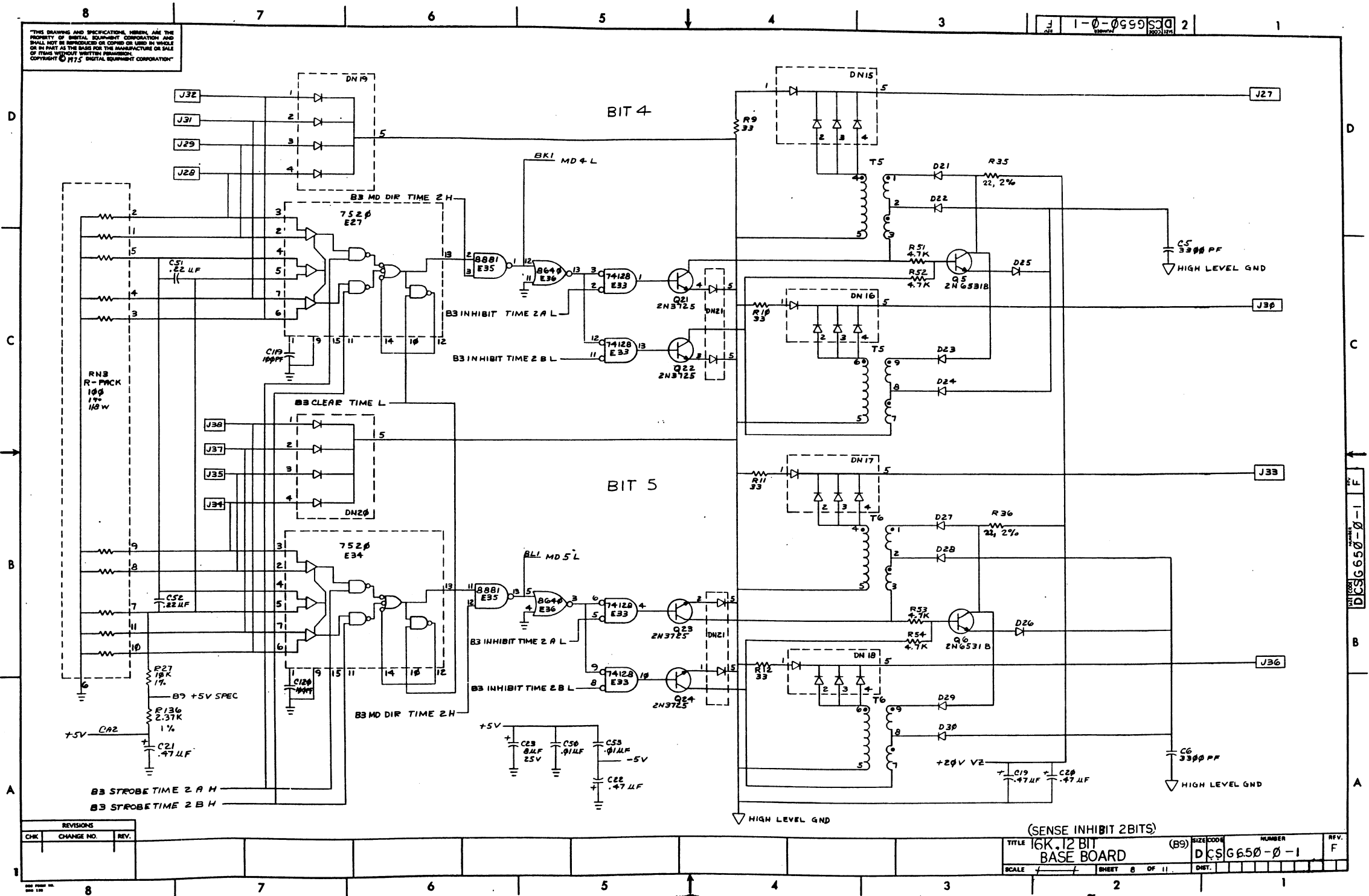
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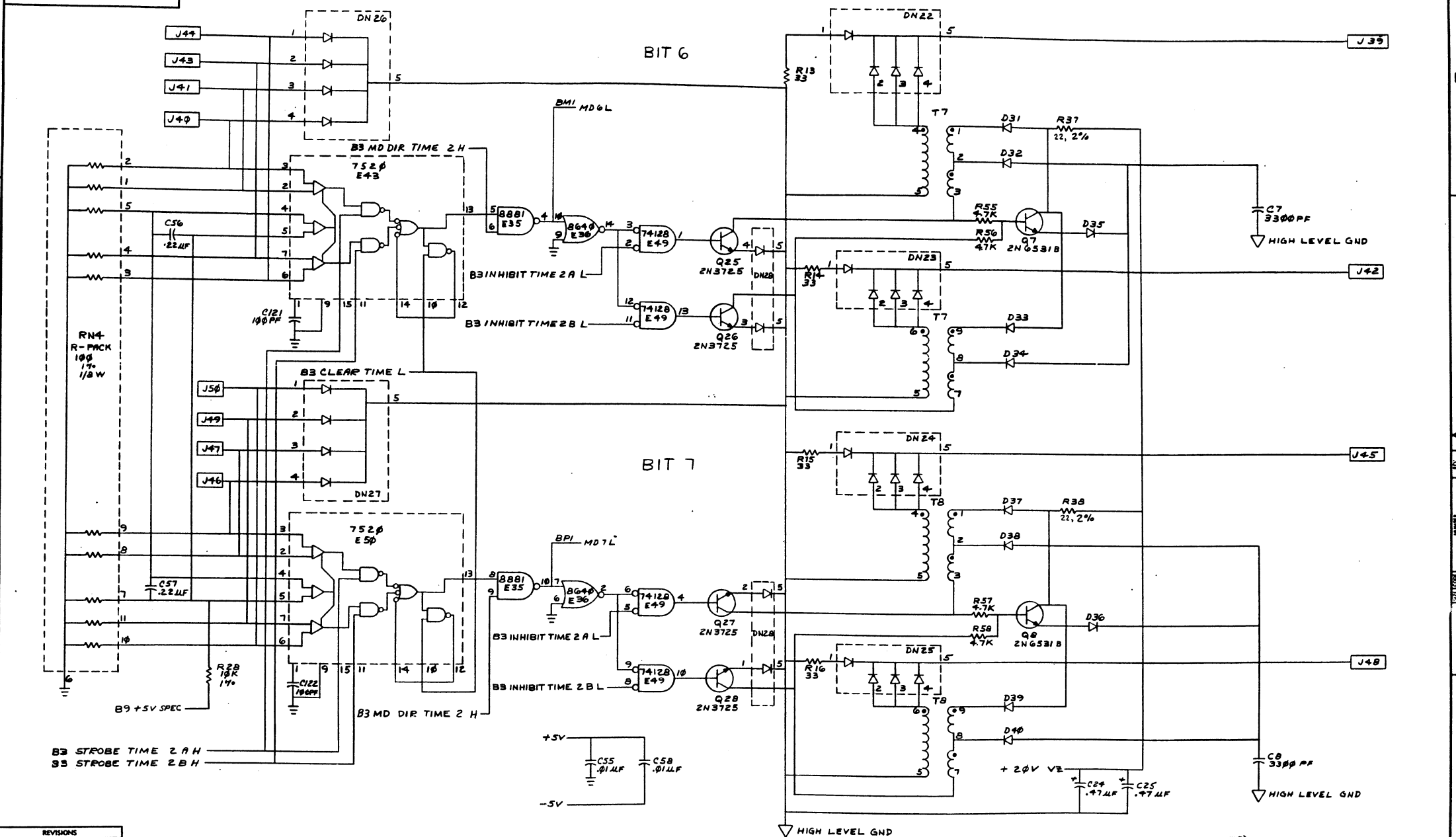
REVISIONS		
CHK	CHANGE NO.	REV.

(SENSE INHIBIT 2 BITS)		TITLE 16K 12 BIT BASE BOARD		SIZE CODE (BB)		NUMBER DCSG 650-0-1		REV. F	
SCALE		SHEET 7 OF 11		DIST.					

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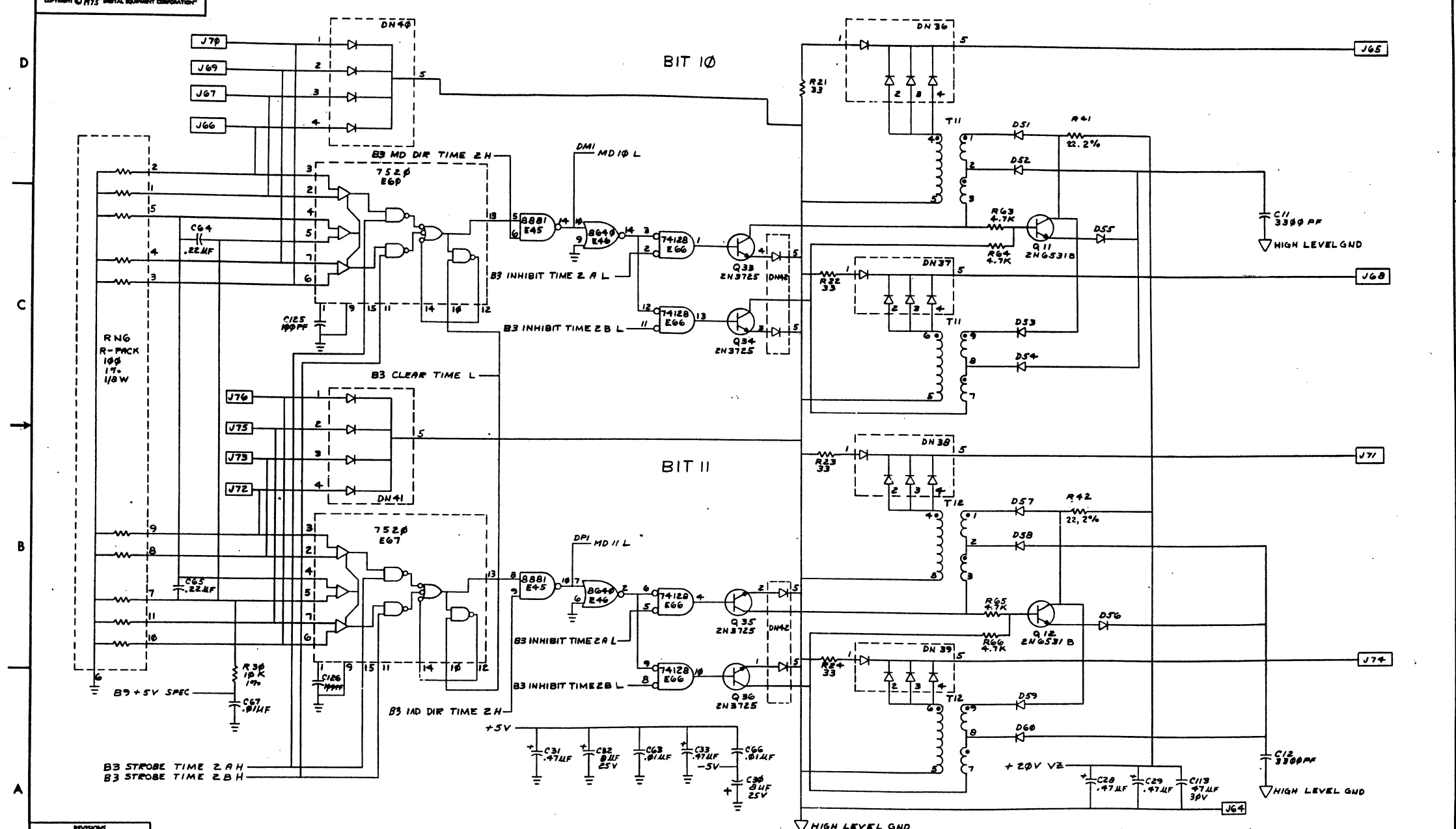
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REVISIONS		
CHK	CHANGE NO.	REV.
1		

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J 1-0-0995630 2



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		16K 12 BIT	(B12)	BASE BOARD	NUMBER	DCS G650-0-1	REV	F
SCALE		1:1	SHEET		11	OF	11	