

IDENTIFICATION

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PRODUCT CODE: MAINDEC-8E-000A-D-(D)
PRODUCT NAME: SE ADDER TESTS
DATE CREATED: NOVEMBER 2, 1970
MAINTAINER: DIAGNOSTIC GROUP
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1. 189(Y

THIS PROGRAM TESTS THE ADDER CIRCUITS OF THE PDP-8E. THE PROGRAM IS COMPOSED OF FOUR PARTS.

A SIMULATOR FOR THE TAD INSTRUCTION WHICH TESTS ALL COMBINATIONS OF TWO ARGUMENT ADDITIONS.

A SIMULATOR FOR ROTATE INSTRUCTIONS THAT TESTS ROTATION OF ALL POSSIBLE ARGUMENTS WITH RAL, RAR, RTL, RTR AND BSH.

A CARRY GENERATION TEST

A SERIES OF RANDOM NUMBER TESTS

2. REQUIREMENTS

2.1 EQUIPMENT

PDP-8E EQUIPPED WITH AT LEAST 4K OF MEMORY AND A TELETYPE

2.2 STORAGE

THE PROGRAM IS STORED IN LOCATIONS 0000-5554 AND UTILIZES LOCATIONS 7775-7777 AS A TEST AREA.

2.3 PRELIMINARY PROGRAMS

MAINDEX-8E-D0AA, D0BA

3. LOADING PROCEDURE

THE STANDARD PROCEDURE FOR LOADING BINARY TAPES IS TO BE USED.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SR00=1 SUPPRESS HALT ON ERROR
SR01=1 SUPPRESS ERROR TYPEOUT
SR02=1 LOOP ON ERROR
SR03=1 FAST TEST
SR09=1 HALT AT END OF TEST
SR10=1 SUPPRESS END OF TEST TYPEOUT
SR11=1 LOOP ON PRESENT TEST

4.2 STARTING ADDRESSES

NORMAL STARTING ADDRESS-0200
RESTORE LOADERS-7600

4.3 OPERATOR ACTION

4.3.1 SET SR=0200

4.3.2 PRESS ADDR LOAD SWITCH

4.3.3 SET S=0000

4.3.4 PRESS CLEAR AND CONT SWITCHES

5. OPERATING PROCEDURE

5.1 FAST TEST

THE ADDITION SIMULATOR NORMALLY STARTS WITH ARG1 AND ARG2 0000. TO SPEED UP THE TEST, THE VALUE OF ARG2 MAY BE SET AT SOME OTHER VALUE INITIALLY, TO DO THIS, DEPOSIT THE DESIRED VALUE IN LOCATION 170, AND PROCEED AS IN 4., BUT WITH SR=0400 INSTEAD OF 0000 IN 4.3.3

5.2 TO RESTORE AND START BINARY LOADER, STOP PROGRAM, LOAD ADDRESS 7600 AND START COMPUTER.

5.3 OPTIONS

SEE 4.1

6. ERRORS

6.1 ERROR MESSAGES

6.1.1 SIMULATED ADDITION TEST

IF A FAILURE OCCURS DURING THE SIMULATED ADDITION TEST, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND THEN HALT:

```
ARG1      SIMULATED ADD TEST FAILED
XXXXXXXXXX ARG2      SIMULATED      ARG1+ARG2      ARG2+ARG1
XXXXXXXXXX XXXXXXXXXXXX X XXXXXXXXXXXX X XXXXXXXXXXXX X XXXXXXXXXXXX
```

ARG1 AND ARG2 ARE THE TWO NUMBERS THAT WERE ADDED, SIMULATED IS THE ANSWER PRODUCED BY THE ADDITION SIMULATOR (LINK AND AC)
(ARG1+ARG2 IS THE RESULT OF ADDING ARG2 TO ARG1
(ARG1 IS IN AC INITIALLY)
(ARG2+ARG1 IS THE RESULT OF ADDING ARG1 TO ARG2
(ARG2 IS IN AC INITIALLY).

NOTE: EITHER THE SIMULATION OR THE ACTUAL ADDITIONS MAY HAVE FAILED.

6.1.2 SIM(ID ROTATE TEST

IF A FAILURE OCCURS DURING THE SIMULATED ROTATE TEST, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND THEN HALT:

```
SIMULATED AAA TEST FAILED
ORIGINAL      SIMULATED      ACTUAL
XXXXXXXXXXXXX X XXXXXXXXXXXXX X XXXXXXXXXXXXXXX
```

ORIGINAL IS THE LINK AND ACCUMULATOR TO BE ROTATED
SIMULATED IS THE SIMULATED RESULT OF ROTATION
ACTUAL IS THE REAL RESULT OF ROTATION
AAA IS THE INSTRUCTION BEING TESTED, I.E. RAL,RAR,RTL,RTR,BSW

6.1.3 FALSE CARRY TEST

IF A FAILURE OCCURS DURING THE FALSE CARRY TEST, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND THEN HALT:

```
DATA ERROR
AAAA X XXXXXXXXXXXXXXX
```

AAAA IS THE STARTING ADDRESS OF THE TEST THAT FAILED
X XXXXXXXXXXXXXXX ARE THE CONTENTS OF THE LINK AND AC

NOTE: EACH FALSE CARRY TEST EXPECTS LINK=1 AND AC=0
AS A RESULT.

6.1.4 RANDOM ADD TEST 1

IF A FAILURE OCCURS DURING RANDOM ADD TEST 1, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND THEN HALT:

```
RANDOM ADD TEST 1 FAILED
RANDA  RANDC  RESULT
XXXXXXXXXXXXX XXXXXXXXXXXXXXXX X XXXXXXXXXXXXXXX
```

RANDA IS A RANDOM NUMBER
RANDC IS THE COMPLEMENT OF RANDA
RESULT IS THE RESULT OF CONSECUTIVE ADDITIONS OF
RANDA AND RANDC

NOTE: THE EXPECTED RESULT IS LINK=1, AC=0

6.1.5 RANDOM ADD TEST 2

IF A FAILURE OCCURS DURING RANDOM ADD TEST 2, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND HALT:

```
RANDOM ADD TEST 2 FAILED
ARG1    ARG2    EXPECTED    ARG1+ARG2
XXXXXXXXXXXXX XXXXXXXXXXXXXXXX X XXXXXXXXXXXXXXXX XXXXXXXXXXXXXXX
```

6.1.6 RANDOM ROTATE TESTS

IF A FAILURE OCCURS DURING ONE OF THE RANDOM ROTATE TESTS, THE PROGRAM WILL TYPE THE FOLLOWING MESSAGE AND THEN HALT:

RANDOM AAA TEST FAILED
ORIGINAL ACTUAL
X XXXXXXXXXXXX X XXXXXXXXXXXX

AAAAAR, RAL, RTR OR RTL

6.2 ERROR HALTS

THE FOLLOWING TABLE LISTS ERROR HALT LOCATIONS AND THE TEST THAT THEY APPLY TO

LOCATION	TEST
502	SIMAD
1066	SIMROT (WITH LOCATION OF SPECIFIC TEST IN AC)
3035	FCT (WITH LOCATION OF SPECIFIC TEST IN AC)
3510	RNAD1
4041	RNAD2
4661	RANDOM ROTATE (WITH LOCATION OF SPECIFIC TEST IN AC)

6.2 ERROR RECOVERY

DEPRESS CONT TO RESUME TEST

6.3 LOOPING ON ERROR

6.3.1 SWITCH REGISTER CONTROL

SET SR00=1 TO SUPPRESS ERROR HALT
SET SR01=1 TO SUPPRESS ERROR TYPEOUT
SET SR02=2 TO LOOP
DEPRESS CONT

6.3.2 PROGRAM MODIFICATION

THERE ARE NOPS IN EACH TEST PROVIDED TO ALLOW THE OPERATOR TO SET UP LOOPS TIGHTER THAN THOSE AVAILABLE IN 6.3.1.

7. RESTRICTIONS

NONE

8. EXECUTION TIME

APPROXIMATELY 35 MINUTES. IF 3R3331, AND (XXXX=7777(SEE .1)
ONE PASS TAKES APPROXIMATELY 40 SECONDS.

AS EACH TEST OR GROUP OF TESTS IS COMPLETED, THE NAME OF THAT
TEST WILL BE TYPED. THE SEQUENCE IS:

SIMAD
SIMROT
FCT
RANDOM

9. PROGRAM DESCRIPTION

9.1 SIMULATED ADDITION TEST

THE SIMULATED ADDITION TESTS SIMULATES THE ADDITION OF TWO
ARGUMENTS, ARG1 AND ARG2. ACTUAL ADDITIONS ARE PERFORMED, AND
THEN THE ACTUAL RESULTS ARE COMPARED TO THE SIMULATED
ANSWER.

THE SIMULATOR OPERATES IN THE FOLLOWING MANNER:
THE ARGUMENTS ARE "AND'ED" TOGETHER, AND ANY BITS IN THE
RESULT THAT ARE 1'S WILL BE CARRY BITS. THE ARGUMENTS ARE
"OR'ED" TOGETHER AND THE RESULT IS STORED. THE PREVIOUSLY
GENERATED CARRIES ARE ROTATED ONCE TO THE LEFT AND THEN
"AND'ED" WITH THE "OR" OF THE TWO ARGUMENTS. ANY BITS THAT ARE
1'S ARE ALSO CARRIES AND THESE ARE COMBINED WITH THE PREVIOUS
CARRIES. THE PROCEDURE CONTINUES UNTIL NO NEW CARRIES ARE
GENERATED. THE FINAL CARRY RESULT IS EXCLUSIVE "OR" WITH THE
"OR" OF THE ARGUMENTS TO GET THE SIMULATED SUM.

9.2 SIMULATED ROTATE TESTS

EACH OF THE ROTATE INSTRUCTIONS, RAR, RAL, RTR, RTL AND BSW
IS SIMULATED FOR ALL POSSIBLE COMBINATIONS OF AC AND LINK,
AND THE RESULTS ARE COMPARED TO THE RESULTS OF THE ACTUAL
ROTATE.

9.3 FALSE CARRY TEST

VARIOUS COMBINATIONS OF INSTRUCTIONS AND DATA ARE USED TO
DETECT EITHER FALSE CARRIES, OR MISSING CARRIES.

1.4 RANDOM ADD TEST 1

A RANDOM NUMBER AND ITS COMPLEMENT ARE ADDED SUCCESSIVELY AND THE EXPECTED RESULT IS ALWAYS LINK=1, AC=0.

1.5 RANDOM ADD TEST 2

A RANDOM NUMBER, AND ITS MODIFIED COMPLIMENT ARE ADDED TO PRODUCE 1 KNOW BIT IN THE AC, WITH THE LINK=1.

1.6 RANDOM ROTATE TEST

A RANDOM NUMBER IS SUCCESSIVELY ROTATED AND THE EXPECTED RESULT IS THE ORIGINAL NUMBER.

/
/ ADDER TEST
/ FOR POP-8/E
/ COPYRIGHT 1970 DIGITAL EQUIPMENT CORP. MAYNARD MASS.
/ V 82 07552

7501	MQA=7501
7421	SQL=7421
7002	BSW=7002
6007	CAF=6007

/ SWITCH REGISTER MASK BITS

0103	SR00=K4000	/=0, HALT ON DATA ERROR
0104	SR01=K2000	/=1, SUPPRESS DATA ERROR
0105	SR02=K1000	/=1, LOOP ON DATA ERROR
0106	SR03=K0400	/=1, FAST TEST

```

/ #1, HALT AT END OF TEST
/ #1, SUPPRESS END OF TEST
/ #1, LOOP ON TEST

```

EQUIVALENCIES

```

00023      RAC=ARG1
00024      RLNK=ARG2
00031      RRAC=SUM1
00033      RRLNK=SUM2
00025      TEMPAC=SINAC
00026      TEMPL=SIMLNK
00037      TEMPI=WD1
00037      W1=WD1
00040      W2=WD2
00035      RHFLG=AHFLG
00067      NERROP=XLOOP

```

```

7775      *7775
7775      TSTA0, 0
7776      TSTA1, 0
7777      TSTA2, 0

0000      *0000
0000      TSTA3, 0
0001      TSTA4, JMP
0002      TSTA5, 3
0003      TSTA6, 2
0004      TSTA7, 0

```

0010 *10
/INDEX REGISTERS

0010 0000 TSTIND, 0
0011 0000 POINT1, 0
0012 0000 POINT2, 0

0020 *20
0020 0000 CNTR1, 0
0021 0000 ADA1, ADA2
0022 7777 ADA2, 7777

/SIMULATION VARIABLES

0023 0000 ARG1, 0
0024 0000 ARG2, 0
0025 0000 SIMAC, 0
0026 0000 SIMLNK, 0
0027 0000 A1ORA2, 0
0030 0000 CARRY, 0
0031 0000 SUM1, 0
0032 0000 LINK1, 0
0033 0000 SUM2, 0
0034 0000 LINK2, 0

/MESSAGE OUTPUT VARIABLES

0035 0000 AHFLG, 0
0036 0000 CHAR, 0
0037 0000 WD1, 0
0040 0000 WD2, 0

/RANDOM VARIABLES

0041 0037 RANDA, 37
0042 0000 RANDB, 0
0043 0000 RANDC, 0
0044 0000 LINKR, 0
0045 0000 LINKRC, 0

/INDIRECT POINTERS

0046 1600 XPRINT, PRINT /CHARACTER STRING TYPE
0047 1652 XTYPE, TYPE /CHARACTER TYPE

PAL10	V141	11-NOV-70	21:28	PAGE 1-2
0050	XRHD,	RHD	/TYPE ROTATE ERROR HEADER	
0051	XSROT,	SROTAL	/COMMON ROTATE SIMULATOR	
0052	XRALTA,	RALTAB-1	/RAL MASK TABLE	
0053	XRTLTA,	RTLTAB-1	/RTL MASK TABLE	
0054	XRTRTA,	RTLRTAB-1	/RTL MASK TABLE	
0055	XBSWTA,	BSWTAB-1	/BYTE SWAP MASK TABLE	
0056	XCOMRO,	COMROT	/ROTATE COMPARISON FOR SIMULATION	
0057	XNXTR0,	NXTROT	/ROTATE SETUP FOR SIMULATION	
0060	XLNKOU,	LNKOUT	/TYPE LINK	
0061	XWDOUT,	WDOUT	/TYPE DATA WORD	
0062	XAMEAS,	SAMEAS	/COMPARE DATA	
0063	XAMEA,	SAMEA		
0064	XAVREG,	SAVREG	/SAVE AC AND LINK	
0065	XDATER,	DATER	/DATA ERROR HANDLER FOR FCT	
0066	XHALT2,	HALT2	/DATA ERROR HALT FOR FCT	
0067	XLOOP,	LOOP	/LOOP ON TEST	
0070	XSTA0,	TSTA0		
0071	XSTA1,	TSTA1		
0072	XSTA2,	TSTA2		
0073	XRAND,	RANDOM	/RANDOM NUMBER GENERATOR	
0074	XLOOP2,	HLTA+4		
0075	XLOOP1,	LOOP1		
			/WIDELY USED CONSTANTS	
0076	K240,	240		
0077	K260,	260		
0100	K261,	261		
0101	K6000,	6000		
0102	XRARTA,	.		
0103	K4000,	4000		
0104	K2000,	2000		
0105	K1000,	1000		
0106	K0400,	400		
0107	K0200,	200		
0110	K0100,	100		
0111	K0040,	40		
0112	K0020,	20		
0113	K0010,	10		
0114	K0004,	4		
0115	K0002,	2		
0116	K0001,	1		
0117		0		
0120		4000		
0121		1		
			/TEST POINTERS FOR FCT	
0122	SEQ1,	FCT1		
0123	SEQ2,	FCT2		
0124	SEQ3,	FCT3		
0125	SEQ4,	FCT4		

/SIMULATE ADDITION BY SIMULATED GENERATEION OF SUM
/AND CARRY BITS

/FORM OR OF ARG1 WITH ARG2

0203 7340 SIMAD, CLA CLL CMA /LOAD AC WITH ARG1
0204 0023 AND ARG1 /PLACE IN MQ
0205 7421 MQL
0206 7040 CMA
0207 0024 AND ARG2 /LOAD AC WITH ARG2
0210 7501 MQA /FORM ARG1 OR ARG2
0211 3027 DCA A10RA2 /SAVE ARG1 OR ARG2

/FORM XOR(EXCLUSIVE OR) OF ARG1 WITH ARG2
/BY A XOR B=(A AND NOTB)OR(NOTA AND B)
0212 7501 MQA /GET ARG1 FROM MQ
0213 7040 CMA /FORM NOTARG1
0214 0024 AND ARG2 /AND WITH ARG2 TO GET ARG2 AND NOTARG1
0215 7421 MQL /SAVE IN MQ
0216 7040 CMA
0217 0024 AND ARG2 /LOAD AC WITH ARG2
0220 7040 CMA /FORM NOTARG2
0221 0023 AND ARG1 /AND WITH ARG1 TO GET ARG1 AND NOTARG2
0222 7501 MQA /OR WITH ARG2 AND NOTARG1
0223 3025 DCA SIMAC /TO GET ARG1 XOR ARG2
0224 3026 DCA SIMLNK

/AND ARG1 WITH ARG2
/TEST FOR CARRIES
/IF THERE ARE NO BITS IN COMMON BETWEEN ARG1 AND ARG2
/THERE WILL BE NO CARRIES GENERATED

0225 7040 CMA /LOAD AC WITH ARG1
0226 0023 AND ARG1 /AND WITH ARG2
0227 0024 AND ARG2 /ARE THERE ANY CARRIES
0230 7450 SNA ADD /NO, TERMINATE SIMULATION
0231 5273 JMP

/GENERATE CARRIES

0232 7421 MQL /SAVE FIRST CARRIES
0233 7521 MQA MQL /GET CARRIES FROM MQ
0234 0027 AND A10RA2 /AND WITH A10RA2 TO SEE IF MORE CARRIES ARE GENERATED
0235 7450 SNA /ARE THERE ANY MORE CARRIES
0236 5243 JMP ENCAR /NO, END SIMULATION OF CARRIES
0237 7104 CLL RAL /PROPIGATE CARRIES
0240 7521 MQA MQL /GET PREVIOUS CARRIES FROM MQ, SAVE NEW CARRIES
0241 7501 MQA /OR NEW CARRIES WITH PREVIOUS CARRIES

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0242	5233	JMP	NXYCAR	/CONTINUE
		/		/TEST FOR CARRY INTO LINK
		/		/
0243	7501	ENCAR,	MOA	/GET CARRIES
0244	0027		AND	/AND WITH A10RA2
0245	0103		AND	/TEST BIT 00
0246	7450		SNA	/IS BIT 00 1
0247	5252		JMP	ENCAR1
0250	3026		DCA	SIMLNK
0251	5257		JMP	XORALL
0252	7130	ENCAR1,	CLL	CML RAR
0253	0023		AND	ARG1
0254	0024		AND	ARG2
0255	7440		SEA	
0256	3026		DCA	SIMLNK
		/		/FORM XOR OF ARG1, ARG2 AND CARRIES
		/		/TO GET FINAL SIMULATED SUM
		/		/
0257	7501	XORALL,	MOA	/SAVE SIMULATED CARRIES
0260	3030		DCA	CARRY
0261	7501		MOA	
0262	7040		CMA	
0263	0025		AND	SIMAC
0264	7421		MQL	/FORM A10RA2 AND NOTCARRY
0265	7040		CMA	/SAVE IN MQ
0266	0025		AND	
0267	7040		CMA	
0270	0030		AND	CARRY
0271	7501		MOA	/FORM CARRY AND NOTA10RA2
0272	3025		DCA	/OR WITH CONTENTS OF MQ
		/		/TO GET FINAL SIMULATED SUM
		/		/
0273	7340	ADD,		/PERFORM ADDITIONS ARG1+ARG2 AND ARG2+ARG1
0274	0023		CLA	CLL CMA
0275	1024		AND	ARG1
0276	7000		TAD	ARG2
0277	3031		NOP	/LOAD AC WITH ARG1
0300	7010		DCA	/ADD ARG2
0301	3032		RAR	/SAVE RESULT
0302	7040		DCA	/SAVE LINK
0303	0024		CMA	
0304	1023		AND	ARG2
0305	7000		TAD	ARG1
0306	3033		NOP	/LOAD AC WITH ARG2
0307	7010		DCA	/ADD ARG1
0310	3034		DCA	/SAVE RESULT
0311	7000		NOP	/SAVE LINK
		/		/
		/		/COMPARE RESULTS OF REAL ADDS

/IF A=B, A XOR B=0, THIS IS USED TO COMPARE RESULTS

```
0312 2 7340 CLA CLL CMA /GET RESULT OF ARG1+ARG2
0313 0031 AND SUM1 /COMPLEMENT
0314 7040 CMA /AND RESULTS OF ARG2+ARG1
0315 0033 AND SUM2 /IS SUM2 AND NOTSUM1=0
0316 7440 SEA ERROR1 /NO, ERROR
0317 5377 JMP
0320 7040 CMA
0321 0033 AND SUM2 /LOAD AC WITH RESULTS OF ARG2+ARG1
0322 7040 CMA /COMPLEMENT
0323 0031 AND SUM1 /AND WITH SUM1
0324 7440 SEA /IS SUM1 AND NOTSUM2=0
0325 5377 JMP ERROR1 /NO, ERROR
```

/COMPARE REAL AND SIMULATED ADDS

```
0326 7340 CLA CLL CMA /LOAD AC WITH RESULTS OF ARG1+ARG2
0327 0031 AND SUM1 /COMPLEMENT
0330 7040 CMA /AND WITH RESULTS OF SIMULATION
0331 0025 AND SIMAC /IS SIMAC AND NOTSUM1=0
0332 7440 SEA ERROR1 /NO, ERROR
0333 5377 JMP
0334 7040 CMA
0335 0025 AND SIMAC /LOAD AC WITH SIMULATION RESULTS
0336 7040 CMA /COMPLEMENT
0337 0031 AND SUM1 /AND WITH RESULTS OF ARG1+ARG2
0340 7440 SEA /IS SUM1 AND NOTSIMAC=0
0341 5377 JMP ERROR1 /NO, ERROR
```

/COMPARE LINKS GENERATED BY REAL ADDS

```
0342 7340 CLA CLL CMA /GET LINK FROM ARG1+ARG2
0343 0032 AND LINK1
0344 7004 RAL
0345 7240 CLA CMA /GET LINK FROM ARG2+ARG1
0346 0034 AND LINK2
0347 7640 SEA CLA
0350 7020 CML /ARE THEY THE SAME
0351 7430 SZL /NO, ERROR
0352 5377 JMP ERROR1
```

/COMPARE LINKS GENERATED BY REAL AND SIMULATED ADDS

```
0353 7340 CLA CLL CMA /GET LINK FROM ARG1+ARG2
0354 0032 AND LINK1
0355 7004 RAL
0356 7240 CLA CMA
```

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0357	0026	AND	SIMLNK	/GET LINK FROM SIMULATION
0360	7640	SEA	CLA	
0361	7020	CML		
0362	7430	SZL		/ARE THEY THE SAME
0363	5377	JMP	ERROR1	/NO, ERROR

0364	5474	NXTADD,	JMP I	XLOOP2	/TEST FOR SIMULATION WITH SAME DATA
0365	2023	ISE	ARG1		/INCREMENT ARG1
0366	3203	JMP	SIMAD		/GO TO SIMULATION
0367	2024	ISE	ARG2		/INCREMENT ARG2
0370	7410	SKP			/GO TO SIMULATION
0371	5475	JMP I	XLOOP1		/TEST FOR TRANSFER TO NEXT TEST
0372	7240	CLA	CHA		
0373	0024	AND	ARG2		/TRANSFER ARG2 TO ARG1
0374	3023	DCA	ARG1		
0375	5203	JMP	SIMAD		/CONTINUE SIMULATION
0377	0377				
0377	7000	ERROR1,	NOP		

0400	7604	ADDERR,	LAS		/GET SWITCHES
0401	0104	AND	SR01		/TEST SR01
0402	7650	SNA	CLA	ADPRT	/SUPPRESS TYPEOUT IF SR01=1
0403	4217	JMS			/TYPE ERROR MESSAGE
0404	7604	LAS			
0405	0103	AND	SR00		/HALT IF SR00=0
0406	7650	SNA	CLA	HALTA	/HALT WITH ADDRESS OF TEST IN AC
0407	4277	JMS			
0410	7604	LAS			
0411	0105	AND	SR02		/TEST SR02
0412	7640	SEA	CLA		/LOOP WITH SAME DATA IF SR02=1
0413	5615	JMP I	XADD		/LOOP WITH SAME DATA
0414	5616	JMP I	XNXTAD		
0415	0273	XADD,	ADD		
0416	0365	XNXTAD,	NXTADD+1		

0417	0000	ADPRT,			/TYPE ERROR MESSAGE FOR ADDITION TEST
0420	7340	CLA	CLL	CHA	
0421	0035	AND	AHFLG		/GET FLAG FOR ERROR MESSAGE HEADER TYPEOUT
0422	7650	SNA	CLA		/HAS HEADER FOR TEST BEEN TYPED
0423	4267	JMS	CHA	ABOUT	/NO TYPE HEADER
0424	7040	CMA			
0425	0023	AND	ARG1		
0426	3037	DCA	WD1		
0427	4323	JMS	WDOUT		/OUTPUT ARG1
0430	7040	CMA			


```
0431 0024 AND ARG2
0432 3037 DCA WD1
0433 4323 JMS WDOUT
0434 7240 CMA
0435 0026 AND SIMLNK
0436 3040 DCA WD2
0437 7040 CMA
0440 0029 AND SIMAG
0441 3037 DCA WD1
0442 4304 JMS LNKOUT
0443 4323 JMS WDOUT
0444 7040 CMA
0445 0032 AND LINK1
0446 3040 DCA WD2
0447 7040 CMA
0450 0031 AND SUM1
0451 3037 DCA WD1
0452 4304 JMS LNKOUT
0453 4323 JMS WDOUT
0454 7040 CMA
0455 0034 AND LINK2
0456 3040 DCA WD2
0457 7040 CMA
0460 0033 AND SUM2
0461 3037 DCA WD1
0462 4304 JMS LNKOUT
0463 4323 JMS WDOUT
0464 4446 JMS I XPRINT
0465 5542 CRLF-1
0466 9204 JMP HLTA

/
/TYPE HEADER FOR ADDITION TEST ERROR MESSAGE
/
0467 0000 AROUT,
0470 4446 JMS I XPRINT
0471 5217 EM1-1
0472 4446 JMS I XPRINT
0473 4777 DH1-1
0474 7240 CLA CMA
0475 3035 DCA AHFLG
0476 5667 JMP I AROUT

/
/HAUT WITH ADDRESS OF TEST IN AC
/
0477 0000 HALTA,
0500 7240 CLA CMA
0501 0351 AND ADT
0502 7402 HLT
0503 5677 JMP I HALTA

/
/TYPE LINK
/
0504 0000 LNKOUT, 0
```

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0505 7340 CLA CLL CMA
0506 0040 AND WD2
0507 7640 SZA CLA
0510 5320 JMP OUT1
0511 7040 CMA
0512 0077 AND K260
0513 4447 JMS I XTYPE
0514 7040 CMA
0515 0076 AND K240
0516 4447 JMS I XTYPE
0517 5704 JMP I LNKOUT
0520 7040 CMA
0521 0100 AND K261
0522 5313 JMP TYLNK

```

TYLNK,

OUT1,

/TYPE DATA WORD

```

0523 0000 /
0524 7340 CLA CLL CMA
0525 0102 AND XRARTA
0526 3011 DCA POINT1
0527 7040 CMA
0530 0411 AND I POINT1
0531 7450 SNA
0532 5345 JMP SP1
0533 0037 AND WD1
0534 7640 SZA CLA
0535 5342 JMP OUT1A
0536 7040 CMA
0537 0077 AND K260

```

WDOUT,

NXBIT,

```

0540 4447 JMS I XTYPE
0541 5327 JMP NXBIT
0542 7040 CMA
0543 0100 AND K261
0544 5340 JMP TYBIT
0545 7040 CMA
0546 0076 AND K240
0547 4447 JMS I XTYPE
0550 5723 JMP I WDOUT
0551 0203 ADT,

```

TYBIT,

OUT1A,

SP1,

ADT,

/END OF SIMULATED ADD TEST

```

0552 7604 LAS
0553 0115 AND SR10
0554 7650 SNA CLA
0555 5370 JMP SADOK
0556 7604 LAS
0557 0114 AND SR09
0558 07640 SZA CLA
0559 17402 HLT

```

```

/TEST SR10
/IS SR10=1
/NO, TYPE END OF TEST MESSAGE
/TEST SR09
/IS SR
/YES, ( AT END OF TEST

```

0562 7604 LAS /TEST SR11
 0563 0116 AND SR11
 0564 7650 SNA CLA /IS SR11#1
 0565 9377 JMP SIMR /NO, GO TO NEXT TEST
 0566 5767 JMP I .01 /REPEAT SIMAD
 0567 0203 SIMAD
 0570 4446 JMS I XPRINT
 0571 5521 OK1-1
 0572 5356 JMP ADHLT
 0577 0577 JMR, NOP
 0577 7000

/TEST ROTATION BY COMPARISON OF REAL AND SIMULATED
 /ROTATES

/SET UP FOR RAL TEST

/GET MASK TABLE FOR
 /SIMULATED RAL
 /SIMULATE RAL
 /SET UP TO DO REAL ROTATES

/DO REAL RAL

/SAVE ROTATED ACCUMULATOR

/SAVE ROTATED LINK
 /COMPARE ROTATES
 /RETURN HERE FOR LOOP ON ERROR
 /SET UP FOR NEXT ROTATE
 /CONTINUE RAL TEST

/GET MASK TABLE FOR
 /SIMULATED RAR
 /SIMULATED RAR

JMS I XR2

/TEST RAR

CLA CLL CMA
 AND XRARTA
 DCA POINT2
 JMS I XSROT

0626 7340
 0627 0102
 0630 3012
 0631 4451

0600 4752
 0600 4752
 0600 4752

SIMRAL, CLA CLL CMA
 AND XRALTA
 DCA POINT2
 JMS I XSROT
 CLA CLL CMA
 AND RLNK
 SEA CLA
 CML
 CMA
 AND
 RAL
 NOP
 DCA
 SEL
 CMA
 DCA
 JMS I
 JMP
 JMS I
 JMP

0625 4753

0626 7340
 0627 0102
 0630 3012
 0631 4451

PAL10	V141	11-NOV-70	24:28	PAGE 1-13
0632	RRAR,	CLA CLL CMA		
0633		AND RLNK		/SET UP TP DO REAL RAR
0634		SZA CLA		
0635		CML		
0636		CMA		
0637		AND		/DO REAL RAR
0640		RAR		
0641		NOP		/SAVE ROTATED ACCUMULATOR
0642		DCA		
0643		SZL		
0644		CMA		
0645		DCA		/SAVE ROTATED LINK
0646		JMS I		/COMPARE ROTATES
0647		JMP		/RETURN HERE FOR LOOP ON ERROR
0650		JMS I		/SET UP FOR NEXT ROTATE
0651		JMP		/CONTINUE RAR TEST
0652	SIMR03,	JMS I XR3		
4754		/TEST RTL		
0653	SIMRTL,	CLA CLL CMA		/GET MASK TABLE FOR
0654		AND XRTLTA		/SIMULATED RTL
0659		DCA POINT2		/SIMULATE RTL
0656		JMS I XSROT		
0657	RTL,	CLA CLL CMA		/SET UP TO DO REAL ROTATE
0660		AND RLNK		
0661		SZA CLA		
0662		CML		
0663		CMA		
0664		AND		/DO REAL ROTATE
0665		RTL		
0666		NOP		/SAVE ROTATED ACCUMULATOR
0667		DCA		
0670		SZL		
0671		CMA		
0672		DCA		/SAVE ROTATED LINK
0673		JMS I		/COMPARE ROTATES
0674		JMP		/RETURN HERE FOR LOOP ON ERROR
0675		JMS I		/SET UP TO DO NEXT ROTATE
0676		JMP		/CONTINUE RTL TEST
0677	SIMR04,	JMS I XR4		
4755		/TEST RTR		
0700	SIMRTR,	CLA CLL CMA		/GET MASK TABLE FOR
0701		AND XRTRTA		/SIMULATED RTR
0702		DCA POINT2		/SIMULATE RTR
0703		JMS I XSROT		
0704	RRTR,	CLA CLL CMA		

PAL16	V141	11-NOV-70	21:20	PAGE 1-12
0705	0024	AND	RLNK	
0706	7640	SEA CLA		/SET UP TO DO REAL ROTATE
0707	7020	CML		
0708	7040	CMA		
0709	0023	AND	RAC	
0710	7012	RTR		/DO REAL ROTATE
0711	7000	NOP		/SAVE ROTATED ACCUMULATOR
0712	3031	DCA	RRAC	
0713	7430	SEL		
0714	7040	CMA		
0715	3033	DCA	RRLNK	/SAVE ROTATED LINK
0716	4456	JMS I	XCOMRO	/COMPARE ROTATES
0717	5304	JMP I	RRTR	/RETURN HERE FOR LOOP ON ERROR
0718	4457	JMS I	XNXTRO	/SET UP TO DO NEXT ROTATE
0719	5300	JMP	SIMRTR	/CONTINUE RTR TEST
0720				
0721				
0722				
0723				
0724	4756	SIMROS, JMS I	XR5	
0725		/	/TEST BYTE SHAP	
0726	7340	CLA CLL CMA		/GET MASK TABLE FOR
0727	0055	AND XBSWTA		/SIMULATED BSW
0728	3012	DCA POINT2		/SIMULATE BSW
0729	4776	JMS I XSBSW		
0730	7340	CLA CLL CMA		/SET UP FOR REAL BSW
0731	0024	AND RLINK		
0732	7640	SEA CLA		
0733	7020	CML		
0734	7040	CMA		
0735	0023	AND	RAC	
0736	7002	BSW		/DO REAL BSW
0737	7000	NOP		/SAVE ROTATED ACCUMULATOR
0738	3031	DCA	RRAC	
0739	7430	SEL		
0740	7040	CMA		
0741	3033	DCA	RRLNK	/SAVE ROTATED LINK
0742	4456	JMS I	XCOMRO	/COMPARE ROTATES
0743	5331	JMP I	RRBSW	/RETURN HERE FOR LOOP ON ERROR
0744	4457	JMS I	XNXTRO	/SET UP FOR NEXT ROTATE
0745	5325	JMP	SIMBSW	/CONTINUE BSW TEST
0746	5777	JMP I	XROTND	/END OF ROTATE SIMULATION TESTS
0747				
0748				
0749				
0750				
0751				
0752	1400	XR1,		
0753	1410	XR2,		
0754	1420	XR3,		
0755	1430	XR4,		
0756	1440	XR5,		
0757	0001	RALTAB, 1		
0758	0002	2		
0759	0004	4		
0760	0010	10		
0761	0020	20		
0762	0040	40		
0763				
0764				


```

1 31 0000
1 32 7340
1 33 0024
1 34 7640
1 35 5244
1 36 7040
1 37 3024
1 40 2023
1 41 5631
1 42 2231
1 43 5631
1 44 3024
1 45 5631

NXTROT, 0
CLA CLL CMA
AND RLNK
SZA CLA NEWLNK
JMP CMA
DCA 3024
ISE 2023
JMP I NXTROT
ISE 2231
JMP I NXTROT
DCA 3024
JMP I NXTROT

NEWLNK, 0
CLA CLL CMA
AND RLNK
SZA CLA NEWLNK
JMP CMA
DCA 3024
JMP I NXTROT

```

```

/GET LINK OF WORD TO BE ROTATED
/IS IT 0
/NO, CLEAR IT
/YES, SET IT

```

```

/INCREMENT NUMBER TO BE ROTATED
/CONTINUE SIMULATION OF PRESENT ROTATE INSTRUCTION
/PRESENT SIMULATION DONE
/GO TO NEXT TEST

```

/ERROR HANDLER FOR ROTATE TEST

```

1046 7604
1047 0104
1050 7650
1051 4271
1052 7604
1053 0103
1054 7650
1055 5263
1056 7604
1057 0103
1060 7650
1061 5227
1062 5230
1063 7340
1064 0451
1065 1270
1066 7402
1067 5256
1070 7774

ERROR2, LAS
AND SR01
SNA CLA ROTPRT
JMS LAS
LAS SR00
AND CLA HALTB
JMP LAS
AND CLA SR02
SNA CLA ERROT+1
JMP ERROT+2
CLA CLL CMA
AND I XSROT
TAD M4
HLT
JMP HALTB+4
M4, -4

```

```

/TEST SR01
/IS SR01=1
/NO, TYPE ERROR MESSAGE

```

```

/TEST SR00
/IS SR00=1
/NO, HALT WITH ADDRESS OF TEST IN AC

```

```

/TEST SR02
/IS SR02=1
/NO, GO TO NEW DATA
/YES, LOOP WITH SAME DATA

```

/ERROR TYPEOUT FOR SIMULATED ROTATE TEST ERRORS

```

1071 0000
1072 7340
1073 0035
1074 7650
1075 4331
1076 7040
1077 0023
1100 3037
1101 7040

ROTPRT, 0
CLA CLL CMA
AND RNFLG
SNA CLA RHOUT
JMS CMA
AND RAC
DCA WD1
CMA

```

```

/GET ROTATE TEST HEADER FLAG
/HAS HEADER BEEN TYPED
/NO, TYPE HEADER

```

```

1102 0024          AND      RLNK
1103 3040          DCA      WD2
1104 4460          JMS I    XLNKOU
1105 4461          JMS I    XWDOUT
1106 7040          CMA
1107 0025          AND      SIMAC
1110 3037          DCA      WD1
1111 7040          CMA
1112 0026          AND      SIMLNK
1113 3040          DCA      WD2
1114 4460          JMS I    XLNKOU
1115 4461          JMS I    XWDOUT
1116 7040          CMA
1117 0031          AND      RRAC
1120 3037          DCA      WD1
1121 7040          CMA
1122 0033          AND      RRLNK
1123 3040          DCA      WD2
1124 4460          JMS I    XLNKOU
1125 4461          JMS I    XWDOUT
1126 4446          JMS I    XPRINT
1127 5542          CRLF-1
1130 5671          JMP I    ROTPRT

```

/OUTPUT ORIGINAL LINK
/OUTPUT ORIGINAL WORD

/OUTPUT SIMULATED ROTATED LINK
/OUTPUT SIMULATED ROTATED WORD

/OUTPUT ACTUAL ROTATED LINK
/OUTPUT ACTUAL ROTATED WORD

/OUTPUT HEADER FOR ROTATE ERROR MESSAGE

```

1131 0000          RHOUT,
1132 4446          JMS I    XPRINT
1133 0000          RHD,
1134 4446          JMS I    XPRINT
1135 5044          DH2-1
1136 7240          CLA CMA
1137 3035          DCA      RHFLG
1140 5731          JMP I    RHOUT

```

/TYPE SIMULATED XXX TEST FAILED
/WHERE XXX IS THE INSTRUCTION THAT FAILED
/TYPE ORIGINAL, SIMULATED ACTUAL

```

1141 2000          RRTAB, 2000
1142 0400          400
1143 0100          100
1144 0020          20
1145 0004          4
1146 0001          1
1147 4000          4000
1150 1000          1000
1151 0200          200
1152 0040          40
1153 0010          10
1154 0002          2
1155 0000          0
1156 2000          2000
1157 0002          2

```

RRTAB, 2

1234	PA110	V141	11-NOV-70	21128	PAGE 1-17	
1235					/SAVE PARTIALLY ROTATED WORD	
3025		DCA	SIMAC			
5625		JMP I	OR1			
				/SIMULATE BYTE SWAP		
1236		SBSW,			/SET UP FOR ERROR RETURN	
1237			CLA CLL CMA		/CLEAR SIMULATION ARGUMENTS	
1240			AND SBSW		/GET MASK FROM TABLE	
1241			DCA I XSROT		/IS IT 0	
1242			DCA SIMAC		/YES, FINISH SIMULATION	
1243			DCA SIMLNK			
1244		N1BIT,				
1245			AND I POINT2			
1246			SNA			
1247			JMP ENDBSW			
1250			DCA WD1			
1251			CMA			
1252			AND I POINT2			
1253			DCA WD2			
1254			CMA			
1255			AND RAC			
1256			AND WD1			
1257			SZA			
1260			JMS			
1261			CMA			
1262			AND WD1			
1263			MQL			
1264			CMA			
1265			AND WD2			
1266			DCA WD1			
1267			MQA			
1270			DCA WD2			
1271			CMA			
1272			AND RAC			
1273			AND WD1			
1274			SZA			
1275			JMS			
1276			JMP N1BIT			
1277		ENDBSW,	CLA CLL CMA			
1300			AND RLNK			
1301			DCA SIMLNK			
1302			JMP I SBSW			
				/END OF ROTATE, SHIFT LINK		
1303		ENDROT,	CLA CLL CMA			
1304			AND I POINT2			
1305			DCA WD2			
				/GET BIT TO BE ROTATED FROM LINK		

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1306 7040		CMA		
1307 0116		AND	K0001	/GET MASK FOR LINK
1308 0024		AND	RLNK	/TEST LINK
1310 7440		SZA		/IS LINK 0
1312 4225		JMS	OR1	/PLACE LINK IN NEW POSITION
1313 7040		CMA		
1314 0412		AND I	POINT2	/GET MASK FOR BIT TO BE ROTATED INTO LINK
1315 0023		AND	RAC	/TEST BIT IN WORD TO BE ROTATED INTO LINK
1316 7440		SZA		/IS IT 0
1317 7240		CLA CMA		/NO, SET LINK=1
1320 0116		AND	K0001	
1321 3026		DCA	SIMLNK	
1322 5600		JMP I	SROTAL	
1323 7604	ROTDNE,	LAS		
1324 0115		AND	SR10	/TEST SR10
1325 7650		SNA CLA		/IS SR10=1
1326 5342		JMP	SROTOK	/NO, TYPE "SIMROT"
1327 7604	ROTHLT,	LAS		
1330 0114		AND	SR09	/TEST SR09
1331 7640		SZA CLA		/IS SR09=1
1332 7402		HLT		/YES, HALT AT END OF ROTATE TESTS
1333 7604		LAS		
1334 0116		AND	SR11	/TEST SR11
1335 7650		SNA CLA		/IS SR11=1
1336 5740		JMP I	.+2	/NO, GO TO NEXT TEST
1337 5741		JMP I	.+2	/YES, REPEAT ROTATE TESTS
1340 2000		FCT		
1341 0600		SIMROT		
1342 4446	SROTOK,	JMS I	XPRINT	
1343 5525		OK2-1		
1344 5327		JMP	ROTHLT	

PAGE	R1,			
1400				
1401 7340		CLA CLL	CMA	/SET UP HEADER
1402 0250		AND	XM2	/FOR RAL TEST ERROR MESSAGE
1403 3450		DCA I	XRHD	/CLEAR ROTATE HEADER FLAG
1404 3035		DCA	RHFLG	
1405 3024		DCA	RLNK	
1406 3023		DCA	RAC	
1407 5600		JMP I	R1	
1410 0000				
1411 7340		CLA CLL	CMA	/SET UP HEADER
1412 0251		AND	XM3	/FOR RAR TEST ERROR MESSAGE
1413 3450		DCA I	XRHD	
1414 3035		DCA	RHFLG	
1415 3024		DCA	RLNK	
1416 3023		DCA	RAC	

1623	1246	TAD	M40
1624	7510	SPA	
1625	5230	JMP	*3
1626	1076	TAD	K240
1627	5243	JMP	MTP
1630	7001	IAC	
1631	7440	SZA	
1632	5235	JMP	*3
1633	1251	TAD	K215
1634	5243	JMP	MTP
1635	7001	IAC	
1636	7440	SZA	
1637	5242	JMP	*3
1640	1250	TAD	K212
1641	5243	JMP	MTP
1642	1247	TAD	K336
1643	4447	JMS I	XTYPE
1644	5617	JMP I	TYPSET
1645	0077	0077	
1646	7740	M40	
1647	0336	K336	
1650	0212	0212	
1651	0215	0215	
1652	0000	0	
1653	6046	TLS	
1654	6041	TSP	
1655	5254	JMP	-1
1656	7200	CLA	
1657	5652	JMP I	TYPE

1660	0001	BSWTAB, 1
1661	0100	100
1662	0002	2
1663	0200	200
1664	0004	4
1665	0400	400
1666	0010	10
1667	1000	1000
1670	0020	20
1671	2000	2000
1672	0040	40
1673	4000	4000
1674	0000	0

2000	PAGE	CLA CLL
7300	FCT,	TAD SEQ1
1122		DCA SEQ
3154		DCA CNTR1
3020		/
		/
		/FALSE CARRY TEST#1

	PAL10	V141	11-NOV-70	21:20	PAGE 1-22
2046	1136		TAD	INS1	/INSTRUCTION=TAD .-1
2047	3472		DCA I	XSTA2	/LOC.=7777
2050	1137		TAD	INS3	/INSTRUCTION=IAC
2051	3000		DCA	TSTA3	/LOC.=0000
2052	1141		TAD	INS5	/INSTRUCTION=JMP I .+1
2053	3001		DCA	TSTA4	/LOC.=0001
2054	1330		TAD	AD2	/ADDRESS=RET2
2055	3002		DCA	TSTA5	/LOC.=0002
			/		/EXECUTE INSTRUCTIONS PREVIOUSLY ASSEMBLED IN TEST
			/		/ADDRESSES
			/		/
2056	7300	FCL2,	CLA CLL		
2057	5472		JMP I	XSTA2	
2060	7000	RET2,	NOP		
2061	7000		NOP		
2062	4464		JMS I	XAVREG	/SAVE AC AND LINK
			/		/EXPECTED RESULTS ARE AC=0, LINK=1
			/		/
2063	7430		SZL		
2064	7440		SZA		
2065	4465		JMS I	XDATER	
2066	7410		SKP		
2067	4466		JMS I	XHALT2	
2070	4467		JMS I	XLLOOP	
2071	5256		JMP	FCL2	
2072	7200		CLA	SEQ3	
2073	1124		TAD	SEQ	
2074	3154		DCA	SEQ	
2075	5554		JMP I	SEQ	
			/		/FALSE CARRY TEST #3
			/		/
			/		/
2076	7300	FCT3,	CLA CLL		
			/		/
			/		/
			/		/
2077	1137	FCS3,	TAD	INS3	/INSTRUCTION=IAC
2100	3471		DCA I	XSTA1	/LOC.=7776
2101	1333		TAD	INS16	/INSTRUCTION=TAD I 21
2102	3472		DCA I	XSTA2	/LOC.=7777
2103	1152		TAD	INS14	/INSTRUCTION=JMP I .+1
2104	3000		DCA	TSTA3	/LOC.=0000
2105	1331		TAD	AD3	/ADDRESS=RET3
2106	3001		DCA	TSTA4	/LOC.=0001
			/		/
			/		/
			/		/
2107	7300	FCL3,	CLA CLL		
2110	5471		JMP I	XSTA1	

2111 7000 NOP
2112 7000 NOP
2113 4464 JMS I XAVREG

2114 7430 SZ=1
2115 7440 SZ=1 XDAYER
2116 4465 JMS I XHALT2
2117 7430 JMS I XLOOP
2118 4467 JMS I FOL3
2119 7007 JMS I FOL3
2120 7200 CLA
2121 4467 JMS I FOL3
2122 7200 TAC
2123 7200 DCA
2124 4467 JMS I FOL3
2125 7200 DCA
2126 7200 JMS I FOL3
2127 7200 RET1
2128 7200 RET2
2129 7200 RET3
2130 7200 RET3
2131 7200 RET3
2132 7200 RET3
2133 7200 RET3

AD1,
AD2,
AD3,
INS2,
INS16,
INS16,

/*TAD .03 IN 2000

/*FALSE CARRY TEST #4

CLA CLL

FCT4,

2200 7300

2201 7340 /DATA=7777
2202 3471 /LOC.=7776
2203 1156 /INSTRUCTION=TAD , -1
2204 3472 /LOC.=7777
2205 1142 /INSTRUCTION=CMA CML RAR
2206 3200 /LOC.=0000
2207 1141 /INSTRUCTION=JMP I , -1
2210 3001 /LOC.=0001
2211 1324 /ADDRESS=RET4
2212 3002 /LOC.=0002

2213 7340 CLA CLL CMA
2214 5472 JMS I XSTA2
2215 7000 NOP
2216 7000 NOP
2217 4464 JMS I XAVREG


```
2265 1127 TAD SEQ6
2266 3154 DCA SEQ
2267 5554 JMP I SEQ

/
/
/ FALSE CARRY TEST #6
/
/ CLA CLL
/
/ CLA CLL
/
/ FCT6,
/
/ FCS6,
/
/ INSTRUCTION=ISZ .+1
/ LOC.=7777
/ INSTRUCTION=IAC
/ LOC.=0001
/ INSTRUCTION=JMP I .+1
/ LOC.=0002
/ ADDRESS=RET6
/ LOC.=0003
```

```
2302 7340 CLA CLL CMA
2303 3000 DCA TSTA3
2304 7240 CLA CMA
2305 5472 JMP I XSTA2
2306 7000 NOP
2307 7000 NOP
2310 4464 JMS I XAVREG
```

```
2311 7430 SZL
2312 7440 SZA
2313 4465 JMS I XDATER
2314 7410 SKP
2315 4466 JMS I XHALT2
2316 4467 JMS I XLOOP
2317 5302 JMP FCL6
2320 7200 CLA
2321 1130 TAD SEQ7
2322 3154 DCA SEQ
2323 5554 JMP I SEQ
2324 2215 RET4
2325 2252 RET5
2326 2306 RET6
```

```
AD4,
AD5,
AD6,

PAGE

/
/ FALSE CARRY TEST#7
/
```

```
2400 7300  FCT7,  CLA CLL
/
/
FCS7,  CLA CLL      INS9      /INSTRUCTION=ISE I TSTIND
TAD      TAD I      XSTA2     /LOC.=7777
DCA      DCA I      INS3      /INSTRUCTION=IAC
TAD      TAD      TSTA4     /LOC.=0001
DCA      DCA      INS13     /INSTRUCTION=JMP I .+1
TAD      TAD      TSTA3     /LOC.=0002
DCA      TAD      AD7       /ADDRESS=RET7
TAD      DCA      TSTA6     /LOC.=0003
/
/
FCL7,  CLA CLL CMA      /
DCA      DCA      TSTIND
DCA      DCA      TSTA3
CMA      CMA I      XSTA2
JMP      NOP
NOP      NOP I      XAVREG
JMS      /
/
SEL
SEA
JMS I    XDATE
SKP
JMS I    XHALT2
JMS I    XLOOP
JMP      FCL7
CLA
TAD      SEQ8
DCA      SEQ
JMP I    SEQ
/
/
/ FALSE CARRY TEST #8
/
FCT8,  CLA CLL
/
/
FCS8,  CLA CLL      INS3      /INSTRUCTION=IAC
TAD      DCA      TSTA3     /LOC.=0000
DCA      TAD      INS3      /INSTRUCTION=IAC
DCA      DCA      TSTA4     /LOC.=0001
TAD      TAD      INS4      /INSTRUCTION=JMP I .+2
DCA      DCA      TSTA5     /LOC.=0002
```

1 ADDRESS
1 LOC. #004

```

/INSTRUCTION=JMS,01
/LOC.=7777

```

```

/ DATA = 7777
/ LOC. = 7776
/ INSTRUCTION = IAC
/ LOC. = 8000
/ INSTRUCTION = JMP ! 001
/ LOC. = 8001
/ ADDRESS = RET9
/ LOC. = 8002

```

/INSTRUCTION=JMS I , -1

ST-10

CLACLL

CLA	CLL	CMA
DCA	!	XSTA1
TAD		INS3
DCA		YSTA3
TAD		INS5
DCA		TSTA4
TAD		AD9
DCA		TSTA5

```

CLA CLL      INS11
TAD          XSTA2
DCA I        XSTA2
CLA CMA      XSTA2
JMP I
NOP
NOP

```

11

JMS I XAVREG

2512 4464

2513 7430
 2514 7440
 2515 4465
 2516 7410
 2517 4466
 2520 4467
 2521 5303
 2522 7200
 2523 1133
 2524 3154
 2525 5554
 2526 2420
 2527 2455
 2530 2510

AD7,
 AD8,
 AD9,

PAGE

2600

/FALSE CARRY TEST #10

CLA CLL

FCT10,

2600 7300

CLA CLL
 TAD INS12
 DCA I XSTA2
 TAD INS3
 DCA TSTA4

FCS10,

2601 7300
 2602 1150
 2603 3472
 2604 1137
 2605 3001

/INSTRUCTION=JMS I TSTIND
 /LOC.=7777
 /INSTRUCTION=IAC
 /LOC.=0001

/INSTRUCTION=JMP I .+1
 /LOC.=0002
 /ADDRESS=RET10
 /LOC.=0003

TAD INS13
 DCA TSTA5
 TAD AD10
 DCA TSTA6

2606 1151
 2607 3002
 2610 1315
 2611 3003

CLA CLL CMA
 DCA TSTIND
 CMA
 JMP I XSTA2
 NOP
 NOP

FCL10,

2612 7340
 2613 3010
 2614 7040
 2615 5472
 2616 7000
 2617 7000
 2620 4464

JMS I XAVREG

RET10,

2621 7430

SZL

```
2622 7440      SZL
2623 4455      JMS I  XDATER
2624 7410      SKP
2625 4456      JMS I  XHALT2
2626 4467      JMS I  XLOOP
2627 5212      JMP  FCL10
2630 7200      CLA      SEQ11
2631 1134      TAD
2632 3154      DCA      SEQ
2633 5554      JMP I  SEQ

/
/
/ FALSE CARRY TEST #11
/
2634 7300      FCT11,  CLA CLL
/
/
/
2635 7300      PCS11,  CLA CLL
2636 1137      TAD      INS3
2637 3000      DCA      TSTAS
2640 1141      TAD      INS5
2641 3001      DCA      TSTA4
2642 1316      TAD      AD11
2643 3002      DCA      TSTA5
/
/
/
2644 7300      FCL11,  CLA CLL
2645 1153      TAD      INS19
2646 3472      DCA I  XSTA2
2647 7240      CLA CMA
2650 5472      JMP I  XSTA2
2651 7000      NOP
2652 7000      NOP
2653 4464      JMS I  XAVREG
/
/
/
2654 7430      SZL
2655 7440      SZL
2656 4465      JMS I  XDATER
2657 7410      SKP
2660 4466      JMS I  XHALT2
2661 4467      JMS I  XLOOP
2662 5244      JMP  FCL11
2663 7200      CLA      SEQ12
2664 1135      TAD
2665 3154      DCA      SEQ
2666 5554      JMP I  SEQ

/
/
/ INSTRUCTION=IAC
/ LOC.=0000
/ INSTRUCTION=JMP I ,01
/ ADDRESS=0001
/ ADDRESS=RET11
/ LOC.=0002

/ INSTRUCTION=JMS .
/ LOC.=7777
```

/FALSE CARRY TEST #12

CLA CLL

FCT12,

2667 7300

CLA CLL

FCS12,

2670 7300

TAD I

2671 1137

DCA I

2672 3472

TAD

2673 1152

DCA

2674 3000

TAD

2675 1317

DCA

2676 3001

/INSTRUCTION=IAC
/LOC.=7777
/INSTRUCTION=JMP I .+1
/LOC.=0000
/ADDRESS=RET12
/LOC.=0001

CLA CLL CMA

FCL12,

2677 7340

JMP I

2678 5472

NOP

RET12,

2701 7000

NOP

2702 7000

JMS I

2703 4464

XAVREG

SZL

2704 7430

SEA

2705 7440

JMS I

2706 4465

SKP

2707 7410

JMS I

2710 4466

JMS I

2711 4467

JMP

2712 5277

JMP I

2713 5714

ENDFCT

2714 3200

RET10

2715 2616

RET11

2716 2651

RET12

2717 2701

AD10,
AD11,
AD12,

PAGE

3000

/(TAPE 3)

/COMPARE TWO NUMBERS: W1*NOT(W2)+W2*NOT(W1)=0, W1=W2

SAMEAS,

3000 0000

CLA CLL CMA

3001 7340

AND W2

3002 0040

CMA

3003 7040

AND W1

3004 0037

SZA CLA

3005 7640

JMP I

3006 5600

SAMEAS

3007 7040

CMA

3010 0037

/W1*NOT(W2)=0
/W1*NOT(W2)NOT 0, ERROR

PAL10	V141	11-NOV-70	128	PAGE 4
3055	5646	JMP I LOOP		
		/TYPE DATA ERROR MESSAGE		
3056	0000	TYP2,		
3057	4446	JMS I XPRINT		/TYPE "DATA ERROR"
3060	5544	DATE-1		/TYPE TEST ADDRESS
3061	1037	TAD W1		
3062	4673	JMS I XADOUT		
3063	7340	CLA CLL CMA		
3064	0025	AND TEMPAC		
3065	3037	DCA WD1		
3066	0026	AND TEMPL		
3067	3040	DCA WD2		
3070	4460	JMS I XLNKOU		/OUTPUT RECEIVED LINK
3071	4461	JMS I XMDOUT		/OUTPUT RECEIVED AC
3072	5656	JMP I TYP2		
3073	3227	XADOUT, ADOUT		
		/END OF PASS		
		/		
3200	7300	PAGE		
3201	2020	ENDFCT, CLA CLL CNTR1		/INCREMENT PASS COUNT
3202	5224	JMP OUT		/PASS NOT COMPLETE
3203	7604	LAS		
3204	0115	AND SR10		/TEST SR10
3205	7650	SNA CLA		/IS SR10=1
3206	5221	JMP FCTOK		/NO, TYPE FCT
3207	7604	LAS		
3210	0114	AND SR09		/TEST SR09
3211	7640	SZA CLA		/IS SR09=1
3212	7402	HLT		/YES, HALT
3213	7604	LAS		
3214	0116	AND SR11		/TEST SR11
3215	7640	SZA CLA		/IS SR11=1
3216	5224	JMP OUT		/YES, LOOP ON FCT
3217	5620	JMP I .+1		/NO, GO TO NEXT TEST
3220	3400	RNAD1		
3221	4446	JMS I XPRINT		
3222	5532	OK3-1		
3223	5207	JMP FCTHLT		
3224	1122	TAD SEQ1		
3225	3154	DCA SEQ		
3226	5554	JMP I SEQ		
		/		
		/CONVERT ADDRESS TO ASCII AND OUTPUT		
		/		
3227	0000	ADOUT,		
3230	3037	DCA		TEMP1
3231	1037	TAD		TEMP1

```

3232 0266 AND K0007
3233 3264 A2
3234 1037 TAD TEMP1
3235 7006 RTL
3236 7004 RAL
3237 0267 AND K0700
3240 1264 TAD A2
3241 1270 TAD K6060
3242 3264 A2
3243 1037 TAD TEMP1
3244 7012 RTR
3245 7012 RTR
3246 7012 RTR
3247 0266 AND K0007
3250 3263 DCA A1
3251 1037 TAD TEMP1
3252 7012 RTR
3253 7010 RAR
3254 0267 AND K0700
3255 1263 TAD A1
3256 1270 TAD K6060
3257 3263 DCA A1
3260 4446 JMS I XPRINT
3261 3262 A1-1
3262 5627 JMP I ADOUT
3263 0000 A1, 0
3264 0000 A2, 0
3265 4000 4000
3266 0007 K0007, 0007
3267 0700 K0700, 0700
3270 6060 K6060, 6060
    
```

/MULTIPLE ADDITIONS OF RANDOM NUMBER AND ITS TWO'S COMPLEMENT

```

PAGE PAGE
RNAD1, RNAD1,
3400 3400 CLA CLL
3401 7300 JMS I XRAND
3402 7300 CLA CLL
3403 1041 TAD RANDA
3404 1043 TAD RANDC
3405 1043 TAD RANDC
3406 1041 TAD RANDA
3407 1041 TAD RANDA
3410 1041 TAD RANDC
3411 1043 TAD RANDC
3412 1043 TAD RANDA
3413 1041 TAD RANDA
3414 1041 TAD RANDC
3415 1043 TAD RANDC
3416 1041 TAD RANDA
3417 1043 TAD RANDC
3420 1043 TAD RANDC
3421 1041 TAD RANDA
3422 1041 TAD RANDA
3423 1043 TAD RANDC
3424 1043 TAD RANDC
3425 1043 TAD RANDC
3426 1043 TAD RANDC
3427 1043 TAD RANDC
3428 1043 TAD RANDC
3429 1043 TAD RANDC
3430 1043 TAD RANDC
3431 1043 TAD RANDC
3432 1043 TAD RANDC
3433 1043 TAD RANDC
3434 1043 TAD RANDC
3435 1043 TAD RANDC
3436 1043 TAD RANDC
3437 1043 TAD RANDC
3438 1043 TAD RANDC
3439 1043 TAD RANDC
3440 1043 TAD RANDC
3441 1043 TAD RANDC
3442 1043 TAD RANDC
3443 1043 TAD RANDC
3444 1043 TAD RANDC
3445 1043 TAD RANDC
3446 1043 TAD RANDC
3447 1043 TAD RANDC
3448 1043 TAD RANDC
3449 1043 TAD RANDC
3450 1043 TAD RANDC
3451 1043 TAD RANDC
3452 1043 TAD RANDC
3453 1043 TAD RANDC
3454 1043 TAD RANDC
3455 1043 TAD RANDC
3456 1043 TAD RANDC
3457 1043 TAD RANDC
3458 1043 TAD RANDC
3459 1043 TAD RANDC
3460 1043 TAD RANDC
3461 1043 TAD RANDC
3462 1043 TAD RANDC
3463 1043 TAD RANDC
3464 1043 TAD RANDC
3465 1043 TAD RANDC
3466 1043 TAD RANDC
3467 1043 TAD RANDC
3468 1043 TAD RANDC
3469 1043 TAD RANDC
3470 1043 TAD RANDC
3471 1043 TAD RANDC
3472 1043 TAD RANDC
3473 1043 TAD RANDC
3474 1043 TAD RANDC
3475 1043 TAD RANDC
3476 1043 TAD RANDC
3477 1043 TAD RANDC
3478 1043 TAD RANDC
3479 1043 TAD RANDC
3480 1043 TAD RANDC
3481 1043 TAD RANDC
3482 1043 TAD RANDC
3483 1043 TAD RANDC
3484 1043 TAD RANDC
3485 1043 TAD RANDC
3486 1043 TAD RANDC
3487 1043 TAD RANDC
3488 1043 TAD RANDC
3489 1043 TAD RANDC
3490 1043 TAD RANDC
3491 1043 TAD RANDC
3492 1043 TAD RANDC
3493 1043 TAD RANDC
3494 1043 TAD RANDC
3495 1043 TAD RANDC
3496 1043 TAD RANDC
3497 1043 TAD RANDC
3498 1043 TAD RANDC
3499 1043 TAD RANDC
3500 1043 TAD RANDC
    
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PAL10	V141	11-NOV-70	21:20	PAGE 1-34
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3425	1043	TAD	RANDC	
3426	1041	TAD	RANDA	
3427	1043	TAD	RANDC	/AC=0
3430	1041	TAD	RANDA	
3431	1041	TAD	RANDA	
3432	1041	TAD	RANDA	
3433	1043	TAD	RANDC	/AC=0
3434	1043	TAD	RANDC	
3435	7000	NOP		
3436	4464	JMS I	XAVREG	/SAVE AC AND LINK
3437	7430	SEL		/IS LINK=1, AND AC=0
3440	7440	SZA		
3441	4646	JMS I	XRN1ER	/ERROR, AC NOT 0, OR LINK NOT 1 OR BOTH
3442	4667	JMS I	NERROP	/RESULTS OK
3443	5202	JMP	RNAD1+2	
3444	5645	JMP I	.+1	
3445	3600	RNAD2		
3446	3447	XRN1ER, RN1ER		

3447	0000	RN1ER,		/RANDOM ADD TEST 1 ERROR HANDLER
3450	7604	LAS		
3451	0104	AND	SR01	/TEST SR01
3452	7640	SZA CLA		/IS SR01=1
3453	5302	JMP	SKHLT	/YES, SUPPRESS ERROR TYPEOUT
3454	4446	JMS I	XPRINT	/TYPE "RANDOM ADD TEST1 FAILED"
3455	5365	EM10-1		
3456	4446	JMS I	XPRINT	/TYPE "RANDA, RANDC, RESULT"
3457	5116	DH4-1		
3460	7340	CLA CLL	CMA	
3461	0041	AND	RANDA	
3462	3037	DCA	WD1	/OUTPUT RANDA
3463	4461	JMS I	XWDOUT	
3464	7340	CLA CLL	CMA	
3465	0043	AND	RANDC	
3466	3037	DCA	WD1	/OUTPUT RANDC
3467	4461	JMS I	XWDOUT	
3470	7340	CLA CLL	CMA	
3471	0025	AND	TEMPAC	
3472	3037	DCA	WD1	
3473	7040	CMA		
3474	0026	AND	TEMPL	
3475	3040	DCA	WD2	
3476	4460	JMS I	XLNKOU	/OUTPUT RESULTANT LINK
3477	4461	JMS I	XWDOUT	/OUTPUT RESULTANT AC
3500	4446	JMS I	XPRINT	
3501	5542	CRLF-1		
3502	7604	LAS		/TEST SR00
3503	0103	AND	SR00	/IS SR00=1
3504	7640	SZA CLA		/YES, SUPPRESS ERROR HALT
3505	5647	JMP I	RN1ER	

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3613 0352	AND	MASK		
3614 7240	CMA			/COMPLIMENT MASK
3615 3353	DCA	NMASK		
3616 7040	CMA			/GET RANDOM NUMBER
3617 0346	AND	APOS		/TEST SIGN BIT
3620 0352	AND	MASK		/IS NUMBER NEGATIVE
3621 7440	SZA			/YES, MODIFY COMPLIMENT OF NUMBER
3622 5232	JMP	MODNEG		
3623 7040	CMA			/GET RANDOM NUMBER
3624 0346	AND	APOS		/MODIFY WITH MASK
3625 4301	JMS	XOR1		
3626 7040	CMA			
3627 0347	AND	ANEG		/GET COMPLIMENT OF RANDOM NUMBER
3630 3351	DCA	BNEG		/AND USE AS IS
3631 5240	JMP	CBTST1		
3632 7240	CMA	CLA		/MODIFY NEGATIVE NUMBER
3633 0347	AND	ANEG		/GET COMPLIMENT OF RANDOM NUMBER
3634 4315	JMS	XOR2		/MODIFY WITH MASK
3635 7040	CMA			
3636 0346	AND	APOS		/GET RANDOM NUMBER
3637 3351	DCA	BNEG		/AND USE AS IS
3640 7340	CLA	CLL		/LOAD AC WITH MODIFIED ARGUMENT
0350	AND	BPOS		/ADD UNMODIFIED ARGUMENT
3641	TAD	BNEG		/DID CARRY PROPAGATE INTO LINK
3642 1351	SZL			/NO, INCREMENT NUMBER
3643 7430	IAC			/SAVE AC
3644 7001	JMS	I	XAVREG	/COMPARE MODIFIED BIT AND MASK
3645 4464	JMS	I	XAMEA	
3646 4463	SKP			/AC AND MASK DIFFERENT, ERROR
3647 7410	JMS	I	XRN2ER	/NO ERROR, AC AND MASK THE SAME
4756	JMS	I	NERROP	/RETURN HERE FOR LOOPING
3651 4467	JMP	CBTST1		
3652 5240	JMP	CBTST2		
3653 5254				
3654 7340	CBTST2,	CLL	CLA	CMA
0351	AND	BNEG		/LOAD AC WITH UNMODIFIED ARGUMENT
1350	TAD	BPOS		/ADD MODIFIED ARGUMENT
3657 7430	SZL			/DID CARRY PROPAGATE INTO LINK
3660 7001	IAC			/NO, INCREMENT NUMBER
3661 4464	JMS	I	XAVREG	/SAVE AC
3662 4463	JMS	I	XAMEA	/COMPARE AC AND MASK
3663 7410	SKP			
3664 4756	JMS	I	XRN2ER	/AC AND MASK NOT THE SAME, ERROR
3665 4467	JMS	I	NERROP	/NOERROR, AC AND MASK THE SAME
5254	JMP	CBTST2		/RETURN HERE FOR LOOPING
3666				/SHIFT MASK ONE PLACE TO RIGHT
3667 7340	MOVMSK,	CLA	CLL	CMA
0352	AND	MASK		
3671 7010	RAR			
3672 3352	DCA	MASK		
3673 7420	SNL			/HAVE ALL BITS BEEN TESTED
3674 5212	JMP	XTRBT		/NO, CONTINUE
3675 4467	JMS	I	NERROP	/YES, TEST FOR LOOP ON RNAD2
3676 5200	JMP	RNAD2		

```

3677 5700 JMP I .+1
3700 4200 RARR
3721 0000 2
3722 2353 AND MASK
3723 7040 CMA
3724 3354 DCA ABNOT
3725 7040 CMA
3726 0347 AND ANEG
3727 0352 AND MASK
3710 7040 CMA
3711 0354 AND ABNOT
3712 7040 CMA
3713 3350 DCA BPOS
3714 5701 JMP I XOR1
3715 0000 0
3716 0352 AND MASK
3717 7040 CMA
3720 3354 DCA ABNOT
3721 7040 CMA
3722 0346 AND APOS
3723 0353 AND NMASK
3724 7040 CMA
3725 0354 AND ABNOT
3726 3350 DCA BPOS
3727 5715 JMP I XOR2

```

```

3730 0000 SAMEA, 2
3731 7040 CMA
3732 3355 DCA NOTAC
3733 7040 CMA
3734 0025 AND TEMPAC
3735 0353 AND NMASK
3736 7440 SZA
3737 5344 JMP EROUT1
3740 7040 CMA
3741 0352 AND MASK
3742 0355 AND NOTAC
3743 7440 SZA
3744 2330 EROUT1, ISZ
3745 5730 JMP I
3746 0000 APOS, 2
3747 0000 ANEG, 2
3750 0000 BPOS, 2
3751 0000 BNEG, 2
3752 0000 MASK, 2
3753 0000 NMASK, 2
3754 0000 ABNOT, 2
3755 0000 NOTAC, 2
3756 4000 XRN2ER, RA2ER

```

```

4000 PAGE
/ERROR HANDLER FOR RANDOM ADD TEST 2.
/
RN2ER, 0
0000 LAS
4001 AND SR01
4002 SZA CLA
4003 JMP SHLT
4004 JMS I XPRINT
4005 EM11-1
4006 JMS I XPRINT
4007 JMS I XPRINT
4008 DH6-1
4009 CLA CLL
4010 AND BPOS
4011 DCA WD1
4012 JMS I XWDOUT
4013 CMA
4014 BNEG
4015 AND WD1
4016 JMS I XWDOUT
4017 CMA
4018 AND MASK
4019 JMS I XWDOUT
4020 DCA WD1
4021 JMS I XWDOUT
4022 CMA
4023 AND TEMPAC
4024 DCA WD1
4025 JMS I XWDOUT
4026 CMA
4027 AND
4028 DCA WD1
4029 JMS I XPRINT
4030 CRLF-1
4031 LAS
4032 AND SR00
4033 SZA CLA
4034 JMP I RN2ER
4035 CLA CLL
4036 TAD
4037 HLT
4038 JMP I RN2ER
4039
4040
4041
4042
/TEST SR00
/IS SR00 = 1
/YES SUPPRESS ERROR TYPEOUT
/NO, TYPE "RANDOM ADD TEST 2 FAILED"
/TYPE ARG1, ARG2, ARG1+ARG2, EXPECTED
/OUTPUT ARG1
/OUTPUT ARG2
/OUTPUT EXPECTED RESULT
/OUTPUT RESULTANT AC
/TEST SR00
/IS SR00 = 1
/YES, DO NOT HALT
/NO, HALT WITH ADDRESS IN AC

```

```

/ROTATE RANDOM NUMBER RIGHT USING RAR

```

```

4175 3752
4176 3751
4177 3750
4200 7300
4201 1044
4202 7440
4203 7220
4204 1041
PAGE
RARR,
CLA CLL
TAD LINKR
SZA
CLA CML
TAD RANDA
/GET LINK TO BE ROTATED
/GET NUMBER TO BE ROTATED

```


4271	7004	RAL		
4272	7004	RAL		
4273	7004	RAL		
4274	7004	RAL		
4275	7004	RAL		
4276	7004	RAL		
4277	7004	RAL		
4300	7004	RAL		
4301	7004	RAL		
4302	7004	RAL		
4303	7004	RAL		
4304	7004	RAL		
4305	7004	RAL		
4306	7004	RAL		
4307	7004	RAL		
4310	7004	RAL		
4311	7004	RAL		
4312	7004	RAL		
4313	7004	RAL		
4314	7000	NOP		
4315	7000	NOP		
4316	4464	JMS I		
4317	1043	TAD		
4320	7440	SEA		
4321	5325	JMP		
4322	1044	TAD		
4323	3037	DCA		
4324	1026	TAD		
4325	3040	DCA		
4326	4462	JMS I		
4327	4734	JMS I		
4330	4467	JMS I		
4331	5255	JMP I		
4332	5733	JMP I		
4333	4400	RTL		
4334	4613	XRALR,		
4335	4600	XRARR,		

/SAVE AC AND LINK
/ADD COMPLIMENT OF ORIGINAL NUMBER TO AC
/ARE THEY THE SAME
/NO, ERROR

/COMPARE ORIGINAL AND ROTATED LINKS
/LINKS NOT THE SAME, ERROR

/ROTATE RANDOM NUMBER LEFT USING RTL

PAGE	RTL	CLA CLL	LINKR	/GET LINK TO BE ROTATED
4400				
4401	7300	TAD		
4402	1044	SEA		
4403	7440			
4404	7220	CLA CML		
4405	1041	TAD		
4406	7006	RTL		
4407	7006	RTL		
4410	7006	RTL		
4411	7006	RTL		
4412	7006	RTL		
4413	7006	RTL		
4414	7006	RTL		

/GET NUMBER TO BE ROTATED

4415	RTL		
4416	RTL		
4417	RTL		
4420	RTL		
4421	RTL		
4422	RTL		
4423	RTL		
4424	RTL		
4425	RTL		
4426	RTL		
4427	RTL		
4430	RTL		
4431	RTL		
4432	RTL		
4433	RTL		
4434	RTL		
4435	RTL		
4436	RTL		
4437	NOP		
4440	NOP		
4441	JMS I	XAVREG	/SAVE AC AND LINK
4442	TAD	RANDC	/ADD COMPLEMENT OF ORIGINAL NUMBER TO AC
4443	SEA		/ARE THEY THE SAME
4444	JMP		/NO, ERROR
4445	TAD		
4446	DCA	LINKR	
4447	TAD	WD1	
4450	DCA	TEMPL	
4451	JMS I	WD2	
4452	JMS I	XAMEAS	/COMPARE ORIGINAL AND ROTATED LINKS
4453	JMS I	XRTL	/LINKS NOT THE SAME, ERROR
4454	JMP	NERROP	
		RTL	

/ROTATE RANDOM NUMBER RIGHT USING RTR

4455	RTTR,	CLA CLL	
4456		TAD	LINKR
4457		SEA	
4460		CLA CML	
4461		TAD	RANDA
4462		RTR	
4463		RTR	
4464		RTR	
4465		RTR	
4466		RTR	
4467		RTR	
4470		RTR	
4471		RTR	
4472		RTR	
4473		RTR	
4474		RTR	
4475		RTR	
4476		RTR	
4477		RTR	

PAL10

4591	7504	LAS	
4602	2104	AND	SR01
4623	7640	SEA	CLA
4624	5210	JMP	.+4
4625	4446	JMS	I XPRINT
4626	5425	EM12-1	
4627	0204	JMS	ROPRT
4610	7300	CLA	CLL
4611	1200	TAD	RALER
4612	5253	JMP	ROHLT

4613	0000	0	
4614	7604	LAS	
4615	0104	AND	SR01
4616	7640	SEA	CLA
4617	5223	JMP	.+4
4620	4446	JMS	I XPRINT
4621	5444	EM13-1	
4622	4264	JMS	ROPRT
4623	7300	CLA	CLL
4624	1213	TAD	RALER
4625	5253	JMP	ROHLT
4626	0000	0	
4627	7604	LAS	
4630	0104	AND	SR01
4631	7640	SEA	CLA
4632	5236	JMP	.+4
4633	4446	JMS	I XPRINT
4634	5463	EM14-1	
4635	4264	JMS	ROPRT
4636	7300	CLA	CLL
4637	1226	TAD	RTLER
4640	5253	JMP	ROHLT
4641	0000	0	
4642	7604	LAS	
4643	0104	AND	SR01
4644	7640	SEA	CLA
4645	5251	JMP	.+4
4646	4446	JMS	I XPRINT
4647	5502	EM15-1	
4650	4264	JMS	ROPRT
4651	7300	CLA	CLL
4652	1241	TAD	RTLER
4653	3263	DCA	ROBACK
4654	7604	LAS	
4655	0103	AND	SR00
4656	7640	SEA	CLA
4657	5262	JMP	.+3
4660	1263	TAD	ROBACK
4661	7402	HLT	
4662	5663	JMP	I ROBACK
4663	3200	0	

RTLER,

RTLER,

ROHLT,

ROBACK,

4664 0000 ROPRT, 0 JMS I XPRINT
 4665 4446 JMS I XPRINT
 4666 5147 DH5-1
 4667 7340 CLA CLL CMA
 4670 0044 AND LINKR
 4671 3040 DCA WD2
 4672 7040 CMA
 4673 0041 AND RANDA
 4674 3037 DCA WD1
 4675 4460 JMS I XLNKOU
 4676 4461 JMS I XWDOUT
 4677 7040 CMA
 4700 0026 AND TEMPL
 4701 3040 DCA WD2
 4702 4460 JMS I XLNKOU
 4703 7040 CMA
 4704 0025 AND TEMPAC
 4705 3037 DCA WD1
 4706 4461 JMS I XWDOUT
 4707 4446 JMS I XPRINT
 4710 5542 CRLF-1
 4711 5664 JMP I ROPRT

PAGE DH1, TEXT /-+ ARG1 ARG2 SIMULATED ARG1+ARG2 ARG2+ARG1-+/
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DH5, TEXT /00ORIGINAL ACTUAL00/

DH6, TEXT /00 EXPECTED ACTUAL00/

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5215	0114				
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5235	0601				
5236	1114				
5237	0504				
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5270	0124				
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5273	0122				
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5276	2440				
5277	0601				

EM1, TEXT /66 SIMULATED ADD TEST FAILED/

EM2, TEXT /66 SIMULATED RAL TEST FAILED/

EM3, TEXT /66 SIMULATED RAR TEST FAILED/

PAL10	V141	11-NOV-70	22:29	PAGE 1-48
5320	1114			
5301	0504			
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EM5,	TEXT	/..		SIMULATED RTR TEST FAILED/
EM6,	TEXT	/..		SIMULATED BSW TEST FAILED/
EM10,	TEXT	/..		RANDOM ADD TEST 1 FAILED/

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EM11, TEXT /00 RANDOM ADD TEST 2 FAILED/

EM12, TEXT /00 RANDOM RAR TEST FAILED/

EM13, TEXT /00 RANDOM RAL TEST FAILED/

11-NOV-70

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6469 0400
6470 3736
6471 4040
6472 4022
6473 0116
6474 0417
6475 1540
6476 2224
6477 1440
6478 2405
6479 2324
6480 4006
6481 0111
6482 1405
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6484 3736
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6501 0116
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6518 2224
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6560 2224
6561 1440
6562 2405
6563 2324
6564 4006
6565 0111
6566 1405
6567 040

PAL10 11~NOV-70 21120 PAGE 1-51

5545 5736 DATE, TEXT / DATA ERROR-9/

5546 4004
5547 0124
5550 0140
5551 0522
5552 2217
5553 2237
5554 3600

/ /
/ RESTORE BINARY LOADER AND START LOADER
/

07600

7600 7300
7601 1155
7602 3377
7603 5377

CLA CLL
TAD BIN
DCA YSTA2
JMP TSTA2
S

6200	11111111	11111111	11111111	11111111	11100000	00000000	00000000	00000000	00000000
6400	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
6200	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
6300	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
6400	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
6500	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
6600	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
6700	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7000	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7100	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7200	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7300	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7400	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7500	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111	11111111
7600	11110000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000
7700	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000

(((((

A1	3263	EM1	5220	FCT3	2076	K4000	0103
A10.42	0027	EM10	5366	FCT4	2200	K6000	0101
A2	3264	EM11	5406	FCT5	2232	K6060	0270
ABNOV	3754	EM12	5426	FCT6	2270	KXXXX	0170
AD1	2127	EM13	5445	FCT7	2400	LINK1	0032
AD10	2715	EM14	5464	FCT8	2436	LINK2	0034
AD41	2716	EM15	5503	FCT9	2472	LINKR	0044
AD12	2717	EM2	5521	FCTHLT	2207	LINKRG	0045
AD2	2130	EM3	3262	FCTOK	3221	LNKOUT	0504
AD3	2131	EM4	3303	GOTEST	0177	LOOP	5046
AD4	2324	EM5	5324	HALT2	3027	LOOP1	0552
AD5	2325	EM6	5345	HALTA	0477	H4	1070
AD6	2326	ENCAR	0245	HALTB	1063	H40	1046
AD7	2526	ENCAR1	0252	HLTA	0004	MASK	3752
AD8	2527	ENDBSW	1277	HLTB	1052	MODNEG	3632
AD9	2530	ENDFCI	3200	INS1	0136	MOVNSK	3667
ADA1	0021	ENDROT	1303	INS10	0146	MOA	7501
ADA2	0022	ENRN	4555	INS11	0147	HQL	7421
ADD	0273	ERROR1	3744	INS12	0150	MTP	1643
ADDERR	0400	ERROR11	0377	INS13	0151	NIBIT	1244
ADHLT	0556	ERROR2	1046	INS14	0152	NBIT	1207
ADOUT	3227	ERROT	1026	INS15	0153	NERROP	0067
ADPRT	0417	FCL1	2023	INS16	2133	NEHLNK	1044
ADT	0551	FCL10	2612	INS2	2132	NLOOP	3054
AMFLG	0035	FCL11	2644	INS3	0137	NMASK	3753
AMOUT	0467	FCL12	2677	INS4	0140	NOTAG	3755
ALT1BT	3616	FCL2	2096	INS5	0141	NXBIT	0527
ANEG	3747	FCL3	2107	INS6	0142	NXTADD	0364
APOS	3746	FCL4	2213	INS7	0143	NXTBT	3612
ARG1	0023	FCL5	2246	INS8	0144	NXTCAR	0233
ARG2	0024	FCL6	2302	INS9	0145	NXTROT	1031
GIN	0155	FCL7	2412	K0001	0116	OK1	5522
ENEG	3751	FCL8	2450	K0002	0115	OK2	5526
EPDS	3750	FCL9	2503	K0003	3542	OK3	5533
BSW	7002	FCS1	2005	K0004	0114	OK4	5536
BSWTAB	1660	FCS10	2601	K0007	3266	OR1	1225
CAF	6007	FCS11	2635	K0010	0113	OUT	3224
CARRY	0030	FCS12	2670	K0020	0112	OUT1	0520
CBTST1	3640	FCS2	2044	K0040	0111	OUT1A	0542
CBTST2	3654	FCS3	2077	K0077	1645	POINT1	0011
CH44	0036	FCS4	2201	K0100	0110	POINT2	0012
CMRST	0020	FCS5	2233	K0200	0107	PRINT	1600
CMRST1	1000	FCS6	2271	K0400	0106	R1	1400
CMRST2	5543	FCS7	2401	K0700	3267	R2	1410
CMRST3	5545	FCS8	2437	K1000	0105	R2A	3541
CMRST4	3037	FCS9	2473	K2000	0104	R3	1420
DATER	5000	FCT	2000	K212	1650	R4	1430
DH1	5045	FCT1	2004	K215	1651	R5	1440
DH2	5074	FCT10	2600	K240	0076	RAC	0023
DH3	5117	FCT11	2634	K260	0077	RALER	4613
DH4	5150	FCT12	2667	K261	0100	RALR	4255
DH5	5165	FCT2	2043	K336	1647	RALTAB	0757

9AL10	11-NOV-70	21128	PAGE 1-55
0041	SSW	1236	TSTA1 7776
0242	SEC	0154	TSTA2 7777
0243	SEC1	0122	TSTA3 0000
3512	SEC10	0133	TSTA4 0001
4600	SEC11	0134	TSTA5 0002
4200	SEC12	0135	TSTA6 0003
2731	SEC2	0123	TSTA7 0004
2025	SEC3	0124	TSTIND 0010
2616	SEC4	0125	TYBIT 0540
2651	SEC5	0126	TYLNK 0513
2701	SEC6	0127	TYPE 1692
2060	SEC7	0130	TYP52 3096
2111	SEC8	0131	TYPSET 1617
2215	SEC9	0132	W1 0037
2252	SHLT	0033	W2 0040
2306	SIMAC	0025	WD1 0037
2420	SIMAD	0203	WD2 0040
2455	SIMBSW	0725	WDOUT 0523
2510	SIMLNK	0026	XADD 0415
1133	SIMR	0577	XADOUT 3073
0935	SIMRAL	0601	XAMEA 0063
1131	SIMRAR	0626	XAMEAS 0002
0824	SIMRO1	0600	XAVREG 0064
3447	SIMRO2	0625	XBSHTA 0055
4000	SIMRO3	0652	XCOMRO 0056
3400	SIMRO4	0677	XDATER 0065
3600	SIMRO5	0724	XHALT2 0066
4540	SIMRTL	0653	XLNKOU 0060
4552	SIMRTR	0700	XL00P 0067
4663	SKHLT	3502	XL00P1 0075
4653	SP1	0545	XL00P2 0074
4664	SR00	0103	XM2 1450
1323	SR01	0104	XM3 1451
1327	SR02	0105	XM4 1452
1271	SR03	0106	XM5 1453
0031	SR04	0107	XM6 1454
0605	SR05	0110	XNXTAD 0416
0632	SR06	0111	XNXTRO 0057
0033	SR07	0112	XOR1 3701
0657	SR08	0113	XOR2 3715
0704	SR09	0114	XORALL 0257
0200	SR10	0115	XPRINT 0046
4641	SR11	0116	XR1 0792
4400	SROTAL	1200	XR2 0753
1160	SROTOK	1342	XR3 0754
4626	START	0156	XR4 0755
4455	SUM1	0031	XR5 0756
1141	SUM2	2033	XRALR 4334
2570	TEMP1	0037	XRALTA 0052
3730	TEMPAC	0025	XRAND 0073
3000	TEMPL	0026	XRARR 4335
3017	TSTA0	7775	XRARTA 0102

XRHD	0090
XRN1ER	3446
XRN2ER	3756
XROYDN	0777
XRTLR	4560
XRTLTA	0093
XRTRO	4597
XRTHTA	0094
XSDSH	0776
XSR0T	0091
XSTA0	0070
XSTA1	0071
XSTA2	0072
XTYPE	0047
XHDOUT	0061

ERRORS DETECTED: 0

LINKS GENERATED: 3

RUN-TIME: 17 SECONDS

3K CORE USED

