

SNS 0=1
RSW: 0001

8-MODE NO PRESET START 200

KF128

59 SECONDS / PAGE

IDENTIFICATION
MAINDEC-12-DBSA-D
KF128 (AUTOMATIC PRIORITY INTERRUPT)
4-5-71
DIAGNOSTIC GROUP
WALTER MANTER

PRODUCT CODE:
PRODUCT NAME:
DATE CREATED:
MAINTAINER:
AUTHOR:

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/1. ABC RACT

THE AUTOMATIC PRIORITY INTERRUPT SYSTEM (KF128 VERSION) DIAGNOSTIC IS DESIGNED TO TEST ALL ASSOCIATED IOT'S, INSTRUCTIONS AND SIMULATE AUTOMATIC PRIORITY INTERRUPTS (A.P.I.) VIA MAINTENANCE IOT'S. THIS PROGRAM REQUIRES NO TELETYPE COMMUNICATIONS AS ERROR HALTS ARE USED EXCLUSIVELY. THE ASSUMPTION IS MADE THAT THE PDP-12 USED IN CONJUNCTION WITH THE API SYSTEM IS A SOLID, ERROR FREE MACHINE.

/2. REQUIREMENTS

/2.1 EQUIPMENT

1. A PDP-12 WITH THE KF128 AUTOMATIC PRIORITY INTERRUPT OPTION.

2. AN ASR-33 TELETYPE OR EQUIVALENT

/2.2 STORAGE

THIS PROGRAM IS DESIGNED TO RUN IN MEMORY BANK 0 AND IT OCCUPIES VIRTUALLY ALL BANK 0, WITH SENSE SWITCH 0 DEPRESSED THE EXTENDED MEMORY ADDRESSING PORTION OF THE DIAGNOSTIC IS ENABLED TESTING AS MANY MEMORY FIELDS AS ARE SPECIFIED BY RIGHT SWITCHES 9-11.

/2.3 PRELIMINARY PROGRAMS

ALL PDP-12 BASIC INSTRUCTION DIAGNOSTICS AND EXERCISERS MUST HAVE BEEN SUCCESSFULLY RUN PRIOR TO RUNNING THE PROGRAM.

/3. LOADING PROCEDURE

/3.1 METHOD

THIS PROGRAM MUST BE LOADED WITH THE BINARY LOADER, IF YOU ARE UNFAMILIAR WITH THE PROPER BINARY LOADING PROCEDURES REFER TO APPENDIX A OF THIS DOCUMENT, OTHERWISE PROCEED WITH THE FOLLOWING:

A. SET THE TELETYPE READER SWITCH TO FREE.

B. OPEN THE TELETYPE READER AND INSERT THE PROGRAM TAPE SO THAT THE ARROWS ON THE TAPE ARE VISIBLE TO AND POINTING TOWARD THE OPERATOR.

C. CLOSE THE READER AND SET THE READER SWITCH TO START.

D. SET THE TELETYPE FRONT PANEL SWITCH TO START.

E. SET THE LEFT SWITCHES TO 7777.

F. SET THE RIGHT SWITCHES TO 4000.

- G. SET THE MODE SWITCH TO 8 MODE.
- H. DEPRESS I/O PRESET.
- I. DEPRESS START LS.
- J. WHEN THE PROGRAM TAPE HAS BEEN READ IN THE ACCUMULATOR MUST BE 0000. IF IT IS NOT, A READ-IN ERROR HAS OCCURRED AND ONE MIGHT TRY RELOADING THE BINARY LOADER. SEE APPENDIX A.
- K. REMOVE THE PROGRAM TAPE FROM THE READER.

/4. STARTING PROCEDURE

THIS PRELIMINARY SET UP PROCEDURE IS CRITICAL AND ANY OMISSION WILL RESULT IN AN ERROR.

1. SET SENSE SWITCH 0 IF YOU DESIRE TO UTILIZE THE EXTENDED MEMORY ADDRESSING FEATURE OF THE PROGRAM.
2. SET THE RIGHT SWITCH REGISTER SWITCHES 9 TO 11 EQUAL TO THE NUMBER OF EXTENDED MEMORY FIELDS TO BE TESTED. (NOTE: WITH SNS SW 0 SET AND RSW=0000 THE PROGRAM HALTS).
3. SET THE MODE SWITCH TO 8-MODE.

4. DEPRESS I/O PRESET
5. DEPRESS START 20

THE PROGRAM IS RUNNING.

/4.1 CONTROL SWITCH SETTINGS

SENSE SWITCH 0 ALLOWS THE PROGRAM TO TEST SEQUENTIALLY AS MANY EXTENDED 4K MEMORY BANKS AS ARE SPECIFIED BY THE RIGHT SWITCH REGISTER BITS 9-11.

FOR EXAMPLE WITH SNS 0=11

RSW=XXX1--TEST EXT MEM FIELD 1

RSW=XXX2--TEST EXT MEM FIELD 1 AND 2

ETC

RSW=XXX7--TEST EXT MEM FIELD 1, 2, 3, 4, 5, 6 AND 7

IF SNS 0=1 AND RSW=XXX0 THE PROGRAM WILL HALT AND ALLOW THE OPERATOR TO SET THE RIGHT SWITCH REGISTER BITS 9-11 TO THE DESIRED NUMBER OF EXTENDED MEMORY FIELDS. KEY CONTINUE WILL TRY TO RESTART THE EXTENDED MEMORY TEST.

NOTE: IT IS VITAL TO A COMPLETE TEST OF THE KW12B AUTOMATIC PRIORITY INTERRUPT SYSTEM TO TEST AT LEAST 1 EXTENDED MEMORY FIELD.

MESSAGE FORMAT

/5.

1. THERE ARE NO ERROR TYPEOUTS IN THE PROGRAM. THE DIAGNOSTIC IS OF THE FORM OF A BASIC INSTRUCTION TEST AND ERROR HALTS HAVE BEEN USED EXCLUSIVELY WITH A WELL DOCUMENTED INING.

About 1 min / pass

Bell rings every 24 seconds when testing only field 0,

every 50 seconds when

testing fields 0 & 1

MAINTENANCE INSTRUCTIONS

THERE ARE TWO MAINTENANCE IOT'S USED IN THE PROGRAM.

1. MAIN1=6251

THIS IOT USED IN CONJUNCTION WITH THE CONTENTS OF THE AC
SIMULATES AN AUTOMATIC PRIORITY INTERRUPT TO THE HIGHEST
PRIORITY LEVEL SPECIFIED BY THE AC.

FOR EXAMPLE WITH THE MACHINE LEVEL SET TO ALLOW ALL LEVELS
OF INTERRUPTS:

AC=0000 AND MAIN1 IOT--NO INTERRUPT
AC=4XXX AND MAIN1 IOT--LEV0 INTERRUPT
AC=7XXX AND MAIN1 IOT--LEV0 INTERRUPT
AC=3XXX AND MAIN1 IOT--LEV1 INTERRUPT
AC=1XXX AND MAIN1 IOT--LEV2 INTERRUPT
ETC
AC=0001 AND MAIN1 IOT--LEV11 INTERRUPT

2. MAIN2=6052

THIS IOT USED IN CONJUNCTION WITH THE CONTENTS OF THE AC
BITS 9-11 SIMULATES AN AUTOMATIC PRIORITY INTERRUPT TO
THE HIGHEST PRIORITY LEVEL SPECIFIED BY THE AC.

FOR EXAMPLE WITH THE MACHINE LEVEL SET TO ALLOW ALL LEVELS
OF INTERRUPTS:

AC=XXX4 AND MAIN2 IOT--LEV12 INTERRUPT
AC=XXX7 AND MAIN2 IOT--LEV12 INTERRUPT
AC=XXX2 AND MAIN2 IOT--LEV13 INTERRUPT
AC=XXX3 AND MAIN2 IOT--LEV13 INTERRUPT
AC=XXX1 AND MAIN2 IOT--LEV14 INTERRUPT
AC=XXX0 AND MAIN2 IOT--NO INTERRUPT

APPENDIX A

PDP-8 MODE PERFORATED - TAPE LOADER

READIN MODE LOADER

THE READIN MODE (RIM) LOADER IS A MINIMUM LENGTH, BASIC, PERFORATED-TAPE PROGRAM FOR THE 33 ASR. IT IS INITIALLY STORED IN MEMORY BY MANUAL USE OF THE OPERATOR CONSOLE KEYS AND SWITCHES. THE LOADER IS PERMANENTLY STORED IN 18 LOCATIONS OF PAGE 37.

THE RIM LOADER CAN ONLY BE USED IN CONJUNCTION WITH THE 33ASR READER (NOT THE HIGH-SPEED PERFORATED-TAPE READER). BECAUSE A TAPE IN RIM FORMAT IS, IN EFFECT, TWICE AS LONG AS IT NEED BE, IT IS SUGGESTED THAT THE RIM LOADER BE USED ONLY TO READ THE BINARY LOADER WHEN USING THE 33 ASR. (NOTE: SOME PDP-12 DIAGNOSTIC PROGRAM TAPES ARE IN RIM FORMAT).

THE COMPLETE PDP-12 RIM LOADER (SA#7756 IS AS FOLLOWS!)

ABSOLUTE ADDRESS	OCTAL CONTENT	TAG	INSTRUCTION I Z	COMMENTS
7756	6032	BEG,	KCC	/CLEAR AC AND FLAG
7757	6031		KSF	/SKIP IF FLAG = 1
7760	5357		JMP-1	/LOOKING FOR CHARACTER
7761,	6036		KRB	/READ BUFFER
7762,	7106		CLL RTL	
7763,	7006		RTL	/CHANNEL 8 IN ACO
7764,	7510		SQA	/CHECKING FOR LEADER
7765,	5357		JMP BEG+1	/FOUND LEADER
7766,	7006		RTL	/OK, CHANNEL 7 IN LINK
7767	6031		KSF	
7770,	5367		JMP-1	
7771,	6034		KRS	/READ, DO NOT CLEAR
7772,	7420		SNL	/CHECKING FOR ADDRESS
7773,	3776		DCA 1 TEMP	/STORE CONTENT
7774,	3376		DCA TEMP	/STORE ADDRESS
7775,	5356		JMP BEG	/NEXT WORD
7776,	0	TEMP,	0	/TEMP STORAGE
7777,	5XXX		JMP X	/JMP START OF BIN LOADER

PLACING THE RIM LOADER IN CORE MEMORY BY WAY OF THE OPERATOR CONSOLE KEYS AND SWITCHES IS ACCOMPLISHED AS FOLLOWS:

- SET THE STARTING ADDRESS 7756 IN THE LEFT SWITCHES.
- SET THE FIRST INSTRUCTION (6032) IN THE RIGHT SWITCHES.
- PRESS THE FILL SWITCH.
- PRESS THE FILL STEP SWITCH
- SET THE NEXT INSTRUCTION (6031) IN THE RIGHT SWITCHES.
- PRESS THE FILL STEP SWITCH.
- REPEAT STEPS D AND E UNTIL ALL 16 INSTRUCTIONS HAVE BEEN DEPOSITED.

IF A TAPE IN RIM FORMAT, PLACE THE TAPE IN THE READER, SET THE LEFT SWITCHES TO THE STARTING ADDRESS 7756 OF THE RIM LOADER

(NOT OF THE PROGRAM BEING READ), PRESS THE START LS KEY, AND
START THE TELETYPE READER,


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1 /
2 /KF128
3 /
4 /
5 /AUTOMATIC PRIORITY INTERRUPT OPTION TEST
6 ///////////////////////////////////////////////////
7 /
8 /SYMBOL TABLE
9 /
10 APION=6006
11 PJA=6760
12 RES=6771
13 SMLV=6772
14 RMLV=6773
15 RSTK=6774
16 RVEC=6775
17 SSTK=6776
18 SVEC=6777
19 MAIN1=6051
20 MAIN2=6052
21 ION=6001
22 IOP=6002
23 RIF=6224
24 RDF=6214
25
26 /
27 /
28 /PAGE 0
29 /CONSTANTS, TEMPORARY STORAGE AND FLAGS
30 /
31 *0
32 LOC0, 0
33 IMAGE, HLT
34 INC, 0
35 INCC
36 UT16A, UT16
37 UT37A, UT37
38 SETUP, SETUP
39 CLRSTK, CLRST
40 /
41 *20
42 JMP I .+1
43 TO
44 /JMP TO
45 /START OF PROG
46
47 IOPRES, IOPRE
48 COUNT, CNT-1
49 RAN, RANN
50 RAN1, 4731
51 RAN2, 5074
52 TSTPJ-4
53 TSTPJ1-4
54 TSTPJ2-4
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/SHOULD NOT GET AN 8 MODE INTERRUPT VIA LOC 0
/EXCEPT IN TEST TO WHERE LOC 1 = JMP :-1

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STACK, STACKK+1
STACKK+2
STACKK+3
STACKK+4
STACKK+5
/
*40
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HLT
TST
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220
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400
520
720
777
1000
1441
1641
1741
1777
2000
2020
2071
2076
2103
2110
2115
2122
2127
2134
2141
2146
2153
2160
2165
2172
TSTA,
K1,
K2,
K3,
K4,
K7,
K10,
K17,
K20,
K37,
K40,
K60,
K77,
K100,
K177,
K200,
K207,
K220,
K377,
K400,
K520,
K720,
K777,
K1000,
K1441,
K1641,
K1741,
K1777,
K2000,
K2020,
K2071,
K2076,
K2103,
K2110,
K2115,
K2122,
K2127,
K2134,
K2141,
K2146,
K2153,
K2160,
K2165,
K2172,
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/SHOULD NOT GET A LINC MODE INTERRUPT VIA LOC 40

107	0116	2177	K2177,	2177
108	0117	2500	K2500,	2500
109	0120	2501	K2501,	2501
110	0121	2525	K2525,	2525
111	0122	2777	K2777,	2777
112	0123	3500	K3500,	3500
113	0124	3777	K3777,	3777
114	0125	4000	K4000,	4000
115	0126	4400	K4400,	4400
116	0127	5020	K5020,	5020
117	0130	5240	K5240,	5240
118	0131	5241	K5241,	5241
119	0132	5252	K5252,	5252
120	0133	5400	K5400,	5400
121	0134	5577	K5577,	5577
122	0135	5600	K5600,	5600
123	0136	5777	K5777,	5777
124	0137	6000	K6000,	6000
125	0140	6100	K6100,	6100
126	0141	6300	K6300,	6300
127	0142	7000	K7000,	7000
128	0143	7017	K7017,	7017
129	0144	7020	K7020,	7020
130	0145	7077	K7077,	7077
131	0146	7277	K7277,	7277
132	0147	7400	K7400,	7400
133	0150	7402	K7402,	7402
134	0151	7577	K7577,	7577
135	0152	7600	K7600,	7600
136	0153	7740	K7740,	7740
137	0154	7741	K7741,	7741
138	0155	7760	K7760,	7760
139	0156	7770	K7770,	7770
140	0157	7772	M6,	-6
141	0160	7762	M16,	-16
142	0161	6140	VECT0,	VECT0
143	0162	6145	VECT1,	VECT1
144	0163	6152	VECT2,	VECT2
145	0164	6157	VECT3,	VECT3
146	0165	6164	VECT4,	VECT4
147	0166	6200	VECT5,	VECT5
148	0167	6205	VECT6,	VECT6
149	0170	6212	VECT7,	VECT7
150	0171	6217	VECT10,	VECT10
151	0172	6224	VECT11,	VECT11
152	0173	6231	VECT12,	VECT12
153	0174	6236	VECT13,	VECT13
154	0175	6244	VECT14,	VECT14
155	0176	6251	VECT15,	VECT15
156	0177	6256	VECT16,	VECT16


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/ROUTINE TO ZERO STACK AREA
/LOCATION 5600 TO LOCATION 5610
/ENTERED IN 8-MODE

CLRST, 0 /CONTAINS RETURN JUMP ADDRESS
CLA CLL /CLEAR AC AND LINK
TAD K5577 /AC = 5777 /AC = 5577
DCA 10 /LOC 10 = 5777 /LOC 10 = 5577
TAD K7770 /AC = 7770
DCA 11 /LOC 11 = 7770
CLA CLL /CLEAR AC AND LINK
DCA 1 10 /CLEAR STACK ADDRESS IN LOC 10
ISE 11 /DONE IF LOC 11 = 0
JMP -2 /NO-DO IT AGAIN
JMP I CLRST /RETURN TO MAIN PROGRAM

/LINC MODE SUBROUTINE
/TO MOVE MQ REGISTER INTO AC

LMODE
XACMQ, DJR /DISABLE JUMP RETURN
QAC /MQ 0-10 TO AC 1-11
ROL I 1 /ROTATE LEFT 1
QLZ I /SKIP IF MQ 11 = 1
JMP -4 /JMP 4 LOC (ENABLE JMP 0)
BSE I /SET AC BIT 0
1 /TO 1
JMP -41 /ENABLE JMP 0
JMP 0 /RETURN TO MAIN PROG

/LINC MODE SUBROUTINE
/TO SET AC AND MQ = 7777
/ALSO SET LINC AND FLO FLIP FLOPS

SACMQL, LDA I /AC =
7777 /7777
ROR I 14 /MQ = 7777 AND LINC = 1
LDA I /AC =
3777 /3777
ADA I /ADD
1 /1 SETTING FLO = 1
LDA I /AC =
7777 /7777
JMP 0 /RETURN TO MAIN PROG

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(0134) = 5577
 (0136) = 5777
 (0138) = 7770

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244 /
245 /8-MODE SUBROUTINE
246 /TO INCREMENT RETURN PC STORED IN LOC STACKK+1
247 /AS A RESULT OF A PUSH JUMP IOT OR AN INTERRUPT
248 /
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0266 0000 INCC, 0 /PHODE /RETURN JUMP ADDRESS TO MAIN PROGRAM
0267 7300 CLA CLL /CLEAR AC AND LINC
0270 1433 TAD I STACK+1 /GET PC STORED IN LOC STACKK+1
0271 1043 TAD K1 /INCREMENT
0272 3433 DCA I STACK+1 /STORE BACK IN LOC STACKK+1
0273 1150 TAD K7402 /AC = 7402 OR AN 8-MODE HLT
0274 5666 JMP I INCC /RETURN TO MAIN PROGRAM

/
/RANDOM NUMBER GENERATOR
/USED IN TEST T65
/
RANN, 0 /CONTAINS RETURN JUMP ADDRESS
CLA CLL /CLEAR AC AND LINC
TAD /AC = RAN1
RTL /ROTATE 2 LEFT
IAC /INCREMENT
TAD /ADD RAN2
DCA /STORE IN RAN2
TAD /GET RAN2
RTL /ROTATE 2 LEFT
RTL /ROTATE 2 LEFT
IAC /INCREMENT
DCA /STORE IN RAN1
CLA CLL /CLEAR LINC
JMP I RANN /RETURN TO MAIN PROGRAM

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329 /
330 /SET MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS ALL TO ZERO
331 /READ THEM BACK INTO CLEARED AC
332 /BITS 6 AND 7 ALWAYS COME BACK IN THE 1 STATE
333 /THE MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS COME BACK IN COMPLIMENT FORM
334 /
335
336 0441 7300 CLA CLL /CLEAR AC AND LINC
337 0442 1052 TAD K20 /AC = 20
338 0443 6772 SHLV /SET STACK AND VECTOR FIELD BITS AND MACHINE LEVEL = 0
339 0444 7200 CLA /CLEAR AC
340 0445 6773 RMLV /READ STK FLD INTO AC BITS 0-2 AND VCT PLD INTO AC BITS 3-5
341 0446 3002 DCA IMAGE /IMAGE OF AC
342 0447 1002 TAD IMAGE /GET IMAGE
343 0450 7040 CMA /COMPLIMENT AC
344 0451 7440 SZA /AC = 0
345 0452 7402 HLT /ERR-EXAMINE AC
346
347 /SET STACK ADDRESS REGISTER TO ZERO
348 /GENERATE AN IO PRESET
349 /READ STACK REGISTER INTO CLEARED AC
350 /IO PRESET SHOULD HAVE SET STACK POINTER REGISTER TO ALL ONES
351 /READ BACK IN COMPLIMENT FORM
352
353 0453 7300 CLA CLL /CLEAR AC AND LINC
354 0454 6776 SSTK /SET BITS 3-14 OF STK ADD TO AC BITS 0-11
355 0455 7200 CLA /CLEAR AC
356 0456 6774 RSTK /READ STK BITS 3-14 INTO AC BITS 0-11
357 0457 7040 CMA /COMPLIMENT AC
358 0460 7440 SZA /SHOULD BE 0
359 0461 7402 HLT /ERR-EXAMINE AC
360 0462 4422 JMS I IOPRES /LINC MODE IO PRESET
361 0463 6774 RSTK /READ STACK ADD BITS 3-14 INTO AC BITS 0-11
362 0464 7440 SZA /AC SHOULD = 0
363 0465 7402 HLT /ERR AC NOT = 0
364
365 /SET STACK ADDRESS REGISTER TO 7777 IN LINC MODE
366 /READ STACK POINTER REGISTER INTO CLEARED AC
367 /IT COMES BACK IN COMPLIMENT FORM
368
369 0466 7340 CLA CLL CMA /AC = 7777
370 0467 6141 LINC /LINC MODE
371 LMODE
372 IOB /EXEC 8MODE INST
373 0470 0500 /SET BITS 3-14 OF STACK ADD TO AC BITS 0-11
374 0471 0776 SSTK /CLEAR AC LINC AND MQ
375 0472 0011 CLR /EXEC 8MODE INST
376 0473 0500 IOB /EXEC 8MODE INST
377 0474 0774 RSTK /READ STACK ADD BITS 3-14 TO AC BITS 0-11
378 0475 1460 SAE I /SKIP IF AC
379 0476 0000 0 /EQUALS 0
380 0477 0000 HLT /ERR-AC NOT = 7777
381 0500 0002 POP /8MODE
382 PMODE
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/TEST STACK REGISTER ADDRESS POINTER
/IN 8-MODE
/

T5, CLA CLL CMA /AC = 7777
SSTK /SET BTS 3-14 OF STACK ADD TO AC BTS 0-11
CLA /CLEAR AC
K7020 /SET STK FLD TO AC BTS 0-2
SMLV /CLEAR AC
CLA /READ STACK FIELD BTS 0-2 INTO AC BTS 0-2
RMLV /IMAGE OF RESULT
DCA /GET IMAGE
TAD /COMPLIMENT AND INCREMENT
CIA /IMAGE SHOULD = 777
TAD /SHOULD BE 0
SZA /ERR-EXAMINE LOC IMAGE
HLT /LINC MODE IO PRESET
JMS I IOPRES /READ STACK FIELD BTS 0-2 INTO AC BTS 0-2
RMLV /IMAGE OF AC
DCA /GET IMAGE
TAD /COMPLIMENT AND INC
CIA /IMAGE SHOULD = 60
TAD /AC SHOULD = 0
SZA /ERR
HLT

/TEST STACK REGISTER ADDRESS POINTER
/IN 8-MODE
/

T6, CLA CLL /CLEAR AC AND LINC
TAD /AC=5020
SMLV /SET STK FLD TO AC BTS 0-2
CLA /CLEAR AC
TAD /AC=2525 /SET BTS 3-14 OF STK ADD TO AC BTS 0-11
SSTK /CLEAR AC
CLA /READ STK FLD BTS 0-2 INTO AC BTS 0-2 ALSO GET VCT FLD BTS 3-5
RMLV /IMAGE OF RESULT
DCA /GET IMAGE
TAD /COMPLIMENT AND INCREMENT
CIA /IMAGE SHOULD = 2777
TAD /AC SHOULD = 0
SZA /ERR-EXAMINE LOC IMAGE
HLT /READ STACK ADD BTS 3-14 INTO AC BTS 0-11
DCA /IMAGE OF RESULT
TAD /GET IMAGE
CIA /COMPLIMENT AND INCREMENT
TAD /IMAGE SHOULD = 5252
SZA /AC SHOULD = 0
HLT /ERR-EXAMINE LOC IMAGE
JMP I .+1 /JMP TO
T7 /TEST T7

435	DIAL10	V003	15-SEP-71	0:10	PAGE 10	
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/TEST STACK REGISTER ADDRESS POINTER
/IN 8-MODE
/
0600 0600 /CLEAR AC AND LINC
0601 0601 /AC=2020
0602 0602 /SET STK FLD BTS 0-2 TO AC BTS 0-2 ALSO SET VCT BTS = 0
0603 0603 /CLEAR AC
0604 0604 /AC=5252
0605 0605 /SET BTS 3-14 OF STK ADD EQUAL TO AC BTS 0-11
0606 0606 /CLEAR AC
0607 0607 /READ STK FLD BTS 0-2 INTO AC BTS 0-2
0610 0610 /IMAGE OF RESULT
0611 0611 /GET IMAGE
0612 0612 /COMPLIMENT AND INCRIMENT
0613 0613 /IMAGE SHOULD = 5777
0614 0614 /AC SHOULD = 0
0615 0615 /ERR-EXAMINE LOC IMAGE
0616 0616 /READ STACK ADD BTS 3-14 INTO AC BTS 0-11
0617 0617 /IMAGE OF RESULT
0620 0620 /GET IMAGE
0621 0621 /COMPLIMENT AND INCRIMENT
0622 0622 /IMAGE SHOULD = 2525
0623 0623 /AC = 0
0624 0624 /ERR-EXAMINE LOC IMAGE
0625 0625 /
0626 0626 /TEST VECTOR REGISTER ADDRESS POINTER
0627 0627 /IN 8-MODE
0630 0630 /
0631 0631 /CLEAR AC AND LINC
0632 0632 /SET VECTOR ADD BTS 3-9 TO AC BTS 0-6
0633 0633 /CLEAR AC
0634 0634 /READ VECTOR ADD BTS 3-14 INTO AC BTS 0-11
0635 0635 /IMAGE OF AC
0636 0636 /GET IMAGE
0637 0637 /COMPLIMENT AND INCRIMENT
0638 0638 /IMAGE SHOULD = 7741
0639 0639 /AC = 0
0640 0640 /ERR-EXAMINE AC
0641 0641 /
0642 0642 /
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476 /
477 /TEST VECTOR REGISTER ADDRESS POINTER
478 /IN 8-MODE
479 /
480 T11, CLA CLL /CLEAR AC AND LINC
481 TAD K7740 /AC=7740
482 SVEC /SET VECTOR ADD BITS 3-9 TO AC BITS 0-6
483 CLA /CLEAR AC
484 RVEC /READ VECTOR ADD BITS 3-14 INTO AC BITS 0-11
485 DCA IMAGE /IMAGE OF AC
486 TAD IMAGE /GET IMAGE
487 CIA /COMPLIMENT AND INC
488 TAD K1 /IMAGE SHOULD = 1
489 SEA /AC = 0
490 HLT /ERR-EXAMINE AC
491 JMS I IOPRES /LINC MODE IO PRESET
492 RVEC /READ VECTOR ADDRESS BITS 3-14 INTO AC BITS 0-11
493 DCA IMAGE /IMAGE OF AC
494 TAD IMAGE /GET IMAGE
495 CIA /COMPLIMENT AND INC
496 TAD K1 /IMAGE SHOULD = 1
497 SEA /AC = 0
498 HLT /ERR-IO PRESET DID NOT CLEAR VECTOR ADD BITS 3-14
499
500 /TEST VECTOR REGISTER ADDRESS POINTER
501 /IN 8-MODE
502 /
503 T12, CLA CLL /CLEAR AC AND LINC
504 LINC /LINC MODE
505 LMODE /LOAD AC WITH
506 LDA I /OPERAND = 7740
507 7740 /EXEC 8MODE INST
508 IOB /SET VECTOR BITS 3-9 TO AC BITS 0-6
509 SVEC /CLEAR AC HQ LINC
510 CLR /EXEC 8MODE INST
511 IOB /READ VECTOR ADD BITS 3-14 INTO AC BITS 0-11
512 RVEC /AC =
513 SAE I /1
514 1 /ERR-AC NOT = 7740
515 HLT /8 MODE
516 PDP
517 PMODE /LINK MODE PROG IO PRESET
518 JMS I IOPRES
519 4422

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/TEST VECTOR REGISTER ADDRESS POINTER
/IN 8-MODE
/
T13,
7300 0700 /CLEAR AC
1153 0701 /AC = 7740
6777 0702 /SET VECTOR ADD BTS 3-9 TO AC BTS 0-6
7200 0703 /CLEAR AC
1067 0704 /AC=0720
6772 0705 /SET VECTOR FLD BTS 0-2 TO AC BTS 3-5
7200 0706 /CLEAR AC
6773 0707 /READ VECTOR FLD BTS 0-2 INTO AC BTS 3-5 ALSO GET STK FLD BTS 0-2
3002 0710 /IMAGE OF RESULT
1002 0711 /GET IMAGE
7041 0712 /COMPLIMENT AND INC
1145 0713 /IMAGE SHOULD = 7077
7440 0714 /AC = 0
7402 0715 /ERR-EXAMINE LOC IMAGE
4422 0716 /LINC MODE IO PRESET
6773 0717 /READ VECTOR FIELD BITS INTO AC
3002 0720 /IMAGE OF AC
1002 0721 /GET IMAGE
7041 0722 /COMPLIMENT AND INC
1055 0723 /IMAGE SHOULD = 60
7440 0724 /AC = 0
7402 0725 /ERR-AC NOT = 0

/TEST VECTOR REGISTER ADDRESS POINTER
/IN LINC MODE
/
T14,
7300 0726 /CLEAR AC AND LINC
6141 0727 /LINC MODE
1020 0730 /LOAD AC WITH
7720 0731 /OPERAND = 7720
0500 0732 /EXEC 8MODE INST
0772 0733 /SET STACK AND VECTOR FIELD BITS
0011 0734 /CLEAR AC HQ LINC
0500 0735 /EXEC 8MODE INST
0773 0736 /READ STACK AND VECTOR FIELD BITS TO AC BITS 0-5
1460 0737 /AC =
77 0737 /77
0077 0740 /ERR-AC NOT = 77
0000 0741 /8MODE
0002 0742 /PDP
4422 0743 /JMS I IOPRES /LINK MODE PROG IO PRESET

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567 /
568 /TEST VECTOR REGISTER ADDRESS POINTER
569 /IN 8-MODE
570 /
571
572 T15, CLA CLL /CLEAR AC AND LINC
573 TAD K520 /AC=0520
574 SMLV /SET VECTOR FLD BTS 0-2 TO AC BTS 3-5
575 CLA /CLEAR AC
576 TAD K2500 /AC=2500
577 SVEC /SET VECTOR ADD BTS 3-9 EQUAL TO AC BTS 0-6
578 CLA /CLEAR AC
579 RMLV /READ VECTOR FLD BTS 0-2 INTO AC BTS 3-5
580 DCA /IMAGE OF AC
581 TAD IMAGE /GET IMAGE
582 CIA /COMPLIMENT AND INCREMENT
583 TAD K7277 /IMAGE SHOULD = 7277
584 SZA /AC SHOULD = 0
585 HLT /ERR-EXAMINE LOC IMAGE
586 CLA /CLEAR AC
587 RVEC /READ VECTOR ADD BTS 3-14 INTO AC BTS 0-11
588 DCA /IMAGE OF RESULT
589 TAD IMAGE /GET RESULT
590 CIA /COMPLIMENT AND INCREMENT
591 TAD K5241 /IMAGE SHOULD = 5241
592 SZA /AC SHOULD = 0
593 HLT /ERR-EXAMINE LOC IMAGE
594 JMP I .*1 /JMP TO
595 T16 /TEST T16
596
597 /TEST VECTOR REGISTER ADDRESS POINTER
598 /IN 8-MODE
599 /
600
601 T16, *1000 /CLEAR AC AND LINC
602 TAD K220 /AC=0220
603 SMLV /SET VECTOR ADD BTS 0-2 TO AC BTS 3-5
604 CLA /CLEAR AC
605 TAD K9240 /AC = 9240
606 SVEC /SET VECTOR ADD BTS 3-9 TO AC BTS 0-6
607 CLA /CLEAR AC
608 RMLV /READ VECTOR FLD BTS 0-2 INTO AC BTS 3-5
609 DCA /IMAGE OF AC
610 TAD IMAGE /GET IMAGE
611 CIA /COMPLIMENT AND INCREMENT
612 TAD K7577 /IMAGE SHOULD = 7577
613 SZA /AC SHOULD = 0
614 HLT /ERR-EXAMINE LOC IMAGE
615 RVEC /READ VECTOR ADD BTS 3-14 INTO AC BTS 0-11
616 DCA /IMAGE OF AC
617 TAD IMAGE /GET IMAGE
618 CIA /COMPLIMENT AND INC
619 TAD K2501 /IMAGE SHOULD = 2501
620 SZA /AC = 0
621 HLT /ERR-EXAMINE LOC IMAGE

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/
/TEST SETTING OF MACHINE LEVEL REGISTER
/TO ALL POSSIBLE COMBINATIONS
/IN 8-MODE
/

1066	7300	CLA CLL	/CLEAR AC AND LINC
1067	3010	DCA 10	/LOC 10 = 0
1070	1160	TAD M16	/AC = -16
1071	3011	DCA 11	/LOC 11 = -16
1072	1023	TAD COUNT	/POINTS TO TOP OF EXPECTED RESULT LIST
1073	3012	DCA 12	/STORE IN LOC 12
1074	7300	CLA CLL	/AC = 0
1075	1010	TAD 10	/LOC 10 TO AC
1076	6772	SHLV	/SET MACHINE LEVEL TO AC BITS 8-11
1077	7200	CLA	/CLEAR AC
1100	6773	RMLV	/READ MACHINE LEVEL INTO AC BITS 8-11
1101	3002	DCA	/IMAGE OF AC
1102	1002	TAD	/GET IMAGE
1103	7041	CIA	/COMPLIMENT AND INCREMENT
1104	1412	TAD I 12	/IMAGE SHOULD = CONTENTS OF ADDRESS SPECIFIED BY LOC 12
1105	7440	SEA	/AC = 0
1106	7402	HLT	/ERR-EXAMINE LOC IMAGE
1107	4422	JMS I IOPRES	/LINC MODE TO PRESET
1110	6773	RMLV	/READ MACHINE LEVEL INTO AC BITS 8-11 AND STACK AND VECTOR FLD BITS INTO AC BITS 0-5
1111	0051	AND	/AC = 17
1112	7440	SEA	/AC SHOULD = 0
1113	7402	HLT	/ERR-MACHINE LEVEL NOT CLEARED BY IO PRESET
1114	2010	ISE	/INCREMENT LOC 10
1115	5317	JMP	/LOC 10 NOT = 0--SHOULD NEVER = 0
1116	7402	HLT	/--NEVER GET HERE--
1117	2011	ISE	/INCREMENT LOC 11 AND SKIP IF ZERO
1120	5274	JMP	/NO-DO IT AGAIN
1121	5722	JMP I	/JMP TO
1122	1200	T22	/TEST T22

/EXPECTED RESULTS TABLE FOR TEST T21
/

1123	0077	CNT, 77
1124	0076	76
1125	0075	75
1126	0074	74
1127	0073	73
1130	0072	72
1131	0071	71
1132	0070	70
1133	0067	67
1134	0066	66
1135	0065	65
1136	0064	64
1137	0063	63
1140	0062	62
1141	0061	61
1142	0060	60

PC	Instruction	Comments
721	/TEST PUSHJ INSTRUCTION WITH API OFF	
722	/WITH ALL STORED STATUS CLEARED IF POSSIBLE	
723	/TEST RESTORE NOT WITH API OFF	
724	/AFTER ALTERING VALUE OF RETURN PC IN STACK+1	
725	/AND SETTING MACHINE PARAMETERS TO BE RESTORED	
726	/TO THE OPPOSITE OF THE EXPECTED STATE	
727		
728		
729		
730	*1200	
731	JMS I	CLRSTK
732	JMS I	SETUP
733	LINC	MODE
734	LMODE	
735	FLO I	
736	HLT	SKIP IF FLO = 0
737	POP	ERR-FLO SET
738	PMODE	8-MODE
739	CLA CLL	CHA
740	DCA	LOC
741	PJA10	
742	TSTPJ	
743	HLT	
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772 1244 1433 TAD I STACK+1 /GET PC STORED ON STACK BY PUSH JUMP
773 7041 CIA /COMPLIMENT
774 1245 7041 CIA /COMPLIMENT
775 1246 1027 TAD PJP /SHOULD EQUAL TSTPJ=4
776 1247 7440 SZA /AC SHOULD = 0
777 1250 7402 HLT /ERR
778 1251 1434 TAD I STACK+2 /GET MODE FLO LINK AND MACH LEV STORED ON STACK BY PUSH JUMP
779 1252 7440 SZA /AC = 0
780 1253 7402 HLT /ERR
781 1254 1435 TAD I STACK+3 /GET MQ STORED ON STACK BY PUSH JUMP
782 1255 7440 SZA /AC SHOULD = 0
783 1256 7402 HLT /ERR
784 1257 1436 TAD I STACK+4 /GET UF IF AND OF STORED ON STACK BY PUSH JUMP
785 7041 CIA /COMPLIMENT AND INC
786 1261 1045 TAD K3 /LOC STACKK+4 SHOULD = 3
787 1262 7440 SZA /AC = 0
788 1263 7402 HLT /ERR
789 1264 1433 TAD I STACK+1 /GET PC STORED IN LOC STACK+1
790 1265 1044 TAD K2 /INCREMENT BY 2
791 1266 3433 DCA I STACK+1 /STORE BACK IN LOC STACK+1
792 1267 6141 LINC /LINC MODE
793 LMODE
794 JMP
795 STA
796 0
797 1273 0500 IOB /EXECUTE 8-MODE INST
798 1274 0771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
799 1275 0000 HLT /ERR-RESTORE IOT DID NOT EXECUTE
800 PMODE
801 7440 SZA /AC = 0
802 7402 HLT /ERR-AC NOT = 0
803 7430 SEL /LINC = 0
804 7402 HLT /ERR-LINC NOT = 0
805 6141 LINC /LINC MODE
806 LMODE
807 0474 FLO I /FLO = 0
808 0000 HLT /ERR-FLO NOT = 0
809 6243 JMP XACMQ /MQ TO AC
810 1460 SAE I /AC =
811 0000 0 /0
812 1310 HLT /ERR-AC NOT = 0 THEREFORE MQ WAS NOT = 0
813 0500 IOB /EXECUTE 8-MODE INST
814 0224 RIF /READ INSTRUCTION FIELD
815 1460 SAE I /AC =
816 0000 0 /0
817 1315 HLT /ERR-AC NOT = 0
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816				CLR	/CLEAR AC LINC AND MQ
819				IOB	/EXECUTE B-MODE INST
820				ROF	/READ DATA FIELD
821				SAE I	/AC =
822				6	/6
823				HLT	/ERR-DF IN AC NOT = 6
824				CLR	/CLEAR AC LINC AND MQ
825				PDP	/B-MODE
826				PMODE	
827				RMLV	/READ MACHINE LEVEL AND THE STACK AND VECTOR FIELD BITS INTO AC
828				DCA	/IMAGE OF AC
829				TAD	/GET IMAGE
830				CMA	/COMPLIMENT AC
831				SEA	/AC = 0
832				HLT	/ERR
833				RSTK	/READ STACK POINTER REGISTER INTO AC
834				DCA	/IMAGE OF AC
835				TAD	/GET IMAGE
836				CIA	/COMPLIMENT AND INC
837				TAD	/IMAGE SHOULD EQUAL 2177
838				SEA	/AC = 0
839				HLT	/ERR
840				RVEC	/READ VECTOR POINTER REGISTER INTO AC
841				DCA	/IMAGE OF AC
842				TAD	/GET IMAGE
843				CIA	/COMPLIMENT AND INC
844				TAD	/IMAGE SHOULD = 1641
845				SZA	/AC = 0
846				HLT	/ERR
847				JMP I	/JMP TO
848				T24	/TEST T24
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1316	0011				
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1320	0214				
1321	1460				
1322	0006				
1323	0000				
1324	0011				
1325	0002				
1326	6773				
1327	3002			IMAGE	
1330	1002			IMAGE	
1331	7040				
1332	7440				
1333	7402				
1334	6774				
1335	3002			IMAGE	
1336	1002			IMAGE	
1337	7041			K2177	
1340	1116				
1341	7440				
1342	7402				
1343	6775				
1344	3002			IMAGE	
1345	1002			IMAGE	
1346	7041			K1641	
1347	1073				
1350	7440				
1351	7402				
1352	5753			.+1	
1353	1403				
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859 /
860 /TEST PUSHJ INSTRUCTION WITH API OFF IN LINC MODE
861 /AND ALL STATUS TO BE STORED SET TO
862 /1 STATE IF POSSIBLE
863 /TEST RESTORE IOT WITH API OFF
864 /AFTER ALTERING THE VALUE OF THE RETURN PC
865 /ALSO SET MACHINE PARAMETERS THAT WILL BE RESTORED
866 /TO OPPOSITE OF EXPECTED STATE
867 /
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1403	*1403				
1403	JMS I	CLRSTK	/CLEAR STACK		
1404	4406	SETUP	/SETUP API REGISTERS		
1405	1053	K37	/AC = 37		
1406	6772	SHLV	/SET MACH LEV = 17 AND THE STACK AND VECTOR FIELD BITS = 0		
1407	6141	LINC	/LINC MODE		
		LMODE			
1410	1020	LDA I	/AC =		
1411	7426	6000: TSTPJ1+1	/JMP TO TSTPJ1+1		
1412	1040	STA	/STORE AC		
1413	1402	1402	/LOC 1402 OF FIELD 0 (DECODES AS HLT IN 8-MODE)		
1414	0643	LDF	/SET DATA FIELD = 3		
1415	0254	JMP	/SET AC LINC MQ AND FLO		
1416	0500	IOB	/EXECUTE 8-MODE INST		
1417	0700	PJAI0	/PUSH JUMP (WITH FIELD BITS = 0)		
1420	1425	TSTPJ1	/TO LOC TSTPJ1		
1421	0000	HLT	/ERR - PUSH JUMP SKIPPED		
1422	0000	HLT			
1423	7402	JMP	/DECODES AS AN 8-MODE HLT IF RESTOR WAS NOT TO LINC MODE		
1424	7527	JMP	/CONTINUE WITH TEST		
		LMODE			
1425	7402	TSTPJ1	/ARRIVE FROM PUSH JUMP IN LINC MODE (DECODES AS HLT IF 8-MODE)		
1426	0672	L2E I	/LINC BIT = 1		
1427	0000	HLT	/ERR		
1430	1460	SAE I	/AC =		
1431	7777	7777	/7777		
1432	0000	HLT	/ERR		
1433	0454	FLO	/FLO = 1		
1434	0000	HLT	/ERR-FLO NOT = 1		
1435	6243	JMP	/MQ TO AC		
1436	1460	SAE I	/AC =		
1437	7777	7777	/7777		
1440	0000	HLT	/ERR		
1441	0011	CLR	/CLEAR AC LINC AND MQ		
1442	0500	IOB	/EXECUTE 8-MODE INST		
1443	0214	ROF	/READ DATA FIELD		
1444	1460	SAE I	/AC =		
1445	0006	6	/6		
1446	0000	HLT	/ERR		
1447	0011	CLR	/CLEAR AC LINC AND MQ		
1450	0643	LDF	/LOAD DATA FIELD 3		

3

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911						
912	1451	0500	IOB		/EXECUTE 8 MODE INST	
913	1452	0774	RSYK		/READ STACK POINTER REGISTER INTO AC	
914	1453	1460	SAE I		/AC =	
915	1454	2172	2172		/2172	
916	1455	0000	HLT		/ERR	
917						
918						
919	1456	0011	CLR		/CLEAR AC LINC AND MQ	
920	1457	0002	POP		/8 = MODE	
921			PMODE			
922	1460	1432	TAD I	STACK	/GET AC STORED ON STACK BY PUSH JUMP	
923	1461	7040	CMA		/COMPLIMENT	
924	1462	7440	SEA		/AC = 0	
925	1463	7402	HLT		/ERR	
926	1464	1433	TAD I	STACK+1	/GET PC STORED ON STACK BY PUSH JUMP	
927	1465	7041	CIA		/COMPLIMENT AND INC	
928	1466	1030	TAD	PJPC1	/SHOULD EQUAL TSTPJ1-4	
929	1467	7440	SEA		/AC = 0	
930	1470	7402	HLT		/ERR	
931	1471	1434	TAD I	STACK+2	/GET MODE FLO LINC AND MACHINE LEVEL STORED ON STACK BY PUSH JUMP	
932	1472	7041	CIA		/COMPLIMENT AND INC	
933	1473	1143	TAD	K7017	/LOCATION STACK+2 SHOULD=7017	
934	1474	7440	SEA		/AC = 0	
935	1475	7402	HLT		/ERR	
936	1476	1435	TAD I	STACK+3	/GET MQ STORED ON STACK BY PUSH JUMP	
937	1477	7040	CMA		/COMPLIMENT AC	
938	1500	7440	SEA		/AC SHOULD = 0	
939	1501	7402	HLT		/ERR	
940	1502	1436	TAD I	STACK+4	/GET UF IF AND DF STORED ON STACK BY PUSH JUMP	
941	1503	7041	CIA		/COMPLIMENT AND INC	
942	1504	1045	TAD	K3	/STACK+4 SHOULD = 3	
943	1505	7440	SEA		/AC SHOULD = 0	
944	1506	7402	HLT		/ERR	
945	1507	1433	TAD I	STACK+1	/GET PC STORED ON STACK BY PUSH JUMP	
946	1510	1044	TAD	K2	/INCREMENT BY 2	
947	1511	3433	DCA I	STACK+1	/STORE BACK IN STACK+1	
948	1512	6141	LINC		/LINC MODE	
949			LMODE			
950	1513	1020	LDA I		/AC =	
951	1514	7424	6000:TSTPJ1-1		/JMP TO LOC TSTPJ1-1	
952	1515	1040	STA		/STORE AC	
953	1516	1402	1402		/IN LOC 1402 OF FIELD 0 (DECODES AS HLT IN 8=MODE)	
954	1517	0011	CLR		/CLEAR AC LINC AND MQ	
955	1520	1120	ADA I		/ADD	
956	1521	0000	0		/0 TO AC	
957	1522	0474	FLO I		/TO INSURE FLO = 0	
958	1523	0000	HLT		/ERR-FLO NOT = 0	
959	1524	0002	PDP		/8 = MODE	
960			PMODE			
961	1525	6771	RES		/RESTORE MACHINE TO LAST STATUS STORED ON STACK	
962	1526	7402	HLT		/ERR - RESTORE SKIPPED	


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/
/TURN ON API ---FIRST TIME---
/TEST THAT STACK AND VECTOR FIELD BITS
/CANNOT BE ALTERED IN 8MODE WITH API ON
/TURN API OFF WITH IOF ---FIRST TIME---
/TEST THAT STACK AND VECTOR FIELD BITS CAN BE ALTERED
/
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*1600
JMS I
JMS I
LINC
LMODE
CLR
PDR
PHODE
APION
TAD
SHLV
CLA
RMLV
DCA
TAD
CIA
TAD
SZA
HLT
IOF
TAD
SHLV
CLA
RMLV
DCA
TAD
CMA
SZA
HLT

726,
CLRSTK
TOPRES
LINC
LMODE
CLR
PDR
PHODE
APION
TAD
SHLV
CLA
RMLV
DCA
TAD
CIA
TAD
SZA
HLT
IOF
TAD
SHLV
CLA
RMLV
DCA
TAD
CMA
SZA
HLT

/CLEAR STACK
/GENERATE IO PRESET
/LINC MODE
/CLEAR AC LINC AND MQ
/8-MODE
/TURN API INTERRUPT SYS ON
/AC = 20
/SET MACHINE LEVEL = 0
/READ STACK AND VECTOR FIELD BITS - MACHINE LEVEL INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AND INC
/IMAGE SHOULD = 77
/AC = 0
/ERR
/TURN API INTERRUPT SYS OFF
/AC = 20
/CLEAR STACK AND VECTOR FIELD BITS - SET MACHINE LEVEL = 0
/CLEAR AC
/READ STACK AND VECTOR FIELD BITS - MACHINE LEVEL INTO AC
/IMAGE OF AC
/GET IMAGE
/COMPLIMENT AC
/AC = 0
/ERR

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1037 /
1038 /TURN API ON
1039 /TEST THAT STACK AND VECTOR FIELD BITS
1040 /CANNOT BE ALTERED IN LINC MODE WITH API ON
1041 /TURN API OFF WITH IOF
1042 /TEST THAT STACK AND VECTOR FIELD BITS CAN BE ALTERED
1043 /
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1074 /

1632 4422 JMS I IOPRES /GENERATE IO PRESET
1633 4141 LINC /LINC MODE
1634 0500 LMODE
1635 0006 IOB /EXECUTE 8-MODE INST
1636 1020 APION /TURN API INTERRUPT SYSTEM ON
1637 0020 LOA I /AC =
1638 0500 20 /20
1639 0772 IOB /EXECUTE 8-MODE INST
1640 0011 SMLV /SET MACHINE LEVEL = 0
1641 0500 CLR /CLEAR AC LINC AND MQ
1642 0773 IOB /EXECUTE 8-MODE INST
1643 1460 RMLV /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
1644 0077 SAE I /AC =
1645 0077 77 /77
1646 0000 HLT /ERR
1647 0500 IOB /EXECUTE 8-MODE INST
1648 0002 IOF /TURN API INTERRUPT SYS OFF
1649 1020 LOA I /AC =
1650 0020 20 /20
1651 0500 IOB /EXECUTE 8-MODE INST
1652 0772 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 0
1653 0011 CLR /CLEAR AC LINC AND MQ
1654 0500 IOB /EXECUTE 8-MODE INST
1655 0773 RMLV /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
1656 1460 SAE I /AC =
1657 0077 7777 /7777
1658 0000 HLT /ERR
1659 0002 POP /8-MODE
1660 0002 PMODE

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1075 /
1076 /TURN API ON
1077 /TEST THAT BITS 3-14 OF THE STACK ADDRESS
1078 /CANNOT BE ALTERED IN 8MODE WITH API ON
1079 /TURN API OFF
1080 /TEST THAT STACK ADD CAN BE ALTERED
1081 /
1082 T30, JMS I IOPRES /GENERATE IO PRESET
1083 APION /TURN AP INTERRUPT SYS ON
1084 SSK /TRY TO SET STACK ADDRESS REGISTER = 0
1085 CLA /CLEAR AC
1086 RSK /READ STACK ADDRESS REGISTER INTO AC
1087 SZA /AC = 0
1088 HLT /ERR
1089 IOF /TURN API INTERRUPT SYS OFF
1090 CLA CLL /CLEAR AC AND LINC
1091 SSK /SET STACK ADDRESS REGISTER = 0
1092 CLA /CLEAR AC
1093 RSK /READ STACK ADDRESS REGISTER INTO AC
1094 CMA /COMPLEMENT AC
1095 SZA /AC = 0
1096 HLT /ERR
1097

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/TURN API ON
/TEST THAT STACK ADDRESS BITS 3-14
/CANNOT BE ALTERED IN LINC MODE WITH API ON
/TURN API OFF
/TEST THAT STACK ADDRESS CAN BE ALTERED
/
731, JMS I IOPRES /GENERATE IO PRESET
SSTK /SET STACK ADDRESS REGISTER = 0
LINC /LINC MODE
LMODE
IOB /EXECUTE 8-MODE INST
APION /TURN API INTERRUPT SYS ON
LDA I /AC =
7777 /7777
IOB /EXECUTE 8-MODE INST
SSTK /TRY TO SET STACK ADDRESS REG = 7777
CLR /CLEAR AC LINC AND MQ
IOB /EXECUTE 8-MODE INST
RSTK /READ STACK ADDRESS REG INTO AC
SAE I /AC =
7777 /7777
HLT /ERR
IOB /EXECUTE 8-MODE INST
ION /TURN API OFF BY TURNING NORMAL INTERRUPT SYS ON
LDA I /AC =
7777 /7777
IOB /EXECUTE 8-MODE INST
SSTK /SET STACK ADDRESS REGISTER = 7777
CLR /CLEAR AC LINC AND MQ
IOB /EXECUTE 8-MODE INST
RSTK /READ STACK ADDRESS REGISTER INTO AC
SAE I /AC =
0 /0
HLT /ERR
POP /8-MODE
PMODE
IOP /TURN NORMAL INTERRUPT SYS OFF
1704 4422
1705 6776
1706 6141
1707 0500
1710 0026
1711 1020
1712 7777
1713 0500
1714 0776
1715 0011
1716 0500
1717 0774
1720 1460
1721 7777
1722 0000
1723 0500
1724 0001
1725 1020
1726 7777
1727 0500
1730 0776
1731 0011
1732 0500
1733 0774
1734 1460
1735 0000
1736 0000
1737 0002
1740 6002

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1137 /
1138 /TURN API ON
1139 /TEST THAT VECTOR ADDRESS
1140 /CANNOT BE ALTERED IN 8MODE WITH API ON
1141 /TURN API OFF
1142 /TEST THAT VECTOR ADDRESS CAN BE ALTERED
1143 /
1144 T32,
1145     JMS I      /SETUP API REGISTERS
1146     SVEC      /SET VECTOR ADDRESS REGISTER = 0
1147     APION     /TURN API INTERRUPT SYS ON
1148     TAD       /AC = 7740
1149     SVEC      /TRY TO SET VECTOR ADDRESS REG = 7740
1150     CLA       /AC = 0
1151     RVEC      /READ VECTOR ADDRESS REGISTER INTO AC
1152     DCA       /IMAGE OF AC
1153     TAD       /GET IMAGE
1154     CIA       /COMPLIMENT AND INC
1155     TAD       /IMAGE SHOULD = 7741
1156     SZA      /AC = 0
1157     HLT      /ERR
1158     IOF      /TURN API INTERRUPT SYS OFF
1159     TAD       /AC = 7740
1160     SVEC      /SET VECTOR ADDRESS = 7740
1161     CLA       /AC = 0
1162     RVEC      /READ VECTOR ADDRESS REGISTER INTO AC
1163     DCA       /IMAGE OF AC
1164     TAD       /GET IMAGE
1165     CIA       /COMPLIMENT AND INC
1166     TAD       /IMAGE SHOULD = 1
1167     SZA      /AC = 0
1168     HLT      /ERR
1169     JMP I     /JMP TO
1170     T33      /TEST T33
1171
1741 4426
1742 6777
1743 6006
1744 1153
1745 6777
1746 7200
1747 6775
1748 3002
1749 1002
1750 7041
1751 1154
1752 7440
1753 7402
1754 6002
1755 1153
1756 6777
1757 7200
1758 6775
1759 3002
1760 1002
1761 7041
1762 1043
1763 7440
1764 7402
1765 5772
1766 2001
1767 1771
1768 1772
1769 1771
1770 1772
1771 1772
1772 2001

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1172 /
1173
1174 /TURN API ON
1175 /TEST THAT VECTOR ADDRESS
1176 /CANNOT BE ALTERED IN LINC MODE WITH API ON
1177 /TURN API OFF
1178 /TEST THAT VECTOR ADDRESS CAN BE ALTERED
1179 /
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2001	*2001	/CLEAR AC AND LINC
7300	CLA CLL	/SETUP API REGISTERS
2002	JMS I	/SET VECTOR ADDRESS REG = 0
2003	SVEC	/LINC MODE
2004	LINC	
	LMODE	
2005	IOB	/EXECUTE 8-MODE INST
2006	APION	/TURN API INTERRUPT SYS ON
2007	LDA I	/AC =
2010	7740	/7740
2011	IOB	/EXECUTE 8-MODE INST
2012	SVEC	/TRY TO SET VECTOR ADDRESS REG = 7740
2013	CLR	/CLEAR AC LINC AND MQ
2014	IOB	/EXECUTE 8-MODE INST
2015	RVEC	/READ VECTOR ADDRESS REGISTER INTO AC
2016	SAE I	/AC =
2017	7741	/7741
2020	HLT	/ERR
2021	IOB	/EXECUTE 8-MODE INST
2022	IOF	/TURN API INTERRUPT SYS OFF
2023	CLR	/CLEAR AC LINC AND MQ
2024	LDA I	/AC =
2025	7740	/7740
2026	IOB	/EXECUTE 8-MODE INST
2027	SVEC	/SET VECTOR ADDRESS REGISTER = 7740
2030	CLR	/CLEAR AC LINC AND MQ
2031	IOB	/EXECUTE 8-MODE INST
2032	RVEC	/READ VECTOR ADDRESS REGISTER INTO AC
2033	SAE I	/AC =
2034	1	/1
2035	HLT	/ERR
2036	PDP	/8-MODE
	PMODE	

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/EXECUTE IN 8 MODE
/TEST PUSHJ AND RES IOTS WITH API ON
/
T34,
JMS I CLRSTK /CLEAR STACK
JMS I SETUP /SET UP API REGISTERS
CML CMA /SET LINC = 1 AND AC = 7777
DCA /LOC 0 = 7777
DCA I UT37A /LOC UT37A = 0
APION /TURN API INTERRUPT SYS ON
PJA10 /PUSH JUMP WITH FIELD BITS = 0
TSTPJ2 /TO LOC TSTPJ2
HLT /ERR = PUSHJ SKIPPED
HLT
AND /RETURN FROM RESTORE IOT (DECODES AS HLT IN LINC MODE)
JMP T34A /CONTINUE WITH TEST
TSTPJ2, AND LOC0 /TEST THAT ARRIVAL FROM PUSHJ IOT WAS IN 8-MODE
SEA /AC = 0
HLT /ERR
RSTK /READ STACK ADDRESS REGISTER INTO AC
DCA IMAGE /IMAGE OF AC
TAD IMAGE /IMAGE OF AC
CIA /COMPLIMENT AND INC
TAD K2172 /IMAGE SHOULD = 2172
SEA /AC = 0
HLT /ERR
TAD I STACK+4 /GET UP IF AND OF STORED ON STACK BY PUSH JUMP IOT
CIA /COMPLIMENT AND INC
TAD K3 /STACK+4 SHOULD = 3
SEA /AC = 0
HLT /ERR
CLA CLL /CLEAR AC AND LINC
TAD I STACK+1 /GET PC STORED ON STACK BY PUSH JUMP IOT
CIA /COMPLIMENT AND INC
TAD PJPC2 /STACKK+1 SHOULD = PJPC2
SEA /AC = 0
HLT /ERR
TAD I STACK+1 /GET PC STORED ON STACK BY PUSH JUMP IOT
TAD K2 /INCREMENT BY 2
DCA I STACK+1 /STORE BACK IN LOC STACKK+1
RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
HLT /ERR - RESTORE SKIPPED
SNL /LINC = 1
HLT /ERR
T34A,
HLT /AC = 0
SEA /ERR
RSTK /READ STACK ADDRESS REGISTER INTO AC
DCA IMAGE /IMAGE OF AC
TAD IMAGE /GET IMAGE
CIA /COMPLIMENT AND INC
TAD K2177 /IMAGE SHOULD = 2177
SEA /AC = 0
HLT /ERR

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2120 5721
2121 2220
2122 2220

/
/TEST MAINTENANCE IOT SIMULATION OF
/ A LEVEL 0 INTERRUPT IN LINC MODE
/ --FIRST TIME AN INTERRUPT HAS BEEN SIMULATED--
/

2200
2201
2202
2203
2204
2205
2206
2207
2208
2209
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2214
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2216
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2222
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2224
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2234
2235

JMP I ,+1
T35

/TEST MAINTENANCE IOT SIMULATION OF
/ A LEVEL 0 INTERRUPT IN LINC MODE
/ --FIRST TIME AN INTERRUPT HAS BEEN SIMULATED--
/

*2200
PMODE
JMS I
JMS I
TAD K7000
DCA I TSTA
TAD K37
SMLV
CLA CLL CMA
DCA LOC0
TAD K5600
SSTK
CLA
TAD
SVEC
LINC
LMODE
CLR
LDA I
6000 I T35A+1
STA
1402
CLR
FLO I
HLT
IOB
APION
LDA I
4000
IOB
MAIN1
NOP
HLT

CLRSTK /CLEAR STACK
IOPRES /GENERATE IO PRESET
K7000 /AC = 7000 OR 8-MODE NOP
TSTA /ENABLE INTERRUPT
K37 /AC = 37
SMLV /SET MACHINE LEVEL = 17
CLA CLL CMA /AC = 7777
DCA LOC0 /LOC 0 = 7777
TAD K5600 /AC = 5600
SSTK /SET STACK ADDRESS REGISTER = 5600
CLA /AC = 0
TAD /AC = 6000
SVEC /SET VECTOR ADDRESS REGISTER = 6000
LINC /LINC MODE
LMODE
CLR /CLEAR AC LINK AND MQ
LDA I /AC =
6000 I T35A+1 /JMP TO T35A+1
STA /STORE IN
1402 /LOC 1402 OF CURRENT INSTRUCTION FIELD
CLR /CLEAR AC LINK AND MQ
FLO I /FLO = 0
HLT /ERR-FLO NOT = 0
IOB /EXECUTE 8-MODE IOT
APION /TURN API INTERRUPT SYS ON
LDA I /AC =
4000 /EXECUTE 8-MODE INST
IOB /EXECUTE API INTERRUPT = LEV 0
MAIN1 /SIMULATES ONE MORE INST BEFORE INTERRUPT
NOP /EXECUTES ONE MORE INST BEFORE INTERRUPT
HLT /ERR

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1310					
1311	2236	7402	T35A,	UT37	/RETURN FROM LEV 0 INTERRUPT IN LINC MODE
1312	2237	1460	JMP I		/AC =
1313	2240	4000	SAE I		/4000
1314	2241	0000	HLT		/ERR-AC NOT = 4000
1315	2242	0452	LZE		/LINC = 0
1316	2243	0000	HLT		/ERR-LINC NOT = 0
1317	2244	0005	GAC		/MQ 0-10 TO AC 1-11
1318	2245	0261	ROL I	1	/ROTATE LEFT 1
1319	2246	0475	OLZ I		/SKIP IF MQ 11 = 1
1320	2247	6252	JMP	.43	/MQ BIT 11 = 0
1321	2250	1620	BSE I		/SET AC BIT 0
1322	2251	0001	1		/TO 1
1323	2252	1460	SAE I		/AC =
1324	2253	0000	0		/0
1325	2254	0000	HLT		/ERR-AC NOT = 0 THEREFORE MQ WAS NOT = 0
1326	2255	0474	FLO I		/FLO = 0
1327	2256	0000	HLT		/ERR-FLO NOT = 0
1328	2257	0002	POP		/8-MODE
1329			PMODE		
1330	2260	6774	RSTK		/READ STACK ADDRESS REGISTER INTO AC
1331	2261	3002	DCA	IMAGE	/IMAGE OF AC
1332	2262	1002	TAD	IMAGE	/GET IMAGE
1333	2263	7041	CIA		/COMPLIMENT AND INC
1334	2264	1116	TAD	K2177	/IMAGE SHOULD = 2177
1335	2265	7440	SZA		/AC = 0
1336	2266	7402	HLT		/ERR
1337	2267	6773	RMLV		/READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
1338	2270	3002	DCA	IMAGE	/IMAGE OF AC
1339	2271	1002	TAD	IMAGE	/GET IMAGE
1340	2272	7041	CIA		/COMPLIMENT AND INC
1341	2273	1155	TAD	K7760	/IMAGE SHOULD = 7760
1342	2274	7440	SZA		/AC = 0
1343	2275	7402	HLT		/ERR

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1344
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1346
1347 /TEST NON EXECUTION OF MAINTENANCE IOTS
1348 /WITH API OFF AND FOR NO INTERRUPT
1349 /OCCURRING WHEN MAINTENANCE IOTS ARE ISSUED
1350 /WITH THE AC = 0 AND API ON
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2276 JMS I CLRSTK /CLEAR STACK
2277 JMS I SETUP /SET UP API REGISTERS
2300 DCA I UT16A /LOC UT16A = 0
2301 TAD K37 /AC = 37
2302 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
2303 CLA CLL CMA /AC = 7777
2304 MAIN1 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0 WITH API OFF
2305 NOP /DELAY
2306 NOP /DELAY
2307 MAIN2 /TRY TO SIMULATE AN API INTERRUPT WITH API OFF
2310 NOP /DELAY
2311 NOP /DELAY
2312 CLA CLL /AC = 0
2313 SMLV /SET MACHINE LEVEL = 0
2314 CLA CLL CMA /AC = 7777
2315 APION /TURN API INTERRUPT SYS ON
2316 MAIN1 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0
2317 NOP /MACHINE LEVEL OF 0 SHOULD PREVENT IT
2320 NOP /DELAY
2321 MAIN2 /TRY TO SIMULATE AN API INTERRUPT TO LEVEL 12
2322 NOP /MACHINE LEVEL OF 0 SHOULD PREVENT IT
2323 NOP /DELAY
2324 CLA CLL /AC = 0
2325 TAD K37 /AC = 37
2326 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
2327 CLA CLL /AC = 0
2330 MAIN1 /TRY TO SIMULATE AN API INTERRUPT
2331 NOP /AC = 0 (NO INTERRUPT LEVEL SET) SHOULD PREVENT IT
2332 NOP /DELAY
2333 MAIN2 /TRY TO SIMULATE AN API INTERRUPT
2334 NOP /AC = 0 (NO INTERRUPT LEVEL SET) SHOULD PREVENT IT
2335 NOP /DELAY
2336 JMP I .+1 /JMP TO
2337 T37 /TEST T37

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////
/TEST INTERRUPTS TO EACH LEVEL
////
/
/TEST MAINTENANCE MODE SIMULATION OF
/A LEVEL 0 INTERRUPT TO LOC 6100 WITH MACHINE LEVEL = 17
/AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600-5604
/AFTER SERVICING INTERRUPT RESTOR IOT RETURNS
/PROGRAM SEQUENCE TO START OF T40 TEST
/
      *2400
      T37,      CLRSTK /CLEAR STACK
                JMS I  /LOC UT37A = 0
                DCA I  /SET UP API REGISTERS
                JMS I  /SET UP API REGISTERS
                TAD     /AC = 37
                SMLV    /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = 17
                CLA CLL /CLEAR AC AND LINC
                TAD     /AC = 7000 OR 8-MODE NOP
                DCA I   /ENABLE LEVEL 0 INTERRUPT TO LOC VECT0
                APION   /TURN API INTERRUPT SYSTEM ON
                TAD     /AC = 4000
                MAIN1   /MAINTENANCE MODE SIMULATION OF A LEVEL 0 INTERRUPT
                NOP     /EXECUTES 1 MORE INST AFTER MAIN1 IOT
                HLT     /ERR
      /
/TEST MAINTENANCE MODE SIMULATION OF
/A LEVEL 1 INTERRUPT TO LOC 6102 WITH MACHINE LEVEL = 17
/AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
/AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
/RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T41 TEST
/
      T40,      NOP
                JMS I  /CLEAR STACK
                TAD     /AC = 7000 OR 8-MODE NOP
                DCA I   /ENABLE LEVEL 1 INTERRUPT TO LOC VECT1
                TAD     /AC = 2000
                MAIN1   /MAINTENANCE MODE SIMULATION OF A LEVEL 1 INTERRUPT
                NOP     /EXECUTES 1 MORE INST AFTER MAIN1 IOT
                HLT     /ERR
      /
/TEST MAINTENANCE MODE SIMULATION OF
/A LEVEL 2 INTERRUPT TO LOC 6104 WITH MACHINE LEVEL = 17
/AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
/AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK*1
/RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T42 TEST
/
      T41,      NOP
                JMS I  /CLEAR STACK
                TAD     /AC = 7000 OR 8-MODE NOP
                DCA I   /ENABLE LEVEL 2 INTERRUPT TO LOC VECT2
                TAD     /AC = 1000
                MAIN1   /MAINTENANCE MODE SIMULATION OF A LEVEL 2 INTERRUPT
                NOP     /EXECUTES 1 MORE INST AFTER MAIN1 IOT
                HLT     /ERR
      /

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1486 /
1487 /TEST MAINTENANCE MODE SIMULATION OF
1488 /A LEVEL 6 INTERRUPT TO LOC 6114 WITH MACHINE LEVEL = 17
1489 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1490 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK+1
1491 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T46 TEST
1492 /
1493 /
1494 T45, NOP
1495 JMS I CLRSTK /CLEAR STACK
1496 TAD K7000 /AC = 7000 OR 8-MODE NOP
1497 DCA I VEC6 /ENABLE LEVEL 6 INTERRUPT TO LOC VECT6
1498 TAD K40 /AC = 40
1499 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 6 INTERRUPT
1500 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1501 HLT /ERR
1502
1503 /
1504 /TEST MAINTENANCE MODE SIMULATION OF
1505 /A LEVEL 7 INTERRUPT TO LOC 6116 WITH MACHINE LEVEL = 17
1506 /AND WITH THE MACHINE STATUS SAVED ON LOC 5600-5604
1507 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK+1
1508 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T47 TEST
1509 /
1510 T46, NOP
1511 JMS I CLRSTK /CLEAR STACK
1512 TAD K7000 /AC = 7000 OR 8-MODE NOP
1513 DCA I VEC7 /ENABLE LEVEL 7 INTERRUPT TO LOC VECT7
1514 TAD K20 /AC = 20
1515 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 7 INTERRUPT
1516 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1517 HLT /ERR
1518
1519 /
1520 /TEST MAINTENANCE MODE SIMULATION OF
1521 /A LEVEL 8 INTERRUPT TO LOC 6120 WITH MACHINE LEVEL = 17
1522 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600-5604
1523 /AFTER SERVICING INTERRUPT AND INC PC IN LOC STACKK+1
1524 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO T50 TEST
1525 /
1526 T47, NOP
1527 JMS I CLRSTK /CLEAR STACK
1528 TAD K7000 /AC = 7000 OR 8-MODE NOP
1529 DCA I VEC10 /ENABLE LEVEL 8 INTERRUPT TO LOC VECT10
1530 TAD K10 /AC = 10
1531 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 8 INTERRUPT
1532 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1533 HLT /ERR

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1532 /
1533 /TEST MAINTENANCE MODE SIMULATION OF
1534 /A LEVEL 9 INTERRUPT TO LOC 6122 WITH MACHINE LEVEL = 17
1535 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1536 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1537 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T51 TEST
1538 /
1539 /
1540 2515 7000 NOP
1541 2516 4407 JMS I CLRSTK /CLEAR STACK
1542 2517 1142 TAD K7000 /AC=7000 OR 8-MODE NOP
1543 2520 3572 DCA I VEC11 /ENABLE LEVEL 9 INTERRUPT TO LOC VEC11
1544 2521 1046 TAD K4 /AC=4
1545 2522 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 9 INTERRUPT
1546 2523 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1547 2524 7402 HLT /ERR
1548
1549 /
1550 /TEST MAINTENANCE MODE SIMULATION OF
1551 /A LEVEL 10 INTERRUPT TO LOC 6124 WITH MACHINE LEVEL=17
1552 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1553 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1554 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T52 TEST
1555 /
1556 2525 7000 NOP
1557 2526 4407 JMS I CLRSTK /CLEAR STACK
1558 2527 1142 TAD K7000 /AC=7000 OR 8-MODE NOP
1559 2530 3573 DCA I VEC12 /ENABLE LEVEL 10 INTERRUPT TO LOC VEC12
1560 2531 1044 TAD K2 /AC=2
1561 2532 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 10 INTERRUPT
1562 2533 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1563 2534 7402 HLT /ERR
1564
1565 /
1566 /TEST MAINTENANCE MODE SIMULATION OF
1567 /A LEVEL 11 INTERRUPT TO LOC 6126 WITH MACHINE LEVEL=17
1568 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1569 /AFTER SERVICING THE INTERRUPT AND INC PC IN LOC STACKK+1
1570 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO START OF T53 TEST
1571 /
1572 2535 7000 NOP
1573 2536 4407 JMS I CLRSTK /CLEAR STACK
1574 2537 1142 TAD K7000 /AC=7000 OR 8-MODE NOP
1575 2540 3574 DCA I VEC13 /ENABLE LEVEL 11 INTERRUPT TO LOC VEC13
1576 2541 1043 TAD K1 /AC=1
1577 2542 6051 MAIN1 /MAINTENANCE MODE SIMULATION OF A LEVEL 11 INTERRUPT
1578 2543 7000 NOP /EXECUTES 1 MORE INST AFTER MAIN1 IOT
1579 2544 7402 HLT /ERR

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1570 /
1579 /TEST MAINTENANCE MODE SIMULATION OF
1580 /A LEVEL 12 INTERRUPT TO LOC 6130 WITH MACHINE LEVEL=17
1581 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1582 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1583 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T54 TEST
1584 /
1585 T53,
1586 NOP 7000
1587 JMS I CLRSTK /CLEAR STACK
1588 TAD K7000 /AC=7000 OR 8-MODE NOP
1589 DCA I VEC14 /ENABLE LEVEL 12 INTERRUPT TO LOC VECT14
1590 TAD K4 /AC=4
1591 MAIN2 /MAINTENANCE MODE SIMULATION OF LEVEL 12 INTERRUPT
1592 NOP /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1593 HLT /ERR
1594 JMP I .+1 /JMP TO
1595 T54 /TEST T54
1596
1597 /TEST MAINTENANCE MODE SIMULATION OF
1598 /A LEVEL 13 INTERRUPT TO LOC 6132 WITH MACHINE LEVEL=17
1599 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1600 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1601 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T54 TEST
1602 /
1603 *2600
1604 NOP
1605 T54,
1606 JMS I CLRSTK /CLEAR STACK
1607 TAD K7000 /AC=7000 OR 8-MODE NOP
1608 DCA I VEC15 /ENABLE LEVEL 13 INTERRUPT TO LOC VECT15
1609 TAD K2 /AC=2
1610 MAIN2 /MAINTENANCE MODE SIMULATION OF A LEVEL 13 INTERRUPT
1611 NOP /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1612 HLT /ERR
1613
1614 /TEST MAINTENANCE MODE SIMULATION OF
1615 /A LEVEL 14 INTERRUPT TO LOC 6134 WITH MACHINE LEVEL=17
1616 /AND WITH THE MACHINE STATUS SAVED ON STACK LOC 5600 TO 5604
1617 /AFTER SERVICING THE INTERRUPT AND INC THE PC IN LOC STACKK+1
1618 /RESTOR IOT RETURNS PROGRAM SEQUENCE TO JMP TO START OF T54 TEST
1619 /
1620 T55,
1621 NOP 7000
1622 JMS I CLRSTK /CLEAR STACK
1623 TAD K7000 /AC=7000 OR 8-MODE NOP
1624 DCA I VEC16 /ENABLE LEVEL 14 INTERRUPT TO LOC VECT16
1625 TAD K1 /AC=1
1626 MAIN2 /MAINTENANCE MODE SIMULATION OF A LEVEL 14 INTERRUPT
1627 NOP /EXECUTES 1 MORE INST AFTER MAIN2 IOT
1628 HLT /ERR

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1627 /
1628 /MAINTENANCE MODE SIMULATION OF MULTIPLE LEVEL INTERRUPTS
1629 /TESTS THAT HIGHEST PRIORITY INTERRUPT IS ACCEPTED
1630 /VECTOR ADDRESS REGISTER IS SET TO 6300
1631 /A MAXIMUM OF 16 OCTAL LEVELS OF INTERRUPTS ARE SAVED ON THE STACK
1632 /
1633 T56, CLA CLL /AC=0
1634 TAD K5577 /AC=5577
1635 DCA 10 /LOC 10=5577
1636 TAD K7600 /AC=7600
1637 DCA 11 /LOC11=7600
1638 DCA I 10 /CLEAR STACK STARTING WITH LOCATION 5600
1639 ISZ 11 /DONE 200 TIMES
1640 JMP I-2 /NO-DO IT AGAIN
1641 JMS I SETUP /SET UP API REGISTERS
1642 TAD K37 /AC=37
1643 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
1644 CLA /AC=0
1645 TAD K6300 /AC = 6300
1646 SVEC /SET VECTOR ADDRESS REGISTER=6400
1647 RVEC /CLEAR AC AND LINC
1648 DCA IMAGE /READ VECTOR ADDRESS REGISTER INTO AC
1649 TAD IMAGE /IMAGE OF AC
1650 CIA K1441 /COMPLIMENT AND INC
1651 TAD K1441 /IMAGE SHOULD = 1441
1652 SZA /AC=0
1653 HLT /ERR
1654 TAD K7000 /AC=7000 OR 8-MODE NOP
1655 DCA I MV16 /ENABLE LEVEL 16 INTERRUPT TO LOC MLV16
1656 TAD K1 /AC=1
1657 APION /TURN API INTERRUPT SYSTEM ON
1658 MAIN2 /MAINTENANCE MODE SIMULATION OF A LEVEL 14 INTERRUPT
1659 NOP /EXECUTES 1 MORE INSTRUCTION AFTER MAIN2 107
1660 HLT /ERR
1661 T56A, CLA CLL /AC=0--RESTORE OCCURS TO HERE FROM MV16 ROUTINE
1662 RSTK /READ STACK ADDRESS REGISTER INTO AC
1663 DCA IMAGE /IMAGE OF AC
1664 TAD IMAGE /COMPLIMENT AND INC
1665 CIA K2177 /IMAGE SHOULD=2177
1666 TAD K2177 /AC=0
1667 SZA /ERR
1668 TAD K7402 /AC=7402 OR 8-MODE HLT
1669 DCA I MV16 /DEPOSIT IN LOC MLV16
1670 JMP I .*1 /JMP TO
1671 T57 /TEST T57
1672 /
1673 MV16, MLV16
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757,
*3000
CLA CLL
JMS I CLRSTK
JMS I SETUP
TAD K37
SMLV
CLA CLL CMA
APION
LINC
LMODE
DJR
IOB
MAIN1
NOP
NOP
IOB
MAIN2
NOP
NOP
LOA I
7000
STA
VECT0
JMP
NOP
HLT
LIF
LOA I
7777
IOB
MAIN1
NOP
NOP
IOB
MAIN2
NOP
NOP
JMP
LOA I
7000

/TEST THAT DJR LIF AND CIF INSTRUCTIONS
/WHEN ISSUED WILL PREVENT API INTERRUPTS
/UNTIL A JMP INSTRUCTION IS EXECUTED
/

/CLEAR AC AND LINC
/CLEAR STACK
/SET UP API REGISTERS
/AC=37
/CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
/AC=7777
/TURN API INTERRUPT SYSTEM ON
/LINC MODE

/DISABLE JMP RETURN
/TRY TO EXECUTE 8-MODE INST
/TRY TO SIMULATE API INTERRUPT TO LEVEL 0
/EXECUTE 1 MORE INST AFTER MAIN1 IOT
/DJR SHOULD PREVENT INTERRUPT
/EXECUTE 8-MODE INST
/TRY TO SIMULATE API INTERRUPT TO LEVEL 12
/EXECUTE 1 MORE INST AFTER MAIN2 IOT
/DJR SHOULD PREVENT INTERRUPT
/AC=
/7000 OR 8-MODE NOP
/STORE IN LOC
/VECT0
/ENABLE INTERRUPTS BY ISSUING A JMP INST
/EXECUTE 1 INST THAN INTERRUPT TO LEVEL 0 (HIGHEST PRIORITY)
/ERR-DID NOT INTERRUPT
/SET INSTRUCTION FIELD 1--RETURN AFTER RESTORE IOT
/AC=
/7777
/EXECUTE AN 8-MODE INST
/TRY TO SIMULATE API INTERRUPT TO LEVEL 0
/EXECUTE 1 MORE INST AFTER MAIN1 IOT
/LIF INST SHOULD PREVENT INTERRUPT
/EXECUTE AN 8-MODE INST
/TRY TO SIMULATE API INTERRUPT TO LEVEL 12
/EXECUTE 1 MORE INST AFTER MAIN2 IOT
/LIF INST SHOULD PREVENT INTERRUPT
/2 JMP INSTRUCTIONS ARE NECESSARY AFTER A LIF INST IN LINC MODE
/TO ENABLE THE INTERRUPTS AGAIN
/AC=
/7000 OR AN 8-MODE NOP

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1724						
1725	3047	1040	STA		/STORE IN LOC	
1726	3050	6140	VECT0		/VECT0	
1727	3051	7052	JMP	.+1	/ENABLE INTERRUPTS BY ISSUING A JMP INST	
1728	3052	0000	HLT		/ERR-DID NOT INTERRUPT IMMEDIATELY AFTER JMP	
1729	3053	0002	PDP		/8-MODE	
1730			PMODE			
1731	3054	7340	CLA CLL CMA		/AC=7777	
1732	3055	6202	CIF		/CHANGE INSTRUCTION FIELD	
1733	3056	6051	MAIN1		/TRY TO SIMULATE AN API INTERRUPT TO LEVEL 0	
1734	3057	7000	NOP		/EXECUTE 1 MORE INST AFTER MAIN1 IOT	
1735	3060	7000	NOP		/CIF INST SHOULD PREVENT INTERRUPT	
1736	3061	6052	MAIN2		/TRY TO SIMULATE AN API INTERRUPT TO LEVEL 12	
1737	3062	7000	NOP		/EXECUTE 1 MORE INST AFTER MAIN2 IOT	
1738	3063	7000	NOP		/CIF INST SHOULD PREVENT INTERRUPT	
1739	3064	1142	TAD	K7000	/AC=7000 OR 8-MODE NOP	
1740	3065	3561	DCA I	VECT0	/STORE IN LOC VECT0	
1741	3066	5267	JMP	.+1	/ENABLE INTERRUPTS BY ISSUING A JMP INST	
1742	3067	7000	NOP		/EXECUTE 1 MORE INST THAN INTERRUPT TO LEVEL 0 (HIGHEST PRIORITY)	
1743	3070	7402	HLT		/ERR	

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1744 /
1745 /TEST THAT A DJR INSTRUCTION WHEN ISSUED AND
1746 /NORMAL OPERATION IS REENABLED WITH A JMP INST
1747 /WILL ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
1748 /
1749 /T60,
1750 /CLEAR AC AND LINC
1751 JMS I CLRSTK
1752 JMS I SETUP
1753 /SET UP API REGISTERS
1754 /AC=37
1755 /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
1756 /AC=0
1757 /TURN API INTERRUPT SYSTEM ON
1758 /LINC MODE
1759 /EXECUTE 8-MODE INST
1760 /PUSH JUMP WITH FIELD BITS=0
1761 /TO LOC T60A
1762 /ERR--RESTORE IOT RET PROC SEQ TO THIS LOC AFTER ALTERED TO A NOP
1763 /8-MODE
1764 /AC = 0
1765 /CHANGE LOC .-3 BACK TO A LINC MODE HLT
1766 /JMP TO
1767 /TEST T61
1768 /
1769 /ARRIVE HERE FROM PUSH JUMP
1770 /8-MODE
1771 /
1772 /GET PC STORED IN STACK+1
1773 /COMPLIMENT AND INC
1774 /PC STORED IN STACK+1 SHOULD SET T60A-6
1775 /AC=0
1776 /ERR
1777 /LINC MODE
1778 /
1779 /DISABLE JUMP RETURN
1780 /AC=
1781 /16 OR A LINC MODE NOP
1782 /STORE IN LOC
1783 /T60A-6
1784 /ENABLE RESTORE IOT BY ISSUING A JMP INST
1785 /EXECUTE 8-MODE INST
1786 /RESTORE
1787 /ERR
1788 /
1789 /
1790 /T60AM6, T60A-6
1791 /

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/
/TEST THAT A LIF INSTRUCTION WHEN ISSUED
/AND NORMAL OPERATION IS REENABLED WITH A JMP INST
/Will ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
/

      *3200
      CLA CLL
      JMS I  CLRSTK
      JMS I  SETUP
      TAD     K37
      SMLV
      CLA
      APION
      LINC
      LMODE
      IOB
      PJAIO
      2000:T61A
      HLT
      POP
      PMODE
      CLA
      DCA
      JMP
      LMODE
      CLR
      POP
      PMODE
      TAD I
      CIA
      TAD
      SZA
      HLT
      LINC
      LMODE
      LIF
      JMP
      NOP
      LDA I
      16
      STA
      T61A-5
      JMP
      IOB
      RES
      HLT

      T61,
      3200
      3201
      3202
      3203
      3204
      3205
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      0000

      /CLEAR AC AND LINC
      /CLEAR STACK
      /SET UP API REGISTERS
      /AC=37
      /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
      /AC=0
      /TURN API INTERRUPT SYSTEM ON
      /LINC MODE
      /EXECUTE 8-MODE INST
      /PUSH JUMP WITH FIELD BITS=0
      /TO LOC T61A
      /ERR--RESTORE IOT RET PROG SEQ TO THIS LOC AFTER ALTERED BY A NOP
      /8-MODE
      /AC = 0
      /CHANGE LOC .-3 BACK TO A LINC MODE HLT
      /EXECUTE TEST T62
      /ARRIVE HERE FROM PUSH JUMP
      /8-MODE
      STACK+1 /GET PC STORED IN LOC STACKK+1
      T61A-5 /COMPLIMENT AND INC
      /PC STORED IN LOC STACKK+1 SHOULD=T61A-3
      /AC=0
      /ERR
      /LINC MODE
      1 /LOAD INSTRUCTION FIELD 1
      .+1 /2 JMP INSTRUCTIONS ARE NECESSARY AFTER A LIP INST IN LINC MODE
      /TO ENABLE RESTORE IOT
      /AC =
      /16 OR A LINC MODE NOP
      /STORE NOP IN LOC
      /T61A-5
      .+1 /ENABLE RESTOR IOT BY DOING A JMP INST
      /EXECUTE 8-MODE INST
      /RESTORE
      /ERR

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1839 /
1840 /TEST THAT A CIF INSTRUCTION WHEN ISSUED
1841 /AND NORMAL OPERATION IS REENABLED WITH A JMP INST
1842 /WILL ALLOW A RESTORE IOT TO BE EXECUTED IMMEDIATELY
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3243 7300 PMODE
3244 4427 CLA CLL /CLEAR AC AND LINC
3245 4406 JMS I CLRSTK /CLEAR STACK
3246 1093 JMS I SETUP /SET UP API REGISTERS
3247 6772 TAD K37 /AC=37
3248 7200 SHLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL=17
3249 6006 CLA /AC=0
3250 6760 APION /TURN API INTERRUPT SYSTEM ON
3251 3262 PJAI0 /EXECUTE A PUSHJ IOT WITH FIELD BITS=0
3252 7402 T62A /TO LOC T62A
3253 7200 HLT /ERR
3254 7200 CLA /RESTOR IOT RETURNS PROGRAM SEQUENCE TO THIS POINT
3255 1150 TAD K7402 /AC = 7402 OR AN 8-MODE HLT
3256 3254 DCA , -3 /STORE IN LOC .-3
3257 3403 JMP I , +1 /CONTINUE WITH
3258 7300 T63 /TEST T63
3259 1433 CLA CLL /ARRIVE HERE FROM PUSHJ IOT
3260 7041 TAD I STACK+1 /GET PC STORED IN LOC STACKK+1
3261 1276 CIA /COMPLIMENT AND INC
3262 6202 TAD T62AM6 /PC STORED IN LOC STACKK+1 SHOULD=T62A-6
3263 7300 CIF /CHANGE INSTRUCTION FIELD
3264 1142 CLA CLL /AC=0
3265 3254 TAD K7000 /AC = 7000 OR AN 8-MODE NOP
3266 5273 DCA T62A-6 /DEPOSIT IN LOC T62A-6
3267 6771 JMP , +1 /ENABLE RESTORE IOT BY DOING A JMP INST
3268 7402 RES /RESTORE
3269 3213 HLT /ERR
3270 3254 T61AM5, T61A-5
3271 3254 T62AM6, T62A-6
3272 3213 /
3273 3254 /IF AN 8 MODE HALT WERE ENCOUNTERED IN
3274 3213 /LINC MODE IT WOULD DECODE AS A JMP TO
3275 3254 /LOC 1402 OF THE CURRENT LINC INSTRUCTION FIELD
3276 3213 /THUS TO HALT THE PROGRAM IN THIS EVENT
3277 3254 /WE SET LOC 1402 = 0 A LINC MODE HALT
3278 3213 /
3279 3254 *3402
3280 3213 LMODE
3281 3254 0
3282 3213 PMODE
3283 3254 UT37,
3284 3213 0
3285 3254 0000
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/
/ RUN RANDOM NUMBER PATTERN WITH MAIN1 AND MAIN2 IOTS
/ TO TEST THAT INHIBIT OF INTERRUPTS BY THE SMLV IOT
/ WORKS FOR A RANDOM SELECTION OF NUMBERS
/ 400 RANDOM NUMBER COMBINATIONS WILL BE TESTED FOR
/ EACH POSSIBLE SETTING OF THE MACHINE LEVEL REGISTER
/
/ T64,
3421 7300 /CLEAR AC AND LINC
3422 4407 JMS I CLRSTK /CLEAR STACK
3423 4406 JMS I SETUP /SET UP API REGISTERS
3424 6006 APION /TURN API INTERRUPT SYSTEM ON
3425 1123 TAD K3500 /AC = 3500
3426 6777 SVEC /SET VECTOR ADDRESS REGISTER = 3500
3427 7340 CLA CLL /AC = 7777
3430 3355 DCA MASK1 /MASK FOR RAN1
3431 1047 TAD K7 /AC = 7
3432 3356 DCA MASK2 /MASK FOR RAN2
3433 3010 DCA 10 /LOC 10 = 0
3434 7300 CLA CLL /CLEAR AC AND LINC
3435 1147 TAD K7400 /AC = 7400
3436 3011 DCA 11 /LOC 11 = 7400
3437 1010 TAD 10 /AC = LOC 10
3440 6772 SMLV /CLEAR STACK AND VECTOR FIELD BITS AND SET MACHINE LEVEL = LOC 10
3441 4424 JMS I RAN /GENERATE RANDOM NUMBERS RAN1 AND RAN2
3442 1029 TAD RAN1 /AC = RAN1
3443 0355 AND MASK1 /MASK
3444 6051 MAIN1 /TRY TO SIMULATE A MAINTENANCE MODE INTERRUPT
3445 7000 NOP
3446 7000 NOP
3447 7200 CLA /AC = 0
3450 1026 TAD RAN2 /AC = RAN2
3451 0356 AND MASK2 /MASK
3452 6052 MAIN2 /TRY TO SIMULATE A MAINTENANCE MODE INTERRUPT
3453 7000 NOP
3454 7000 NOP
3455 2011 ISZ 11 /INC LOC 11--IF 0 DONE 400 RANDOM COMBINATIONS ON PARTICULAR MACHINE LEVEL SETTING
3456 0241 JMP MORE*5 /NOT DONE
3457 7300 CLA CLL /CLEAR AC AND LINC
3460 1355 RAR MASK1 /AC = MASK1
3461 7010 RAR MASK1 /ROTATE AC RIGHT 1
3462 3355 DCA MASK1 /STORE BACK IN LOC MASK1
3463 7430 SZL /SKIP IF LINC = 0
3464 9273 JMP .+7 /DO NOT ALTER MASK2 YET
3465 7300 CLA CLL /CLEAR AC AND LINC
3466 1356 TAD MASK2 /AC = MASK2
3467 7010 RAR MASK2 /ROTATE AC RIGHT 1
3470 3356 DCA MASK2 /STORE BACK IN LOC MASK2
3471 7420 SNL /SKIP IF LINC = 1
3472 5336 JMP T65 /DONE GO TO TEST T65
3473 7300 CLA CLL /CLEAR AC AND LINC
3474 2010 ISZ 10 /INCREMENT LOC 10
3475 5234 JMP MORE /CHANGE MACHINE LEVEL AND DO AGAIN
3476 7402 HLT /SHOULD NEVER GET HERE

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DIAL10 V003

/ VECTOR ADDRESS POINTER TABLE
/ USED WITH TEST T65
/ SHOULD NEVER GET AN INTERRUPT
/

*3500

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7402	3501	7402	7402
HLT	3502	7402	7402
HLT	3503	7402	7402
HLT	3504	7402	7402
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HLT	3506	7402	7402
HLT	3507	7402	7402
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HLT	3533	7402	7402
HLT	3534	7402	7402
HLT	3535	7402	7402

/ UPPER MEMORY INTERRUPT AND PUSH JUMP-RESTORE TEST
/ ONLY EXECUTES IF SNS SW 0 IS SET AND
/ RSW BITS 0-11 NOT = 0 OR SET TO NUMBER OF SEQUENTIAL 4K SECTIONS TO BE TESTED
/

3536	7300	CLA CLL	/CLEAR AC AND LINC
3537	6141	LINC	/LINC MODE
		LMODE	
3540	0440	SNS 0	/SENSE SWITCH 0 = 1
3541	7545	JMP T66	/NO-GO TO NEXT TEST
3542	0002	POP	/CHANGE TO 8-MODE
		PMODE	
3543	4757	EXTME	/GO TO EXTME (EXTENDED MEMORY TEST SECTION) ROUTINE
3544	5345	JMP T66	/GO TO TEST T66

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2017 /
2018 /
2019 /
2020 /
2021 /
2022 /
2023 /
2024 /
2025 /
2026 /
2027 /
2028 /
2029 /
2030 /
2031 /
2032 /
2033 /
2034 /

3545 0002 PDP /CHANGE TO 8-MODE
3546 7300 CLA CLL /CLEAR AC AND LINC
3547 2017 ISZ 17 /INCREMENT LOCATION 17
3550 9353 JMP .+3 /JMP .+3
3551 5752 JMP I .+1 /LOC 17 = 0-RETURN TO
3552 0400 T0 /TEST T0
3553 9754 JMP I .+1 /LOC 17 NOT = 0-RETURN TO
3554 0431 T1 /TEST T1

3555 0000 MASK1, 0
3556 0000 MASK2, 0
3557 4001 EXTME, EXTME /EXTENDED MEMORY TEST

////////////////////
/-----END OF PROGRAM-----/
////////////////////
/
/

```



```

2188 /
2189 /TEST STACK INCREMENTING THROUGH ALL 4096 LOC
2190 /OF MEMORY FIELD 1
2191 /THE STACK ADDRESS REGISTER IS INITIALLY SET TO 0000
2192 /WITH THE STACK FIELD BITS SET TO 1
2193 /AFTER EACH PUSH JUMP IN FIELD 0 THE MACHINE STATUS
2194 /SAVED ON THE STACK IS VERIFIED
2195 /
2196
2197
2198
2199
2200
2201
2202
2203
2204
2205
2206
2207
2208
2209
2210
2211
2212
2213
2214
2215
2216
2217
2218
2219
2220
2221
2222
2223
2224
2225
2226
2227
2228
2229
2230
2231
2232
2233
2234
2235
2236
2237
2238
2239
2240
2241
2242

4200
0000
7300
4201
4202
4203
4204
4205
4206
4207
4210
4211
4212
4213
4214
4215
4216
4217
4220
4221
4222
4223
4224
4225
4226
4227
4230
4231
4232
4233
4234
4235
4236
4237
4240
4241
4242
4243
4244
4245
4246
4247
4250
4251

*4200
FLO1P, 0
CLA CLL
IOF
TAD K1020
SHLV
CLA
SSTK
TAD K6000
SVEC
CLA CMA 10
DCA 11
DCA 11
LINC
LMODE
CLR
POP
PHODE
CLA CLL 11
TAD K5
TAD 11
DCA PJ10
ESTKT
SEA
HLT
SEL
HLT
RSTK
DCA 11
TAD
TAD K1
TAD 11
SEA
HLT
DCA 11
TAD
TAD K1
TAD 11
LINC
LMODE
FLO 1
HLT
QAC
ROL 1 1
QLE 1
JMP
BSE 1 1
0001

/CONTAINS RETURN JMP ADD TO EXTREM ROUTINE
/CLEAR AC AND LINC
/TURN ALL INTERRUPT SYSTEMS OFF
/AC=1020
/SET STACK FIELD BITS = 1
/AC = 0
/SET STACK ADDRESS REGISTER = 0
/AC = 6000
/SET VECTOR ADDRESS REGISTER = 6000
/AC = 7777
/LOC 10 = 7777
/LOC 11 = 0000
/LINC MODE
/CLEAR AC LINC AND HQ
/8-MODE
/CLEAR AC AND LINC
/AC = CONTENTS OF LOC 11
/ADD 5
/DEPOSIT BACK IN LOC 11
/PUSH JUMP IN FIELD 0
/TO LOC ESTKT
/AC = 0--RESTORES OCCURE TO HERE--
/ERR-AC NOT = 0
/LINC = 0
/ERR-LINC NOT = 0
/READ STACK ADDRESS REGISTER INTO AC
/AC TO LOC IMAGE
/GET IMAGE
/ADD 1
/STACK ADD SHOULD = CONTENTS LOC 11
/AC = 0
/ERR-STACK ADD NOT = CONTENTS LOC 11
/CLEAR LINC
/LINC MODE
/FLO = 0
/ERR-FLO NOT = 0
/HQ 0-10 TO AC 1-11
/ROTATE LEFT 1
/SKIP IF HQ 11 = 1
/CONTINUE-AC = HQ
/SET AC BIT 0
/TO 1

```

DIAL10	V003	15-SEP-71	0110	PAGE 50-1
2243	4252	1460	SAE I	/SKIP IF AC
2244	4253	0000	0	/AC = 0
2245	4254	0000	HLT	/ERR-MQ RESTORED FROM STACK NOT = 0
2246	4255	0500	IOB	/EXECUTE 8-MODE INST
2247	4256	0224	RIF	/READ INSTRUCTION FIELD
2248	4257	1460	SAE I	/AC =
2249	4260	0004	4	/AC =
2250	4261	0000	HLT	/ERR-INSTRUCTION FIELD 2 NOT SET
2251	4262	0011	CLR	/CLEAR AC LINC AND MQ
2252	4263	0500	IOB	/EXECUTE 8-MODE INST
2253	4264	0214	ROF	/READ DATA FIELD
2254	4265	1460	SAE I	/AC =
2255	4266	0006	6	/AC =
2256	4267	0000	HLT	/ERR-DATA FIELD 0 NOT SET
2257	4270	0011	CLR	/CLEAR AC LINC AND MQ
2258	4271	0002	POP	/8-MODE
2259	4272	0774	PMODE	/READ STACK ADDRESS REG INTO AC
2260	4273	3002	RSTK	/IMAGE OF AC
2261	4274	1002	DCA	/GET IMAGE
2262	4275	1043	TAD	/ADD 1
2263	4276	1011	TAD	/CONTENTS OF LOC 11 SHOULD=STACK ADD
2264	4277	7440	SEA	/AC=0
2265	4300	7402	HLT	/ERR-STACK ADD NOT = CONTENTS LOC 11
2266	4301	5347	JMP	/RESTOR ANOTHER LEVEL OF STATUS ON STACK
2267	4302	0774	RSTK	/READ STACK ADDRESS REGISTER INTO AC
2268	4303	3002	DCA	/IMAGE OF AC
2269	4304	1002	TAD	/GET IMAGE
2270	4305	1043	TAD	/ADD 1
2271	4306	1011	TAD	/CONTENTS OF LOC 11 SHOULD = STACK ADD
2272	4307	7440	SEA	/AC = 0
2273	4310	7402	HLT	/ERR-STACK ADD NOT = CONTENTS LOC 11
2274	4311	6211	ODF	/CHANGE DATA FIELD TO 1
2275	4312	1410	TAD	/AC FROM STACK TO AC
2276	4313	7440	SEA	/AC = 0
2277	4314	7402	HLT	/ERR-AC STORED ON STACK NOT = 0
2278	4315	1410	TAD	/PC FROM STACK TO AC
2279	4316	3002	DCA	/IMAGE OF AC
2280	4317	1002	TAD	/GET IMAGE
2281	4320	7041	CIA	/COMPLIMENT AND INCREMENT
2282	4321	1363	TAD	/ADDRESS OF RET PC ON STACK SHOULD = ERES
2283	4322	7440	SEA	/AC = 0
2284	4323	7402	HLT	/ERR-RETURN PC ON STACK NOT = ADD ERES
2285	4324	7402	TAD	/MODE FLO LINC AND MACH LEVEL FROM STACK TO AC
2286	4325	7440	SEA	/AC = 0
2287	4326	7402	HLT	/ERR-EXAMINE AC
2288	4327	1410	TAD	/MQ FROM STACK TO AC
2289	4330	7440	SEA	/AC = 0
2290	4331	7402	HLT	/ERR-MQ STORED ON STACK NOT = 0
2291	4332	1410	TAD	/UP IF AND OF FROM STACK TO AC
2292	4333	3002	DCA	/IMAGE OF AC
2293	4334	1002	TAD	/GET IMAGE
2294	4335	7041	CIA	/COMPLIMENT AND INC
2295	4336	1360	TAD	/LOC IMAGE SHOULD = 103
2296	4337	7440	SEA	/AC = 0

```

2298 4340 7402 HLT /ERR-EXAMINE LOC IMAGE
2299 4341 6201 CDF /CHANGE TO DATA FIELD 0
2300 4342 7300 CLA CLL /CLEAR AC AND LINC
2301 4343 1010 TAD 10 /CONTENTS OF LOC 10 TO AC
2302 4344 1357 TAD K5 /ADD 5
2303 4345 7420 SNL /SKIP IF LINC SET
2304 4346 9217 JMP DO /DO AGAIN-HAVE NOT FILLED FIELD 1
2305 4347 7300 CLA CLL /CLEAR AC AND LINC
2306 4350 1011 TAD 11 /CONTENTS OF LOC 11 TO AC
2307 4351 1362 TAD M5 /SUBTRACT 5
2308 4352 7440 SZA /IF AC = 0
2309 4353 5600 JMP I FLD1P /RETURN TO EXTREM ROUTINE
2310 4354 3011 DCA 11 /DEPOSIT BACK IN LOC 11
2311 4355 6771 RES /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
2312 4356 7402 HLT /ERR-RES IOY DID NOT EXECUTE
2313 /
2314 K5, 5
2315 K103, 103
2316 K1020, 1020
2317 M5, -5
2318 ERE, ERES
2319 /
2320 /EXTENDED MEMORY TEST
2321 /RELOCATEABLE PORTION OF TEST T65
2322 /
2323 4400 *4400
2324 7300 CLA CLL
2325 9236 JMP EXTST
2326 7402 HLT
2327 7402 HLT
2328 7402 HLT
2329 7402 HLT
2330 7402 HLT
2331 7402 HLT
2332 7402 HLT
2333 7402 HLT
2334 7402 HLT
2335 7402 HLT
2336 7402 HLT
2337 7402 HLT
2338 7402 HLT
2339 7402 HLT
2340 7402 HLT
2341 7402 HLT
2342 7402 HLT
2343 7402 HLT
2344 7402 HLT
2345 7402 HLT
2346 7402 HLT
2347 7402 HLT
2348 7402 HLT
2349 7402 HLT
2350 7402 HLT
2351 7402 HLT
2352 7402 HLT

```

/	DIAL10	V003	15-SEP-71	0110	PAGE 50-3	
2353	4435	7402	EXTTST, RIF			
2354	4436	6224	HLT			/READ INSTRUCTION FIELD INTO AC
2355	4437	7041	CIA			/COMPLIMENT AND INC
2356	4440	1374	TAD	EIF		/INSTRUCTION FIELD SHOULD=LOC EIF
2357	4441	7440	SEA			/AC=0
2358	4442	7402	HLT			/ERR - INSTRUCTION FIELD NOT=0
2359	4443	6214	RDP			/READ DATA FIELD INTO AC
2360	4444	7440	SEA			/AC=0
2361	4445	7402	HLT			/ERR - DATA FIELD NOT=0

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2362      4446      6773      RMLV
2363      4447      1372      TAD
2364      4450      7440      SEA
2365      4451      7402      HLT
2366      4452      4394      JMS
2367      4453      1342      TAD
2368      4454      7041      CIA
2369      4455      1370      TAD
2370      4456      7440      SEA
2371      4457      7402      HLT
2372      4460      1343      TAD
2373      4461      7041      CIA
2374      4462      1375      TAD
2375      4463      7440      SEA
2376      4464      7402      HLT
2377      4465      1344      TAD
2378      4466      7041      CIA
2379      4467      1366      TAD
2380      4470      7440      SEA
2381      4471      7402      HLT
2382      4472      1345      TAD
2383      4473      7440      SEA
2384      4474      7402      HLT
2385      4475      1346      TAD
2386      4476      7041      CIA
2387      4477      1367      TAD
2388      4500      7440      SEA
2389      4501      7402      HLT
2390      4502      6760      EALTP,
2391      4503      4513      EXTPJ
2392      4504      7402      HLT
2393      4505      4394      JMS
2394      4506      1343      TAD
2395      4507      7001      IAC
2396      4510      3343      DCA
2397      4511      6771      RES
2398      4512      7402      HLT
2399      4513      7440      SEA
2400      4514      7402      HLT
2401      4515      1347      TAD
2402      4516      7440      SEA
2403      4517      7402      HLT
2404      4520      1350      TAD
2405      4521      7041      CIA
2406      4522      1376      TAD
2407      4523      7440      SEA
2408      4524      7402      HLT
2409      4525      1391      TAD
2410      4526      7440      SEA
2411      4527      7402      HLT

/READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
/LOC EFCD SHOULD = 2'S COMPLIMENT OF AC
/AC=0
/ERR -
/ROUTINE TO TEST STACK ADDRESS REGISTER
/AC=CONTENTS OF LOC EXTSTK
/COMPLIMENT AND INC
/LOC EXTSTK SHOULD=4000
/AC=0
/ERR-LOC EXTSTK NOT=4000
/AC=CONTENTS OF LOC EXTSTK+1
/COMPLIMENT AND INC
/LOC EXTSTK+1 SHOULD=CONTENTS OF LOC EXTRM1
/AC=0
/ERR-LOC EXTSTK+1 NOT=CONTENTS OF LOC EXTRM1
/AC=CONTENTS OF LOC EXTSTK+2
/COMPLIMENT AND INC
/LOC EXTSTK+2 SHOULD = 17
/AC=0
/ERR-LOC EXTSTK+2 NOT = 17
/AC=CONTENTS OF LOC EXTSTK+3
/AC=0
/ERR-LOC EXTSTK+3 NOT = 0
/AC=CONTENTS OF LOC EXTSTK+4
/COMPLIMENT AND INC
/LOC EXTSTK+4 SHOULD = 102
/AC=0
/ERR-LOC EXTSTK+4 NOT = 102
/ALTERED INST--PUSH JUMP WITHIN CURRENT FIELD
/TO LOC EXTPJ
/ERR-PJA IOT DID NOT EXECUTE
/TEST STACK ADDRESS REGISTER
/AC = CONTENTS OF LOC EXTSTK+1
/INC
/DEPOSIT BACK IN LOC EXTSTK+1
/RESTORE MACHINE TO LAST LEVEL SAVED ON STACK
/ERR-RESTORE IOT DID NOT EXECUTE
/AC SHOULD = 0
/ERR-AC NOT = 0
/AC SAVED ON STACK AT LOC EXTSTK+5 = 0
/SKIP IF = 0
/ERR--AC SAVED ON STACK NOT = 0
/AC=CONTENTS OF LOC EXTSTK+6
/COMPLIMENT AND INC
/LOC EXTSTK+6 SHOULD=ADDRESS EXTPJ-7
/AC=0
/ERR-
/AC=CONTENTS OF LOC EXTSTK+7
/AC=0
/ERR-CONTENTS OF LOC EXTSTK+7 NOT=0

```



```

2462 /
2463 /STACK ADDRESS TABLE
2464 /LOCATION 5600 TO 5777
2465 /
2466
2467 5600
2468
2469 /FIRST LEVEL SAVED ON STACK
2470 /
2471 STACK, 0 /AC
2472 0 /PC
2473 0 /MODE FLO LINC MACHINE LEVEL
2474 0 /MQ
2475 0 /UF IF DF
2476 /
2477 /SECOND LEVEL SAVED ON STACK
2478 /
2479 0 /AC
2480 0 /PC
2481 0 /MODE FLO LINC MACHINE LEVEL
2482 0 /MQ
2483 0 /UF IF DF
2484 /
2485 /THIRD LEVEL SAVED ON STACK
2486 /
2487 0 /AC
2488 0 /PC
2489 0 /MODE FLO LINC MACHINE LEVEL
2490 0 /MQ
2491 0 /UF IF DF
2492 /
2493 /FOURTH LEVEL SAVED ON STACK
2494 /
2495 0 /AC
2496 0 /PC
2497 0 /MODE FLO LINC MACHINE LEVEL
2498 0 /MQ
2499 0 /UF IF DF
2500 /
2501 /FIFTH LEVEL SAVED ON STACK
2502 /
2503 0 /AC
2504 0 /PC
2505 0 /MODE FLO LINC MACHINE LEVEL
2506 0 /MQ
2507 0 /UF IF DF

```



```
2508 /
2509 /SIXTH LEVEL SAVED ON STACK
2510 /
2511 /
2512 5631 0000 /AC
2513 5632 0000 /PC
2514 5633 0000 /MODE FLO LINC MACHINE LEVEL
2515 5634 0000 /MQ
2516 5635 0000 /UF IF DF
2517 /
2518 /SEVENTH LEVEL SAVED ON STACK
2519 /
2520 5636 0000 /AC
2521 5637 0000 /PC
2522 5640 0000 /MODE FLO LINC MACHINE LEVEL
2523 5641 0000 /MQ
2524 5642 0000 /UF IF DF
2525 /
2526 /EIGHTH LEVEL SAVED ON STACK
2527 /
2528 5643 0000 /AC
2529 5644 0000 /PC
2530 5645 0000 /MODE FLO LINC MACHINE LEVEL
2531 5646 0000 /MQ
2532 5647 0000 /UF IF DF
2533 /
2534 /NINTH LEVEL SAVED ON STACK
2535 /
2536 5650 0000 /AC
2537 5651 0000 /PC
2538 5652 0000 /MODE FLO LINC MACHINE LEVEL
2539 5653 0000 /MQ
2540 5654 0000 /UF IF DF
2541 /
2542 /TENTH LEVEL SAVED ON STACK
2543 /
2544 5655 0000 /AC
2545 5656 0000 /PC
2546 5657 0000 /MODE FLO LINC MACHINE LEVEL
2547 5660 0000 /MQ
2548 5661 0000 /UF IF DF
2549 /
2550 /ELEVENTH LEVEL SAVED ON STACK
2551 /
2552 5662 0000 /AC
2553 5663 0000 /PC
2554 5664 0000 /MODE FLO LINC MACHINE LEVEL
2555 5665 0000 /MQ
2556 5666 0000 /UF IF DF
```

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```

2557 /
2558 /TWELVETH LEVEL SAVED ON STACK
2559 /
2560 /
2561 0000 /AC
2562 0000 /PC
2563 0000 /MODE FLO LINC MACHINE LEVEL
2564 0000 /MQ
2565 0000 /UF IF DP
2566 /
2567 /THIRTEENTH LEVEL SAVED ON STACK
2568 /
2569 0000 /AC
2570 0000 /PC
2571 0000 /MODE FLO LINC MACHINE LEVEL
2572 0000 /MQ
2573 0000 /UF IF DP
2574 /
2575 /FOURTEENTH LEVEL SAVED ON STACK
2576 /
2577 0000 /AC
2578 0000 /PC
2579 0000 /MODE FLO LINC MACHINE LEVEL
2580 0000 /MQ
2581 0000 /UF IF DP
2582 /
2583 /FIFTEENTH LEVEL SAVED ON STACK
2584 /
2585 0000 /AC
2586 0000 /PC
2587 0000 /MODE FLO LINC MACHINE LEVEL
2588 0000 /MQ
2589 0000 /UF IF DP
2590 /
2591 /THE LENGTH OF THE STACK IS ONLY
2592 /LIMITED BY CORE -- THIS DIAGNOSTIC
2593 /USES ONLY FIFTEEN LEVELS ON STACK
2594 /

```

```

2595 /
2596 /
2597 /ENTERED FROM T35 TEST
2598 /FIRST EXECUTION OF SIMULATED INTERRUPT
2599 /TO LEVEL 0 ONLY
2600 /
2601 *6000
2602 6000 /LEVEL 0 INTERRUPT
2603 6001 /JMP TO SUBROUTINE
2604 6002 /LEVEL 1 INTERRUPT
2605 6003 HLT
2606 6004 /LEVEL 2 INTERRUPT
2607 6005 HLT
2608 6006 /LEVEL 3 INTERRUPT
2609 6007 HLT
2610 6010 /LEVEL 4 INTERRUPT
2611 6011 HLT
2612 6012 /LEVEL 5 INTERRUPT
2613 6013 HLT
2614 6014 /LEVEL 6 INTERRUPT
2615 6015 HLT
2616 6016 /LEVEL 7 INTERRUPT
2617 6017 HLT
2618 6020 /LEVEL 8 INTERRUPT
2619 6021 HLT
2620 6022 /LEVEL 9 INTERRUPT
2621 6023 HLT
2622 6024 /LEVEL 10 INTERRUPT
2623 6025 HLT
2624 6026 /LEVEL 11 INTERRUPT
2625 6027 HLT
2626 6030 /LEVEL 12 INTERRUPT
2627 6031 HLT
2628 6032 /LEVEL 13 INTERRUPT
2629 6033 HLT
2630 6034 /LEVEL 14 INTERRUPT
2631 6035 HLT

```

```

2632 /
2633 /TEST API REGISTERS AND PC STORED IN LOC STACK+1
2634 /INCREMENT PC IN LOC STACK+1 AND ISSUE RESTOR IOT
2635 /
2636 /TST,
2637 6236 7402 HLT
2638 6237 7300 CLA CLL
2639 6240 1150 TAD K7402
2640 6241 3236 DCA TST
2641 6242 6775 RVEC
2642 6243 3002 DCA IMAGE
2643 6244 1002 TAD IMAGE
2644 6245 7041 CIA
2645 6246 1074 TAD K1741
2646 6247 7440 SEA
2647 6250 7402 HLT
2648 6051 6774 RSTK
2649 6052 3002 DCA IMAGE
2650 6053 1002 TAD IMAGE
2651 6054 7041 CIA
2652 6055 1115 TAD K2172
2653 6056 7440 SEA
2654 6057 7402 HLT
2655 6060 6773 RMLV
2656 6061 3002 DCA IMAGE
2657 6062 1002 TAD IMAGE
2658 6063 7040 CMA
2659 6064 7440 SEA
2660 6065 7402 HLT
2661 6066 1433 TAD I
2662 6067 7041 CIA
2663 6070 1276 TAD T35AM1
2664 6071 7440 SEA
2665 6072 7402 HLT
2666 6073 4403 JMS I
2667 6074 6771 RES
2668 6075 7402 HLT
2669 /
2670 T35AM1, T35A-1

```

/LEVEL 0 INTERRUPT FROM LOC 6001 (LOC CHANGED TO NOP)
 /AC=0
 /AC = 7402 OR AN 8-MODE HLT
 /LOC TST = HLT
 /READ VECTOR ADDRESS REGISTER INTO AC
 /IMAGE OF AC
 /COMPLIMENT AND INC
 /IMAGE SHOULD = 1741
 /AC = 0
 /ERR
 /READ STACK ADDRESS REGISTER INTO AC
 /IMAGE OF AC
 /COMPLIMENT AND INC
 /IMAGE SHOULD = 2172
 /AC = 0
 /ERR
 /READ STACK AND VECTOR FIELD BITS AND MACHINE LEVEL INTO AC
 /IMAGE OF AC
 /IMAGE SHOULD = 7777
 /AC = 0
 /ERR
 /GET PC STORED IN LOC STACK+1
 /COMPLIMENT AND INC
 /PC STORED IN STACK+1 SHOULD = T35A-1
 /AC = 0
 /ERR
 /INC PC STORED IN LOC STACK+1
 /RESTORE MACHINE TO PREVIOUS STATUS STORED ON STACK
 /ERR

2671	6100	*6100	LOC0	/LEVEL 0 INTERRUPT
2672	0000	AND	VEC0	/JMP TO SERVICE ROUTINE
2673	5561	JMP I	LOC0	/LEVEL 1 INTERRUPT
2674	0000	AND	VEC1	/JMP TO SERVICE ROUTINE
2675	6103	JMP I	LOC0	/LEVEL 2 INTERRUPT
2676	0000	AND	VEC2	/JMP TO SERVICE ROUTINE
2677	6105	JMP I	LOC0	/LEVEL 3 INTERRUPT
2678	0000	AND	VEC3	/JMP TO SERVICE ROUTINE
2679	6107	JMP I	LOC0	/LEVEL 4 INTERRUPT
2680	5564	AND	VEC4	/JMP TO SERVICE ROUTINE
2681	6110	JMP I	LOC0	/LEVEL 5 INTERRUPT
2682	0000	AND	VEC5	/JMP TO SERVICE ROUTINE
2683	6111	JMP I	LOC0	/LEVEL 6 INTERRUPT
2684	5565	AND	VEC6	/JMP TO SERVICE ROUTINE
2685	6112	JMP I	LOC0	/LEVEL 7 INTERRUPT
2686	0000	AND	VEC7	/JMP TO SERVICE ROUTINE
2687	6113	JMP I	LOC0	/LEVEL 8 INTERRUPT
2688	5566	AND	VEC10	/JMP TO SERVICE ROUTINE
2689	6114	JMP I	LOC0	/LEVEL 9 INTERRUPT
2690	0000	AND	VEC11	/JMP TO SERVICE ROUTINE
2691	6115	JMP I	LOC0	/LEVEL 10 INTERRUPT
2692	5567	AND	VEC12	/JMP TO SERVICE ROUTINE
2693	6116	JMP I	LOC0	/LEVEL 11 INTERRUPT
2694	0000	AND	VEC13	/JMP TO SERVICE ROUTINE
2695	6117	JMP I	LOC0	/LEVEL 12 INTERRUPT
2696	5570	AND	VEC14	/JMP TO SERVICE ROUTINE
2697	6120	JMP I	LOC0	/LEVEL 13 INTERRUPT
2698	0000	AND	VEC15	/JMP TO SERVICE ROUTINE
2699	6121	JMP I	LOC0	/LEVEL 14 INTERRUPT
2700	5571	AND	VEC16	/JMP TO SERVICE ROUTINE
2701	6122	JMP I	LOC0	
2702	0000	AND		
2703	6123	JMP I		
2704	5572	AND		
2705	6124	JMP I		
2706	0000	AND		
2707	6125	JMP I		
	5573	AND		
	6126	JMP I		
	0000	AND		
	6127	JMP I		
	5574	AND		
	6130	JMP I		
	0000	AND		
	6131	JMP I		
	5575	AND		
	6132	JMP I		
	0000	AND		
	6133	JMP I		
	5576	AND		
	6134	JMP I		
	0000	AND		
	6135	JMP I		
	5577			

```

2708 /
2709 /VECTOR INTERRUPT SERVICE ROUTINES
2710 /FIRST INSTRUCTION EXECUTED IN THE
2711 /EXPECTED SERVICE ROUTINE WAS CHANGED
2712 /FROM A HLT TO A NOP
2713 /ALL OTHER ROUTINES HAVE A HLT
2714 /
2715 /LEVEL 0 INTERRUPT FROM LOCATION 6101
2716 /
2717 /
2718 /
2719 /CHANGED FROM HLT TO NOP FOR LEVEL 0 INTERRUPT
2720 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2721 /DEPOSIT 8-MODE HLT IN LOC VECT0
2722 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2723 /ERR
2724 /
2725 /
2726 /
2727 /CHANGED FROM HLT TO NOP FOR LEVEL 1 INTERRUPT
2728 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2729 /DEPOSIT 8-MODE HLT IN LOC VECT1
2730 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2731 /ERR
2732 /
2733 /
2734 /
2735 /CHANGED FROM HLT TO NOP FOR LEVEL 2 INTERRUPT
2736 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2737 /DEPOSIT 8-MODE HLT IN LOC VECT2
2738 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2739 /ERR
2740 /
2741 /
2742 /
2743 /CHANGED FROM HLT TO NOP FOR LEVEL 3 INTERRUPT
2744 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2745 /DEPOSIT 8-MODE HLT IN LOC VECT3
2746 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2747 /ERR
2748 /
2749 /
2750 /
2751 /CHANGED FROM HLT TO NOP FOR LEVEL 4 INTERRUPT
2752 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2753 /DEPOSIT 8-MODE HLT IN LOC VECT4
2754 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2755 /ERR

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        7402

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        6234
        6235

        7402
        4403
        3231
        6771
        7402

/LEVEL 5 INTERRUPT FROM LOCATION 6113
/
        *6200
        HLT
        JMS I      INC
        DCA        VECT5
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 5 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT5
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

/LEVEL 6 INTERRUPT FROM LOCATION 6115
/
        HLT
        JMS I      INC
        DCA        VECT6
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 6 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT6
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

/LEVEL 7 INTERRUPT FROM LOCATION 6117
/
        HLT
        JMS I      INC
        DCA        VECT7
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 7 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT7
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

/LEVEL 8 INTERRUPT FROM LOCATION 6121
/
        HLT
        JMS I      INC
        DCA        VECT10
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 8 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT10
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

/LEVEL 9 INTERRUPT FROM LOCATION 6123
/
        HLT
        JMS I      INC
        DCA        VECT11
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 9 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT11
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR

/LEVEL 10 INTERRUPT FROM LOCATION 6125
/
        HLT
        JMS I      INC
        DCA        VECT12
        RES
        HLT
/CHANGED FROM HLT TO NOP FOR LEVEL 10 INTERRUPT
/INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
/DEPOSIT 8-MODE HLT IN LOC VECT12
/RESTORE MACHINE TO LAST STATUS STORED ON STACK
/ERR
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2806 /
2807 /LEVEL 11 INTERRUPT FROM LOCATION 6127
2808 /
2809 /VECT13, HLT /CHANGED FROM HLT TO NOP FOR LEVEL 11 INTERRUPT
2810 6236 7402 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2811 6237 4403 JMS I INC
2812 6240 3236 DCA VECT13 /DEPOSIT 8-MODE HLT IN LOC VECT13
2813 6241 3236 DCA VECT13 /DEPOSIT 8-MODE HLT IN LOC VECT13
2814 6242 6771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2815 6243 7402 HLT /ERR
2816 /
2817 /LEVEL 12 INTERRUPT FROM LOCATION 6131
2818 /
2819 /VECT14, HLT /CHANGED FROM HLT TO NOP FOR LEVEL 12 INTERRUPT
2820 6244 7402 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2821 6245 4403 JMS I INC
2822 6246 3244 DCA VECT14 /DEPOSIT 8-MODE HLT IN LOC VECT14
2823 6247 6771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2824 6250 7402 HLT /ERR
2825 /
2826 /LEVEL 13 INTERRUPT FROM LOCATION 6133
2827 /
2828 /VECT15, HLT /CHANGED FROM HLT TO NOP FOR LEVEL 13 INTERRUPT
2829 6251 7402 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2830 6252 4403 JMS I INC
2831 6253 3251 DCA VECT15 /DEPOSIT 8-MODE HLT IN LOC VECT15
2832 6254 6771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2833 6255 7402 HLT /ERR
2834 /
2835 /LEVEL 14 INTERRUPT FROM LOCATION 6135
2836 /
2837 /VECT16, HLT /CHANGED FROM HLT TO NOP FOR LEVEL 14 INTERRUPT
2838 6256 7402 /INC PC IN LOC STACKK+1 AND RETURN WITH AC = 7402
2839 6257 4403 JMS I INC
2840 6260 3256 DCA VECT16 /DEPOSIT 8-MODE HLT IN LOC VECT16
2841 6261 6771 RES /RESTORE MACHINE TO LAST STATUS STORED ON STACK
2842 6262 7402 HLT /ERR

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2893		DIAL10	V003	15-SEP-71	0:10	PAGE 63	
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6400	MLV16,	HLT	*6400				
7402	CLA CLL			/CHANGED FROM HLT TO NOP FOR LEVEL 14 INTERRUPT			
7300	TAD			/CLEAR AC AND LINC			
1150	DCA	K7402		/AC = 7402 OR 8-MODE HLT			
3200	TAD	MLV16		/DEPOSIT IN LOC MLV16			
1142	DCA	K7000		/AC = 7000 OR 8-MODE NOP			
3245	TAD	MLV15		/ENABLE LEVEL 13 INTERRUPT BY SETTING LOC MLV15 = NOP			
6774	RSTK			/READ STACK ADDRESS REGISTER INTO AC			
3002	DCA	IMAGE		/IMAGE OF AC			
1002	TAD	IMAGE					
7041	CIA			/COMPLIMENT AND INC			
1115	TAD	K2172		/IMAGE SHOULD = 2172			
7440	SEA			/AC = 0			
7402	HLT			/ERR			
1433	TAD I	STACK+1		/GET PC STORED IN LOC STACKK+1			
7041	CIA			/COMPLIMENT AND INC			
1221	TAD	.+2		/PC STORED IN STACKK+1 SHOULD = T56A-1			
5222	JMP	.+2					
2654	T56A-1						
7440	SEA			/AC = 0			
7402	HLT			/ERR			
1433	TAD I	STACK+1		/GET PC STORED IN LOC STACKK+1			
7001	IAC			/INCREMENT			
3433	DCA I	STACK+1		/STORE BACK IN LOC STACKK+1			
1045	TAD	K3		/AC = 3			
0052	MAIN2			/MAINTENANCE MODE SIMULATION OF A LEVEL 13 INTERRUPT			
7000	NOP			/EXECUTE 1 MORE INST AFTER MAIN2 IOT			
7402	HLT						
7300	CLA CLL			/AC = 0--RESTORE OCCURES TO HERE FROM MLV15 ROUTINE			
6774	RSTK			/READ STACK ADDRESS REGISTER INTO AC			
3002	DCA	IMAGE		/IMAGE OF AC			
1002	TAD	IMAGE					
7041	CIA			/COMPLIMENT AND INC			
1115	TAD	K2172		/IMAGE SHOULD = 2172			
7440	SEA			/AC = 0			
7402	HLT			/ERR			
6771	RES			/RESTORE MACHINE TO LAST STATUS SAVED ON STACK			
7402	HLT			/ERR			

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2936						/
2937						/MULTIPLE LEVEL INTERRUPT-LEVEL 13
2938						/PREVIOUS STATUS SAVED ON STACK LOC 5605 TO 5611
2939						/
2940						MLV15, HLT
2941	6445	7402				/CHANGED FROM HLT TO NOP FOR LEVEL 13 INTERRUPT
2942	6446	7300	CLA CLL			/CLEAR AC AND LINC
2943	6447	1150	TAD	K7402		/AC = 7402 OR 8-MODE HLT
2944	6450	3245	DCA	MLV15		/DEPOSIT IN LOC MLV15
2945	6451	1142	TAD	K7000		/AC = 7000 OR 8-MODE NOP
2946	6452	3312	DCA	MLV14		/ENABLE LEVEL 12 INTERRUPT BY SETTING LOC MLV14 = NOP
2947	6453	6774	RSTK			/READ STACK ADDRESS REGISTER INTO AC
2948	6454	3002	DCA	IMAGE		/IMAGE OF AC
2949	6455	1002	TAD	IMAGE		
2950	6456	7041	CIA			/COMPLIMENT AND INC
2951	6457	1114	TAD	K2165		/IMAGE SHOULD = 2165
2952	6460	7440	SEA			/AC = 0
2953	6461	7402	HLT			/ERR
2954	6462	1760	TAD I	STK1		/GET PC STORED IN LOC STK1P
2955	6463	7041	CIA			/COMPLIMENT AND INC
2956	6464	1266	TAD	.02		/PC STORED IN LOC STK1P SHOULD = MLV16B
2957	6465	5267	JMP	.02		
2958	6466	6432				/AC = 0
2959	6467	7440	SEA			/ERR
2960	6470	7402	HLT			/GET PC STORED IN LOC STK1P
2961	6471	1760	TAD I	STK1		/INCREMENT
2962	6472	7001	IAC			/STORE BACK IN LOC STK1P
2963	6473	3760	DCA I	STK1		/AC = 7
2964	6474	1047	TAD	K7		/MAINTENANCE MODE SIMULATION OF A LEVEL 12 INTERRUPT
2965	6475	6052	MAIN2			/EXECUTE 1 MORE INST AFTER MAIN2 IOT
2966	6476	7000	NOP			
2967	6477	7402	HLT			/AC = 0--RESTORE OCCURES TO HERE FROM MLV14 ROUTINE
2968	6500	7300	CLA CLL			/READ STACK ADDRESS REGISTER INTO AC
2969	6501	6774	RSTK			/IMAGE OF AC
2970	6502	3002	DCA	IMAGE		
2971	6503	1002	TAD	IMAGE		/COMPLIMENT AND INC
2972	6504	7041	CIA			/IMAGE SHOULD = 2165
2973	6505	1114	TAD	K2165		/AC = 0
2974	6506	7440	SEA			/ERR
2975	6507	7402	HLT			/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
2976	6510	6771	RES			
2977	6511	7402	HLT			/ERR

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2978 /
2979 /MULTIPLE LEVEL INTERRUPT - LEVEL 12
2980 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5612 TO 5616
2981 /
2982 /
2983 /CHANGED FROM HLT TO NOP FOR LEVEL 12 INTERRUPT
2984 /CLEAR AC AND LINC
2985 /AC = 7402 OR 8-MODE HLT
2986 /DEPOSIT IN LOC MLV14
2987 /AC = 7000 OR 8-MODE NOP
2988 /ENABLE LEVEL 11 INTERRUPT BY SETTING LOC MLV13=NOP
2989 /READ STACK ADDRESS REGISTER INTO AC
2990 /IMAGE OF AC
2991 /COMPLIMENT AND INC
2992 /IMAGE SHOULD = 2160
2993 /AC = 0
2994 /ERR
2995 /GET PC STORED IN LOC STK2P
2996 /COMPLIMENT AND INC
2997 /PC STORED IN LOC STK2P SHOULD = MLV15B
2998
2999 /AC = 0
3000 /ERR
3001 /GET PC STORED IN LOC STK2P
3002 /INCREMENT
3003 /STORE BACK IN LOC STK2P
3004 /AC = 1
3005 /MAINTENANCE MODE SIMULATION OF A LEVEL 11 INTERRUPT
3006 /EXECUTE 1 MORE TEST INST AFTER MAIN1
3007
3008 /AC = 0--RESTOR OCCURS TO HERE FROM MLV13 ROUTINE
3009 /READ STACK ADDRESS REGISTER INTO AC
3010 /IMAGE OF AC
3011 /COMPLIMENT AND INC
3012 /IMAGE SHOULD = 2160
3013 /AC = 0
3014 /ERR
3015 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3016
3017 /
3018 /
3019 /
3020 /
3021 /MLV13I, MLV13
3022 /STK1, STK1P
3023 /STK2, STK2P

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3024			/			
3025			/			
3026			/			
3027			/			
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3029			/			
3030	6600	6600	MLV13,	*6600		
3031	6601	7402	HLT	CLL	CHANGED FROM HLT TO NOP FOR LEVEL 11 INTERRUPT	
3032	6602	7300	TAD		/CLEAR AC AND LINC	
3033	6603	1150	DCA	K7402	/AC = 7402 OR 8-MODE HLT	
3034	6604	3200	TAD	MLV13	/DEPOSIT IN LOC MLV13	
3035	6605	1142	DCA	K7000	/AC = 7000 OR 8-MODE NOP	
3036	6606	3245	RSTK	MLV12	/ENABLE LEVEL 10 INTERRUPT BY SETTING LOC MLV12 = NOP	
3037	6607	6774	DCA	IMAGE	/READ STACK ADDRESS REGISTER INTO AC	
3038	6610	3002	TAD	IMAGE	/IMAGE OF AC	
3039	6611	1002	CIA		/COMPLIMENT AND INC	
3040	6612	7041	TAD	K2153	/IMAGE SHOULD = 2153	
3041	6613	1112	SZA		/AC = 0	
3042	6614	7440	HLT		/ERR	
3043	6615	7402	TAD I	STK3	/GET PC STORED IN LOC STK3P	
3044	6616	1760	CIA		/COMPLIMENT AND INC	
3045	6617	7041	TAD	.02	/PC STORED IN LOC STK3P SHOULD = MLV14B	
3046	6620	1221	JMP	.02		
3047	6621	9222	MLV14B			
3048	6622	0544	SZA		/AC = 0	
3049	6623	7440	HLT		/ERR	
3050	6624	7402	TAD I	STK3	/GET PC STORED IN LOC STK3P	
3051	6625	1760	IAC		/INCREMENT	
3052	6626	7001	DCA I	STK3	/STORE BACK IN LOC STK3P	
3053	6627	3760	TAD	K3	/AC = 3	
3054	6630	1045	MAIN1		/MAINTENANCE MODE SIMULATION OF LEVEL 10 INTERRUPT	
3055	6631	0031	NOP		/EXECUTE 1 MORE INST AFTER MAIN1 IOT	
3056	6632	7000	MLV13B,			
3057	6633	7402	HLT			
3058	6634	7300	CLL		/AC = 0--RESTORE OCCURS TO HERE FROM MLV12 ROUTINE	
3059	6635	0774	RSTK		/READ STACK ADDRESS REGISTER INTO AC	
3060	6636	3002	DCA	IMAGE	/IMAGE OF AC	
3061	6637	1002	TAD	IMAGE		
3062	6640	7041	CIA		/COMPLIMENT AND INC	
3063	6641	1112	TAD	K2153	/IMAGE SHOULD = 2153	
3064	6642	7440	SZA		/AC = 0	
3065	6643	7402	HLT		/ERR	
3066	6644	0771	RES		/RESTORE MACHINE TO LAST STATUS SAVED ON STACK	
		7402	HLT		/ERR	

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/
/MULTIPLE LEVEL INTERRUPT - LEVEL 10
/PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5624 TO 5630
/
MLV12, HLT 7422
CLA CLL 6645
TAD 7320 6646
DCA 1150 6647
TAD 3245 6650
DCA 1142 6651
TAD 3312 6652
DCA 6774 6653
RSTK 6654 6654
DCA 3002 6655
TAD 1002 6655
CIA 7041 6656
TAD 1111 6657
SZA 7440 6660
HLT 7402 6661
TAD I 1761 6662
CIA 7041 6663
TAD 1266 6664
JMP 9267 6665
MLV13B 6666
SEA 7440 6667
HLT 7402 6670
TAD I 1761 6671
IAC 7001 6672
DCA I 3761 6673
TAD 1047 6674
MAIN1 6675
NOP 7000 6676
MLV12B, HLT 7402 6677
CLA CLL 6700
RSTK 7300 6701
DCA 6774 6702
TAD 3002 6703
CIA 1002 6704
TAD 7041 6705
SZA 1111 6706
HLT 7440 6707
RES 7402 6710
HLT 6771 6711
7402

/CHANGED FROM HLT TO NOP FOR LEVEL 10 INTERRUPT
/CLEAR AC AND LINC
/AC = 7402 OR 8-MODE HLT
/DEPOSIT IN LOC MLV12
/AC = 7000 OR 8-MODE
/ENABLE LEVEL 9 INTERRUPT BY SETTING LOC MLV11 = NOP
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2146
/AC = 0
/ERR
/GET PC STORED IN LOC STK4P
/COMPLIMENT AND INC
/PC STORED IN LOC STK3P SHOULD = MLV13B
/AC = 0
/ERR
/GET PC STORED IN LOC STK4P
/INCREMENT
/STORE BACK IN LOC STK4P
/AC = 7
/MAINTENANCE MODE SIMULATION OF A LEVEL 9 INTERRUPT
/EXECUTE 1 MORE INST AFTER MAIN1
/AC = 0--RESTORE OCCURS TO HERE FROM MLV11 ROUTINE
/READ STACK ADDRESS REGISTER INTO AC
/IMAGE OF AC
/COMPLIMENT AND INC
/IMAGE SHOULD = 2146
/AC = 0
/ERR
/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
/ERR

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3109			/			/MULTIPLE LEVEL INTERRUPT TEST - LEVEL 9
3110			/			/PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5631 TO 5635
3111			/			
3112			/			
3113			MLV11,	HLT		/CHANGED FROM HLT TO NOP FOR LEVEL 9 INTERRUPT
3114	6712	7402	CLA CLL			/CLEAR AC AND LINC
3115	6713	7300	TAD	K7402		/AC = 7402 OR 8-MODE HLT
3116	6714	1150	DCA	MLV11		/DEPOSIT IN LOC MLV11
3117	6715	3312	TAD	K7000		/AC = 7000 OR 8-MODE HLT
3118	6716	1142	DCA I	MLV10I		/ENABLE LEVEL 8 INTERRUPT BY SETTING LOC MLV10 = NOP
3119	6717	3757	RSTK			/READ STACK ADDRESS REGISTER INTO AC
3120	6720	6774	DCA	IMAGE		/IMAGE OF AC
3121	6721	3002	TAD	IMAGE		
3122	6722	1002	CIA			/COMPLIMENT AND INC
3123	6723	7041	TAD	K2141		/IMAGE SHOULD = 2141
3124	6724	1110	SZA			/AC = 0
3125	6725	7440	HLT			/ERR
3126	6726	7402	TAD I	STK5		/GET PC STORED IN LOC STK5P
3127	6727	1762	CIA			/COMPLIMENT AND INC
3128	6730	7041	TAD	.+2		/PC STORED IN LOC STK5P
3129	6731	1333	JMP			
3130	6732	5334	MLV12B			
3131	6733	6677	SZA			/AC = 0
3132	6734	7440	HLT			/ERR
3133	6735	7402	TAD I	STK5		/GET PC STORED IN LOC STK5P
3134	6736	1762	IAC			/INCREMENT
3135	6737	7001	DCA I	STK5		/STORE BACK IN LOC STK5P
3136	6740	7062	TAD	K17		/AC = 17
3137	6741	1051	MAIN1			/MAINTENANCE MODE SIMULATION OF A LEVEL 8 INTERRUPT
3138	6742	6051	NOP			/EXECUTE 1 MORE TEST AFTER MAIN1 IOT
3139	6743	7000	MLV11B,	HLT		
3140	6744	7402	CLA CLL			/AC = 0--RESTORE OCCURS HERE FROM MLV10 ROUTINE
3141	6745	7300	RSTK			/READ STACK ADDRESS REGISTER INTO AC
3142	6746	6774	DCA	IMAGE		/IMAGE OF AC
3143	6747	3002	TAD	IMAGE		
3144	6750	1002	CIA			/COMPLIMENT AND INC
3145	6751	7041	TAD	K2141		/IMAGE SHOULD = 2141
3146	6752	1110	SZA			/AC = 0
3147	6753	7440	HLT			/ERR
3148	6754	7402	RES			/RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3149	6755	6771	HLT			/ERR
3150	6756	7402				
3151			MLV10I,	MLV10		
3152	6757	7000	STK3,	STK3P		
3153	6760	5620	STK4,	STK4P		
3154	6761	5625	STK5,	STK5P		
3155	6762	5632				


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3200 /
3201 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 7
3202 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5643 TO 5647
3203 /
3204 /
3205 /CHANGED FROM HLT TO NOP FOR LEVEL 7 INTERRUPT
3206 /CLEAR AC AND LINC
3207 /AC = 7402 OR 8-MODE HLT
3208 /DEPOSIT IN LOC MLV7
3209 /AC = 7000 OR 8-MODE NOP
3210 /ENABLE LEVEL 6 INTERRUPT BY SETTING LOC MLV6 = NOP
3211 /READ STACK ADDRESS REGISTER INTO AC
3212 /IMAGE OF AC
3213 /COMPLIMENT AND INC
3214 /IMAGE SHOULD = 2127
3215 /AC = 0
3216 /ERR
3217 /GET PC STORED IN LOC STK7P
3218 /COMPLIMENT AND INC
3219 /PC STORED IN LOC STK7P SHOULD = MLV108
3220 /AC = 0
3221 /ERR
3222 /GET PC STORED IN LOC STK7P
3223 /INCREMENT
3224 /STORE BACK IN LOC STK7P
3225 /AC = 77
3226 /MAINTENANCE MODE SIMULATION OF A LEVEL 6 INTERRUPT
3227 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3228 /AC = 0--RESTORE OCCURS TO HERE FROM MLV6 ROUTINE
3229 /READ STACK ADDRESS REGISTER INTO AC
3230 /IMAGE OF AC
3231 /COMPLIMENT AND INC
3232 /IMAGE SHOULD = 2127
3233 /AC = 0
3234 /ERR
3235 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3236 /
3237 /
3238 /
3239 /
3240 /
3241 /
```

Address	Instruction	Comment
7045	HLT	
7046	CLA CLL	
7047	TAD	
7050	DCA	
7051	TAD	
7052	DCA	
7053	RSTK	
7054	DCA	
7055	TAD	
7056	CIA	
7057	TAD	
7060	SEA	
7061	HLT	
7062	TAD I	
7063	CIA	
7064	TAD	
7065	JMP	
7066	MLV108	
7067	SEA	
7070	HLT	
7071	TAD I	
7072	IAC	
7073	DCA I	
7074	TAD	
7075	MAIN1	
7076	NOP	
7077	HLT	
7100	CLA CLL	
7101	RSTK	
7102	DCA	
7103	TAD	
7104	CIA	
7105	TAD	
7106	SEA	
7107	HLT	
7110	RES	
7111	HLT	

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3242 /
3243 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 6
3244 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5650 TO 5654
3245 /
3246 /CHANGED FROM HLT TO NOP FOR LEVEL 6 INTERRUPT
3247 /CLEAR AC AND LINC
3248 /AC = 7402 OR 8-MODE HLT
3249 /DEPOSIT IN LOC MLV6
3250 /AC = 7000 OR 8-MODE NOP
3251 /ENABLE LEVEL 5 INTERRUPT BY SETTING LOC MLV5 = NOP
3252 /READ STACK ADDRESS REGISTER INTO AC
3253 /IMAGE OF AC
3254 /COMPLIMENT AND INCREMENT
3255 /IMAGE SHOULD = 2122
3256 /AC = 0
3257 /ERR
3258 /GET PC STORED IN LOC STK10P
3259 /COMPLIMENT AND INC
3260 /PC STORED IN LOC STK10P SHOULD = MLV7B
3261
3262 /AC = 0
3263 /ERR
3264 /GET PC STORED IN LOC STK10P
3265 /INCREMENT
3266 /STORE BACK IN LOC STK10P
3267 /AC = 177
3268 /MAINTENANCE MODE SIMULATION OF A LEVEL 5 INTERRUPT
3269 /EXECUTE 1 MORE INST AFTER MAIN1 IOT
3270
3271 /AC = 0--RESTORE OCCURS TO HERE FROM MLV5 ROUTINE
3272 /READ STACK ADDRESS REGISTER INTO AC
3273 /IMAGE OF AC
3274 /COMPLIMENT AND INC
3275 /IMAGE SHOULD = 2122
3276 /AC = 0
3277 /ERR
3278 /RESTORE MACHINE TO LAST STATUS SAVED ON STACK
3279
3280 /
3281 /MLV51, MLV5
3282 /STK6, STK6P
3283 /STK7, STK7P
3284 /STK10, STK10P

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3332 /
3333 /MULTIPLE LEVEL INTERRUPT TEST - LEVEL 4
3334 /PREVIOUS MACHINE STATUS SAVED ON STACK LOC 9662 TO 9666
3335 /
3336 /
3337 /CHANGED FROM HLT TO NOP FOR LEVEL 4 INTERRUPT
3338 /CLEAR AC AND LINC
3339 /AC=7402 OR 8-MODE HLT
3340 /DEPOSIT IN LOC MLV4
3341 /AC=7000 OR 8-MODE NOP
3342 /ENABLE LEVEL 3 INTERRUPT BY SETTING LOC MLV3=NOP
3343 /READ STACK ADDRESS REGISTER INTO AC
3344 /IMAGE OF AC
3345 /COMPLIMENT AND INC
3346 /IMAGE SHOULD = 2110
3347 /AC=0
3348 /ERR
3349 /GET PC STORED IN LOC STK12P
3350 /COMPLIMENT AND INC
3351 /PC STORED IN LOC STK12P SHOULD = MLV50
3352 /
3353 /AC=0
3354 /ERR
3355 /GET PC STORED IN LOC STK12P
3356 /INCREMENT
3357 /STORE BACK IN LOC STK12P
3358 /AC=777
3359 /MAINTENANCE MODE SIMULATION OF A LEVEL 3 INTERRUPT
3360 /EXECUTE 1 MORE INST AFTER MAIN1 JOT
3361 /
3362 /AC=0--RESTORE OCCURS TO HERE FROM MLV3 ROUTINE
3363 /READ STACK ADDRESS REGISTER INTO AC
3364 /IMAGE OF AC
3365 /COMPLIMENT AND INC
3366 /IMAGE SHOULD=2110
3367 /AC=0
3368 /ERR
3369 /RESTORE MACHINE TO LAST STATUS STORED ON STACK
3370 /
3371 /
3372 /
3373 /

```

	DIAL10	V003	15-SEP-71	0110	PAGE 74	
3374			/			/MULTIPLE LEVEL INTERRUPT TEST - LEVEL 3
3375			/			/PREVIOUS MACHINE STATUS SAVED ON STACK LOC 5667 TO 5673
3376			/			/
3377			/			/
3378			/			/
3379	7312	7402	MLV3,	HLT		/CHANGED FROM HLT TO NOP FOR LEVEL 3 INTERRUPT
3380	7313	7300		CLA CLL		/CLEAR AC AND LINC
3381	7314	1150		TAD	K7402	/AC=7402 OR 8-MODE HLT
3382	7315	3312		DCA	MLV3	/DEPOSIT IN LOC MLV3
3383	7316	1142		TAD	K7000	/AC=7000 OR 8-MODE NOP
3384	7317	3757		DCA I	MLV21	/ENABLE LEVEL 2 INTERRUPT BY SETTING LOC MLV2=NOP
3385	7320	6774		RSTK		/READ STACK ADDRESS REGISTER INTO AC
3386	7321	3002		DCA	IMAGE	/IMAGE OF AC
3387	7322	1002		TAD	IMAGE	
3388	7323	7041		CIA		/COMPLEMENT AND INC
3389	7324	1102		TAD	K2103	/IMAGE SHOULD =2103
3390	7325	7440		SEA		/AC=0
3391	7326	7402		HLT		/ERR
3392	7327	1762		TAD I	STK13	/GET PC STORED IN LOC STK13P
3393	7330	7041		CIA		/COMPLEMENT AND INC
3394	7331	1333		TAD	.02	/PC STORED IN LOC STK13P SHOULD = MLV40
3395	7332	9334		JMP	.02	
3396	7333	7277				
3397	7334	7440		SEA		/AC=0
3398	7335	7402		HLT		/ERR
3399	7336	1762		TAD I	STK13	/GET PC STORED IN LOC STK13P
3400	7337	7001		IAC		/INCREMENT
3401	7340	3762		DCA I	STK13	/STORE BACK IN LOC STK13P
3402	7341	1075		TAD	K1777	/AC=1777
3403	7342	0051		MAIN1		/MAINTENANCE MODE SIMULATION OF A LEVEL 2 INTERRUPT
3404	7343	7000		NOP		/EXECUTE 1 MORE INST AFTER MAIN1 IOT
3405	7344	7402	MLV3B,	HLT		
3406	7345	7300		CLA CLL		/AC=0--RESTORE OCCURS TO HERE FROM MLV2 ROUTINE
3407	7346	6774		RSTK		/READ STACK ADDRESS REGISTER INTO AC
3408	7347	3002		DCA	IMAGE	/IMAGE OF AC
3409	7350	1002		TAD	IMAGE	
3410	7351	7041		CIA		/COMPLEMENT AND INC
3411	7352	1102		TAD	K2103	/IMAGE SHOULD=2103
3412	7353	7440		SEA		/AC=0
3413	7354	7402		HLT		/ERR
3414	7355	6771		RES		/RESTORE MACHINE TO LAST STATUS STORED ON STACK
3415	7356	7402		HLT		/ERR
3416						
3417	7357	7400		MLV21,	MLV2	
3418	7360	9656		STK11,	STK11P	
3419	7361	9663		STK12,	STK12P	
3420	7362	9670		STK13,	STK13P	

3464		DIAL10	V003	15-SEP-71	0110	PAGE 76	
3465	/						/MULTIPLE LEVEL INTERRUPT TEST - LEVEL 1
3466							/PREVIOUS MACHINE STATUS SAVED ON STACK LOC 9700 TO 9704
3467	/						/
3468							MLV1,
3469		7445	7402	HLT	CLL		/CHANGED FROM HLT TO NOP FOR LEVEL 1 INTERRUPT
3470		7446	7300	CLA		K7402	/CLEAR AC AND LINC
3471		7447	1150	TAD		MLV1	/AC=7402 OR 8-MODE HLT
3472		7450	3245	DCA			/DEPOSIT IN LOC MLV1
3473		7451	1142	TAD		K7000	/AC=7000 OR 8-MODE NOP
3474		7452	3312	DCA		MLV0	/ENABLE LEVEL 0 INTERRUPT BY SETTING LOC MLV0=NOP
3475		7453	0774	RSTK			/READ STACK ADDRESS REGISTER INTO AC
3476		7454	3002	DCA		IMAGE	/IMAGE OF AC
3477		7455	1002	TAD		IMAGE	
3478		7456	7041	CIA			/COMPLIMENT AND INC
3479		7457	1100	TAD		K2071	/IMAGE SHOULD = 2071
3480		7460	7440	SZA			/AC=0
3481		7461	7402	HLT			/ERR
3482		7462	1743	TAD I		STK15	/GET PC STORED IN LOC STK15P
3483		7463	7041	CIA			/COMPLIMENT AND INC
3484		7464	1266	TAD		.+2	/PC STORED IN LOC STK15P SHOULD = MLV20
3485		7465	9267	JMP		.+2	
3486		7466	7432	MLV20			
3487		7467	7440	SZA			/AC=0
3488		7470	7402	HLT			/ERR
3489		7471	1743	TAD I		STK15	/GET PC STORED IN LOC STK15P
3490		7472	7001	IAC			/INCREMENT
3491		7473	3743	DCA I		STK15	/STORE BACK IN LOC STK15P
3492		7474	7340	CLA CLL		CMA	/AC=7777
3493		7475	0051	MAIN1			/MAINTENANCE MODE SIMULATION OF A LEVEL 0 INTERRUPT
3494		7476	7000	NOP			/EXECUTE 1 MORE INST AFTER MAIN1 IOT
3495		7477	7402	MLV10,			
3496		7500	7300	CLA CLL			/AC=0 -- RESTORE OCCURS TO HERE FROM MLV1 ROUTINE
3497		7501	6774	RSTK			/READ STACK ADDRESS REGISTER INTO AC
3498		7502	3002	DCA		IMAGE	/IMAGE OF AC
3499		7503	1002	TAD		IMAGE	
3500		7504	7041	CIA			/COMPLIMENT AND INC
3501		7505	1100	TAD		K2071	/IMAGE SHOULD = 2071
3502		7506	7440	SZA			/AC=0
3503		7507	7402	HLT			/ERR
3504		7510	0771	RES			/RESTORE MACHINE TO LAST STATUS STORED ON STACK
3505		7511	7402	HLT			/ERR

[illegible]

AGAIN	1074	K1000	0071	K5241	0131	MLV13B	6632
ALT0	4104	K1020	4361	K5252	0132	MLV13I	6557
ALT1	4140	K103	4360	K5400	0133	MLV14	6512
ALT2	4144	K1100	4163	K5577	0134	MLV14A	6352
APION	6006	K1441	0072	K5600	0135	MLV14B	6544
CLRST	0230	K1641	0073	K5777	0136	MLV15	6445
CLRSTK	0007	K17	0051	K60	0055	MLV15A	6353
CNT	1123	K1741	0074	K6000	0137	MLV15B	6477
COUNT	0023	K177	0060	K6100	0140	MLV16	6400
DO	4217	K1777	0075	K6300	0141	MLV16A	6354
E102	4567	K2	0044	K7	0047	MLV16B	6432
E17	4566	K20	0052	K7000	0142	MLV1A	6337
E4000	4570	K200	0061	K7017	0143	MLV1B	7477
EALTP	4502	K2000	0076	K7020	0144	MLV2	7400
EALTPJ	4175	K2020	0077	K7077	0145	MLV2A	6340
EFLD	4572	K2064	7541	K720	0067	MLV2B	7432
EFLDI	4174	K207	0062	K7277	0146	MLV2I	7357
EIF	4574	K2071	0100	K7400	0147	MLV3	7312
EIFI	4173	K2076	0101	K7402	0150	MLV3A	6341
ERE	4363	K2103	0102	K7577	0151	MLV3B	7344
ERES	4225	K2110	0103	K7600	0152	MLV4	7245
EREST	4347	K2115	0104	K77	0056	MLV4A	6342
ESTKT	4302	K2127	0105	K7740	0153	MLV4B	7277
EXIM	4573	K2134	0106	K7741	0154	MLV5	7200
EXICIA	4165	K2141	0107	K7760	0155	MLV5A	6343
EXICTR	4166	K2141	0110	K777	0070	MLV5B	7232
EXTGO	4035	K2146	0111	K7770	0156	MLV5I	7157
EXTKPS	4577	K2153	0112	KCDF	4171	MLV6	7112
EXTME	3557	K2160	0113	KMLV	4170	MLV6A	6344
EXTNEM	4001	K2169	0114	LO00	0000	MLV6B	7144
EXTPU	4513	K2172	0115	M16	0160	MLV7	7045
EXTPM7	4576	K2177	0116	M5	4362	MLV7A	6345
EXTRET	4117	K220	0045	M6	0157	MLV7B	7077
EXTRM1	4575	K2200	0047	MAIN1	6051	MORE	3434
EXTST	4176	K2201	0120	MAIN2	6052	MV16	2671
EXTSTK	4542	K2225	0121	MASK1	3555	PJA	6760
EXTTST	4436	K2777	0122	MASK2	3556	PJPC	0027
EXTX	4167	K3	0045	MLV0	7512	PJPC1	0030
EXXX	4571	K3200	0123	MLV0A	6336	PJPC2	0031
EXXXI	4172	K37	0053	MLV1	7445	RAN	0024
FLDIP	4200	K377	0064	MLV10	7000	RAN1	0025
FLDIPT	4177	K3777	0124	MLV10A	6346	RAN2	0026
IMAGE	0002	K4	0046	MLV10B	7032	RANN	0275
INC	0003	K40	0004	MLV10I	6757	RDF	6214
INCC	0266	K400	0005	MLV11	6712	RES	6771
IOF	6002	K4000	0125	MLV11A	6347	RIF	6224
ION	6001	K4377	4164	MLV11B	6744	RMLV	6773
IOPRE	0202	K4400	0126	MLV12	6645	RSTK	6774
IOPRES	0022	K5	4357	MLV12A	6350	RVEC	6775
K1	0043	K5020	0127	MLV12B	6677	SACMQL	0254
K10	0050	K520	0066	MLV13	6600	SETUP	0006
K100	0057	K5240	0130	MLV13A	6351	SETUPP	0213

SMLV	6772	T27	1632	UT16	1402
SSTK	6776	T3	0453	UT16A	0004
STACK	0032	T30	1665	UT37	3402
STACKK	5600	T31	1704	UT37A	0009
STK1	4560	T32	1741	VEC0	0161
STK10	7162	T33	2001	VEC1	0162
STK10P	5651	T34	2037	VEC10	0171
STK11	7360	T34A	2105	VEC11	0172
STK11P	5656	T35	2200	VEC12	0173
STK12	7361	T35A	2236	VEC13	0174
STK12P	5663	T35AM1	6076	VEC14	0175
STK13	7362	T36	2276	VEC15	0176
STK13P	5670	T37	2400	VEC16	0177
STK14	7542	T4	0466	VEC2	0163
STK14P	5675	T40	2415	VEC3	0164
STK15	7543	T41	2425	VEC4	0165
STK15P	5702	T42	2435	VEC5	0166
STK16	7544	T43	2445	VEC6	0167
STK16P	5707	T44	2455	VEC7	0170
STK1P	5606	T45	2465	VEC10	6140
STK2	6501	T46	2475	VEC11	6149
STK2P	5613	T47	2505	VEC110	6217
STK3	6760	T5	0501	VEC111	6224
STK3P	5620	T50	2515	VEC112	6231
STK4	6761	T51	2525	VEC113	6236
STK4P	5625	T52	2535	VEC114	6244
STK5	6762	T53	2545	VEC115	6251
STK5P	5632	T54	2555	VEC116	6256
STK6	7160	T55	2600	VEC12	6152
STK6P	5637	T56	2600	VEC13	6157
STK7	7161	T56A	2605	VEC14	6164
STK7P	5644	T57	3000	VEC15	6200
STKTST	4504	T6	0526	VEC16	6205
SVEC	6777	T60	3071	VEC17	6212
T0	0400	T60A	3112	VT0	4400
T1	0431	T60AM6	3133	XACMQ	0243
T10	0625	T61	3200		
T11	0637	T61A	3220		
T12	0662	T61AM5	3275		
T13	0700	T62	3243		
T14	0726	T62A	3202		
T15	0744	T62AM6	3276		
T16	1000	T63	3403		
T17	1023	T64	3421		
T2	0441	T65	3536		
T20	1047	T66	3545		
T21	1006	T7	0600		
T22	1200	TST	6036		
T22A	1276	TSTA	0042		
T24	1403	TSTPJ	1216		
T24A	1527	TSTPJ1	1425		
T26	1600	TSTPJ2	2053		

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ERRORS DETECTED: 0
LINKS GENERATED: 0
RUN-TIME: 45 SECONDS
3K CORE USED

[illegible]

INC	3193	3212	3213	3234	3235	3254	3255	3276	3277	3302	3303	3324	3325	3344	3345	3346	3347	3348	3349	3350
	3345	3366	3367	3386	3387	3408	3409	3434	3435	3456	3457	3476	3477	3498	3499	3500	3501	3502	3503	3504
	3499	3516	3517	3518	3519	3520	3521	3522	3523	3524	3525	3526	3527	3528	3529	3530	3531	3532	3533	3534
	3535	3536	3537	3538	3539	3540	3541	3542	3543	3544	3545	3546	3547	3548	3549	3550	3551	3552	3553	3554
	2820	2828	2836	2837	2838	2839	2840	2841	2842	2843	2844	2845	2846	2847	2848	2849	2850	2851	2852	2853
	2854	2855	2856	2857	2858	2859	2860	2861	2862	2863	2864	2865	2866	2867	2868	2869	2870	2871	2872	2873
	2874	2875	2876	2877	2878	2879	2880	2881	2882	2883	2884	2885	2886	2887	2888	2889	2890	2891	2892	2893
	2894	2895	2896	2897	2898	2899	2900	2901	2902	2903	2904	2905	2906	2907	2908	2909	2910	2911	2912	2913
	2914	2915	2916	2917	2918	2919	2920	2921	2922	2923	2924	2925	2926	2927	2928	2929	2930	2931	2932	2933
	2934	2935	2936	2937	2938	2939	2940	2941	2942	2943	2944	2945	2946	2947	2948	2949	2950	2951	2952	2953
	2954	2955	2956	2957	2958	2959	2960	2961	2962	2963	2964	2965	2966	2967	2968	2969	2970	2971	2972	2973
	2974	2975	2976	2977	2978	2979	2980	2981	2982	2983	2984	2985	2986	2987	2988	2989	2990	2991	2992	2993
	2994	2995	2996	2997	2998	2999	3000	3001	3002	3003	3004	3005	3006	3007	3008	3009	3010	3011	3012	3013
	3014	3015	3016	3017	3018	3019	3020	3021	3022	3023	3024	3025	3026	3027	3028	3029	3030	3031	3032	3033
	3034	3035	3036	3037	3038	3039	3040	3041	3042	3043	3044	3045	3046	3047	3048	3049	3050	3051	3052	3053
	3054	3055	3056	3057	3058	3059	3060	3061	3062	3063	3064	3065	3066	3067	3068	3069	3070	3071	3072	3073
	3074	3075	3076	3077	3078	3079	3080	3081	3082	3083	3084	3085	3086	3087	3088	3089	3090	3091	3092	3093
	3094	3095	3096	3097	3098	3099	3100	3101	3102	3103	3104	3105	3106	3107	3108	3109	3110	3111	3112	3113
	3114	3115	3116	3117	3118	3119	3120	3121	3122	3123	3124	3125	3126	3127	3128	3129	3130	3131	3132	3133
	3134	3135	3136	3137	3138	3139	3140	3141	3142	3143	3144	3145	3146	3147	3148	3149	3150	3151	3152	3153
	3154	3155	3156	3157	3158	3159	3160	3161	3162	3163	3164	3165	3166	3167	3168	3169	3170	3171	3172	3173
	3174	3175	3176	3177	3178	3179	3180	3181	3182	3183	3184	3185	3186	3187	3188	3				

[illegible]

	3229	3271	3319	3361	3403	3451	3493		1699	1736	1939	2923	2965
MAIN2	20#	1360	1370	1380	1501	1609	1624		1660				
MASK1	1921	1932	1945	1947	2026#								
MASK2	1923	1938	1951	1953	2027#								
MLV0	2878	3474	3511#	3514									
MLV0A	2848	2878#											
MLV1	2879	3432	3469#	3472									
MLV10	2886	3152	3162#	3165									
MLV10A	2864	2886#											
MLV10B	3180#	3222											
MLV10I	3119	3152#											
MLV11	2887	3077	3114#	3117									
MLV11A	2866	2887#											
MLV11B	3140#	3179											
MLV12	2880	3035	3072#	3075									
MLV12A	2868	2888#											
MLV12B	3098#	3131											
MLV13	2889	3021	3030#	3033									
MLV13A	2870	2889#											
MLV13B	3056#	3089											
MLV13I	2988	3021#											
MLV14	2890	2946	2983#	2986									
MLV14A	2872	2890#											
MLV14B	3009#	3047											
MLV15	2891	2904	2941#	2944									
MLV15A	2874	2891#											
MLV15B	2967#	3000	2899#	2902									
MLV16	1676	2892											
MLV16A	2876	2892#											
MLV16B	2925#	2958											
MLV1A	2850	2879#											
MLV1B	3495#	3526											
MLV2	2880	3417	3427#	3430									
MLV2A	2852	2880#											
MLV2B	3453#	3486											
MLV2I	3384	3417#											
MLV3	2881	3342	3379#	3382									
MLV3A	2854	2881#											
MLV3B	3405#	3444											
MLV4	2882	3300	3337#	3340									
MLV4A	2856	2882#											
MLV4B	3363#	3396											
MLV5	2883	3285	3205#	3290									
MLV5A	2858	2883#											
MLV5B	3321#	3354											
MLV5I	3252	3285#											
MLV6	2884	3210	3247#	3250									
MLV6A	2860	2884#											
MLV6B	3273#	3312											
MLV7	2885	3167	3205#	3208									
MLV7A	2862	2885#			</								

[illegible]

STK4	3085	3092	3094	3154#
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STK5	3127	3134	3136	3155#
STK5P	2513#	3155		
STK6	3175	3182	3184	3286#
STK6P	2521#	3286		
STK7	3218	3225	3227	3287#
STK7P	2529#	3287		
STKTST	2367	2394	2441#	2450
SVEC	18#	192	467	483
T0	1291	1647	1919	2073
T1	43	163	290#	2022
T11	321#	2024		
T10	466#			
T11	481#			
T12	504#			
T13	525#			
T14	551#			
T15	572#			
T16	595	601#		
T17	628#			
T2	336#			
T20	650#			
T21	673#			
T22	701	731#		
T22A	747	801#		
T24	850	871#		
T24A	889	965#		
T26	998	1009#		
T27	1046#			
T3	353#			
T30	1083#			
T31	1106#			
T32	1146#			
T33	1171	1182#		
T34	1220#			
T34A	1231	1258#		
T35	1271	1279#		
T35A	1296	1311#		
T35AM1	2663	2670#	2670	
T36	1351#			
T37	1384	1397#		
T4	369#			
T40	1417#			
T41	1432#			
T42	1448#			
T43	1463#			
T44	1478#			
T45	1494#			
T46	1509#			
T47	1524#			
T5	387#			
T50	1540#			

T51	1555#				
T52	1570#				
T53	1586#				
T54	1595	1604#			
T55	1619#				
T56	1634#				
T56A	1663#	2916			
T57	1674	1684#			
T6	412#				
T60	1750#				
T60A	1761	1770#	1784	1791	
T60AM6	1775	1791#			
T61	1768	1799#			
T61A	1810	1818#	1834	1873	
T61AM5	1823	1873#			
T62	1816	1846#			
T62A	1854	1861#	1868	1874	
T62AM6	1864	1874#			
T63	1860	1892#			
T64	1914#				
T65	1955				
T66	2006	2002#	2016#		
T7	434	2010			
T7	63	441#	2637#	2640	
TST	63	2603			
TSTA	63#	1282	748#		
TSTPJ	49	742	885	891#	951
TSTPJ1	50	878	1232#		
TSTPJ2	51	1227	880	891	
UT16	36	858#			
UT16A	36#	1353			
UT37	37	1311	1884#		
UT37A	37#	1224	1398		
VEC0	143#	1404	1740	1900	2679
VEC1	144#	1420	2601		
VEC10	151#	1527	2695		
VEC11	152#	1543	2697		
VEC12	153#	1558	2699		
VEC13	154#	1573	2701		
VEC14	155#	1589	2703		
VEC15	156#	1607	2705		
VEC16	157#	1622	2707		
VEC2	145#	1435	2603		
VEC3	146#	1451	2605		
VEC4	147#	1466	2687		
VEC5	148#	1481	2689		
VEC6	149#	1497	2691		
VEC7	150#	1512	2693		
VEC10	143	1705	1726	2719#	2721
VEC11	144	2727#	2729		
VEC110	151	2785#	2787		
VEC111	152	2793#	2795		
VEC112	153	2801#	2803		
VEC113	154	2810#	2812	2813	

VECT14	155	2819#	2821	
VECT15	156	2827#	2829	
VECT16	157	2835#	2837	
VECT2	145	2735#	2737	
VECT3	146	2743#	2745	
VECT4	147	2751#	2753	
VECT5	148	2761#	2763	
VECT6	149	2769#	2771	
VECT7	150	2777#	2779	
VT0	2324#			
XACMQ	220#	757	809	972

