```
6                    1800 BAUD
7                    2400 BAUD
```

MAINDEC-08-DHMPA-A-D

PDP-8/E EXTENDED MEM
PARITY TEST

ABSTRACT

THE PDP-8/E EXTENDED MEMORY PARITY CHECKERBOARD DIAGNOSTIC
IS DESIGNED TO DETECT PARITY FAILURES ON HALF-SELECTED LINES
UNDER WORST CASE NOISE CONDITIONS. THE WORST CASE PATTERN
PROVIDED WILL GENERATE WORST CASE NOISE CONDITIONS IN ALL
PDP-8/E PARITY STACKS. ALSO, THE PARITY ERROR INTERRUPT AND
THE PARITY DATA DECODING CIRCUITS ARE CHECKED FOR PROPER OPERATION.
THIS PROGRAM WILL TEST SYSTEMS EQUIPPED WITH PARITY AND FROM
8K TO 32K WORDS OF CORE MEMORY. AUTOMATIC PROGRAM RELOCATION IS
PROVIDED FOR ERROR IDENTIFICATION, AND THE OPERATOR IS GIVEN
A DEGREE OF CONTROL OVER THE PROGRAM BY VARIOUS SR SETTINGS.

REQUIREMENTS

A PDP-8/E COMPUTER EQUIPPED WITH MEMORY PARITY AND AT LEAST
8K OF CORE MEMORY.

STORAGE - THIS PROGRAM OCCUPIES CORE LOCATION 0000 - 5177 AND 6400
        - 6630 OCTAL.

LOADING - BINARY LOADER

STARTING PROCEDURE

SET THE SR TO THE INSTRUCTION FIELD AND DATA FIELD OF THE
STACK WHICH CONTAINS THE PROGRAM.
PRESS KEY EXTD ADDR LOAD.
SET THE SR EQUAL TO 0200.
PRESS KEYS ADDR LOAD, CLEAR, AND CONT. A SETUP SR MESSAGE WILL
BE PRINTED.
SET THE SR FOR DESIRED OPERATION.
PRESS KEY CONT.

PRINTOUTS - YES

SWITCH REGISTER OPTIONS - YES

SWITCH	0 (DOWN)	1 (UP)
SR00	CONTINUE AFTER ERROR	HALT AFTER ERROR
SR01	TYPEOUT ERRORS	INHIBIT ERROR TYPEOUTS
SR02	NORMAL	TTY BELL ON ERROR
SR03	RELOCATE PROGRAM	INHIBIT PROGRAM RELOCATION
SR04	NORMAL	CHANGE STACK LIMITS
SR05	NORMAL	HALT AFTER CURRENT TEST
SR06-08	STARTING STACK LIMIT (0-7)	
SR09-11	ENDING STACK LIMIT (0-7)	

MAINDEC-08-DHLSA-A

LS8E LINE PRINTER TEST

ABSTRACT
        THE LINE PRINTER DIAGNOSTIC TEST PROGRAM IS DESIGNED TO TEST
        FOR CORRECT OPERATION OF THE CONTROL INTERFACE AND CORRECT
        CHARACTER GENERATION OF THE CENTRONICS MODEL 101 LINE PRINTER.
        THE NORMAL FLOW OF THIS DIAGNOSTIC PROGRAM IS TO SEQUENTIALLY
        TEST FIVE SECTIONS, WHEN A TEST SECTION IS ENTERED THE TITLE
        OF THE TEST SECTION WILL BE PRINTED ON THE TELETYPE.

REQUIREMENTS
        ANY OF THESE PROCESSORS: PDP-8,8/I,8/L,8E OR PDP 12
        CENTRONICS MODEL 101 LINE PRINTER
        LS8E CONTROL INTERFACE
        TELETYPE-ASCII KEYBOARD PRINTER

LOADING - BINARY LOADER

STARTING PROCEDURE
        LOAD LINE PRINTER WITH LINE PRINTER PAPER
        TURN ON POWER BY PRESSING ON/OFF SWITCH
        SWITCH SHOULD LIGHT UP AFTER HAVING BEEN PRESSED.
        AT THIS POINT ALL OTHER LIGHTS SHOULD BE OFF.
        PRESS SELECT SWITCH
        SELECT SWITCH SHOULD LIGHT UP AFTER HAVING BEEN PRESSED,
        SELECT STARTING ADDRESS AND LOAD ADDRESS (START 0200)
        PRESS START
        (FOR A PDP-8/E:START=CLEAR; THEN CONT.)
        PROGRAM WILL HALT AT ADDRESS 422.
        AT THIS TIME SELECT A PROCESSOR AND ALL OTHER SWITCHES
        (SEE SWITCH SETTINGS)
        PRESS CONTINUE

PRINTOUTS - YES

SWITCH REGISTER OPTIONS - YES
        SW0=0   DO ONE PASS OF COMPLETE TEST AND HLT
        SW0=1   LOOP ON COMPLETE TEST

        SW1=0   PRINT ERROR MESSAGE
        SW1=1   INHIBIT ERROR MESSAGE PRINT OUT

        SW2=0   DON'T LOOP ON CURRENT TEST
        SW2=1   LOOP ON CURRENT TEST

        SW3=0   NORMAL TEST FLOW
        SW3=1   REPEAT CURRENT TEST SECTION

        SW4=0   NORMAL TEST FLOW
        SW4=1   HLT AFTER COMPLETION OF SELECTED PRINT TEST

        PROCESSOR SELECTION SWITCHES

        SWS   05-06-07

        001   PDP-8 OR PDP-8/I
        010   PDP-8/L
        011   PDP-12
        100   PDP-8/E

        TEST SECTION INHIBIT SWITCHES

18

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SW8=0    DO THE BASIC IOT TEST
SW8=1    INHIBIT BASIC ITO TEST

SW9=0    DO THE FORMAT CONTROL CHARACTER TESTS
SW9=1    INHIBIT FORMAT CONTROL CHARACTER TESTS

SW10=0   DO THE CARACTER PRINT TESTS
SW10=1   INHIBIT CHARACTER PRINT TESTS

SW11=0   DO THE TIMING TESTS
SW11=1   INHIBIT TIMING TESTS
```

MAINDEC-08-DHRKA-A-D

RK8E DISKLESS CONTROL TEST

ABSTRACT

THE RK8E DISKLESS CONTROL TEST IS DESIGNED FOR THE
PURPOSE OF CHECKOUT OF THE RK8E DISK CONTROL LOGIC NOT
REQUIRING THE USE OF THE DISK DRIVE. THIS TEST SHOULD
BE RUN WITH ALL EXISTING DRIVES SET TO THE LOAD POSITION.

REQUIREMENTS
PDP-8/E COMPUTER
AT LEAST 4K OF READ/WRITE MEMORY
ASR-33 TELETYPE OR EQUIVALENT
RK8E DISK CONTROL
RK05 DISK DRIVE

STORAGE - THE PROGRAM UTILIZES OR OCCUPIES LOCATIONS 0000 TO 7577
OF THE CURRENT FIELD.

STARTING PROCEDURE
REGULAR DISKLESS CONTROL TEST

SET THE SWITCH LABELED "RUN/LOAD" TO THE "LOAD" POSITION ON
ALL DRIVES

SET THE SWTICH LABELED "ON/OFF" TO THE "ON" POSITION ON
ALL DRIVES.

SET THE SWITCH REGISTER TO 0200 AND PRESS LOAD ADDRESS.

SET THE SWITCH REGISTER TO 0000.

SET SWR9-11 TO THE AMOUNT OF EXTENDED R/W MEMORY BANKS
AND START THE COMPUTER RUNNING.

SET SWR1=1 IF THE OPERATOR DESIRES TO INHIBIT THE END
OF TEST HALT AT LOCATION "ENDHLT".

SWR4=1 SHOULD ALWAYS BE USED TO STOP THE PROGRAM.

THE PROGRAM SHOULD PRINT THE FOLLOWING MESSAGE AT
THE COMPLETION OF EACH SUCCESSFUL PASS APROX. EVERY 3.5
MINUTES.

19