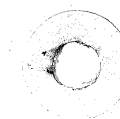
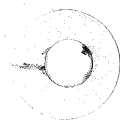
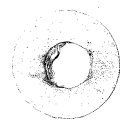


IDENTIFICATION

PRODUCT CODE: MAINDEC-08-D1EC-D
PRODUCT NAME: PDP-8, 8/I EXTENDED MEMORY
CHECKERBOARD
DATE CREATED: NOVEMBER 1, 1971
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: J. RICHARDSON - J. VROBEL

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CORPORATION



1. ABSTRACT

The PDP-8, 8/I Extended Memory Checkerboard diagnostic is designed to provide worst case half-select noise conditions in order to determine the operational status of core memory. Four data patterns, and their complements, are written and checked for error. The patterns provided will generate the worst case noise conditions for a PDP-8 or 8/I equipped with standard or specially purchased core stacks, and will test systems equipped with from 8K to 32K words of core memory. Automatic program relocation is provided in order to test all memory stacks from each stack.

Teletype print-outs are provided for error identification. Also, the operator is given a degree of control over the program by various SR settings. These are explained in detail in Section 8.2.

2. REQUIREMENTS

2.1 Equipment

A standard PDP-8 or 8/I equipped with at least 8K words of core memory.

2.2 Storage

The program occupies locations 0010 to 3334.

2.3 Preliminary Programs

The Binary Loader must be in memory. Also, all diagnostics for a basic 4K PDP-8 must have been previously run successfully.

3. LOADING PROCEDURE

- a. Turn off the Teletype reader.
- b. Set the SR to 7777.
- c. Press LOAD ADDRESS; then START.
- d. Place the Binary tape in Teletype reader and turn on the reader.
- e. When the program has been loaded, stop the computer, turn off the reader, and remove the tape.

4. STARTING PROCEDURE

4.1 Starting Address

Start from address 200 to specify the amount of core memory to test; SR settings, and to receive a header print-out.

4.2 Restarting Address

Start from address 207 to change the test limits; SR settings, and to inhibit the header print-out.

4.3 Operator Action

Immediately after starting from address 200 or 207, the program will print TEST LIMITS. The operator must then specify, via the Teletype keyboard, the amount of core memory to test, followed by a carriage return.

The following rules govern the amount of memory to test:

a. Type two octal numbers, separating the numbers with a comma. The first number signifies the lowest order 4K stack to test; the second signifies the highest order.

b. The program expects the 4K stacks to be numbered sequentially starting with a stack 0.

c. If the highest order stack to test is typed as the first stack, the program will interchange the two values so as to make the second value the first to test.

d. After typing the second octal number, press the carriage return key to terminate the line.

e. The program will test the lowest and highest order 4K stack specified, plus every stack between, starting with the lowest specified.

f. Any single stack, or two or more sequential stacks may be specified.

g. The stack containing the program may be included when specifying two or more stacks.

The stack containing the program will be tested after automatic program relocation takes place (see Section 5.3.1).

h. If a typing error is made, press the RUB-OUT key. TEST LIMITS will be printed again. All previous input is disregarded.

For the following examples assume the program to be located in stack 0, and the program has been started from address 200 or 207. The amount of core memory available is 32K.

Example A:

TEST LIMITS

0,7 (denotes carriage return)

Example A indicates stacks 0, 1, 2, 3, 4, 5, 6 and 7 will be tested.

Example B:

TEST LIMITS

7,0_q

The program will perform exactly as Example A.

Example C:

TEST LIMITS

4,5_q

Only stacks 4 and 5 will be tested.

Example D:

TEST LIMITS

3,3_q

Stack 3 alone will be tested.

Example E:

TEST LIMITS

0,0 PROGRAM IS LOCATED IN FIELD 0

TEST LIMITS

0,1_q

Example E shows the message printed by the program when a single stack is selected which currently contains the program. TEST LIMITS is printed again, and the operator must then correct the test limits.

Operation of the program is unpredictable if the amount of memory selected for testing exceeds the actual amount available, i.e., selecting 32K for testing on a PDP-8 or 8/I equipped with a maximum of 28K.

4.3.1 Setup SR

After the test limit is specified, the program will print SETUP SR. For normal program operation, the SR must be set to equal 0000_g. Press the carriage return key after setting the SR to 0000. The program will then run until stopped by the operator. Normal program operation is defined as performing all four checkerboard patterns on all of available memory from every memory stack.

5. OPERATING PROCEDURE

5.1 Program and Operator Action

- a. Load the program into stack 0 using the procedure described in Section 3.
- b. Set the SR to 200; press LOAD ADDRESS, and then start.
- c. The message TEST LIMITS will be printed. Specify the limits, via keyboard, as described in Section 4.3.
- d. The message SETUP SR will be printed. Set the SR to 0000₈, and press the carriage return key.
- e. The program will perform all four tests on all of core memory specified, after which, automatic program relocation takes place.

5.2 Operational Switch Settings

Normal operation of the program requires the SR set to 0000₈. Refer to Section 8.2, applications, for switch settings provided for trouble-shooting.

5.3 Subroutine Abstracts

5.3.1 Program Relocation

Program relocation is governed entirely by the amount of core memory selected for testing. Under certain conditions the program will not relocate at all, but will remain in the current 4K stack to perform the tests (see below). The program first relocates to the highest order 4K stack under test. From there it relocates to the next lower stack (after performing all four tests). The program keeps relocating to the next lower stack until it reaches the lowest order stack under test. The testing and relocation cycle is then repeated.

The contents of the entire 4K stack are relocated. This enables the RIM Loader, and any other information to be carried with the program.

The program provides a degree of protection for itself by recording the first error encountered in any stack. When a faulty stack is next in sequence to contain the program, the program will skip the faulty stack and relocate to the first lower order stack which is error-free. If all lower order stacks are faulty, program relocation will not take place. The tests will be run again from the current stack. Relocation will resume when an error-free stack is found.

Also, the program will not relocate if any of the conditions described below exist.

- a. Only one 4K stack is selected for testing.
- b. SR 9 is on a 1 to inhibit relocation (see Section 8.2.6).

The INSTRUCTION FIELD indicators will indicate the current stack containing the program.

5.3.2 The Checkerboard Patterns

Four test patterns, and their complements, are used to test memory. All memory stacks, except the one with the program, are tested with one pattern before the next test is executed.

Any one, or any combination, of the four tests may be run by placing one, or any combination, of SR 3, 4, 5, or 6 on a 1 after the message SETUP SR is printed. The test specified by the most significant switch on a 1 will be executed first. SR 3, 4, 5 and 6 all on a 0 will enable all tests to be run. SR 3 = test 1; 4 = test 2; 5 = test 3; 6 = test 4.

The following steps are performed by each of the four tests:

- a. Write the pattern once in all stacks selected for testing; starting with the lowest order stack.
- b. Select the lowest order stack and perform a read, complement data, write sequence once on each location, until all 4K has been complemented.
- c. Repeat step b 31 more times. The stack will end up with the pattern originally loaded.

No error checking has been performed as yet.

- d. Read 4-word segments and complement each segment 4 times; then read each of the 4 words and check for error.

e. After checking the entire 4K stack for errors, repeat step d again. This time stall for a random period of time after reading and checking every 400₈ word block. The maximum stall is 18.4 ms; the minimum is 3 μ s.

- f. Setup for the next sequential 4K stack and repeat steps b through f.

When all selected stacks have been checked the next test in sequence is executed, and steps a through f repeated. Program relocation takes place after the fourth test is executed in this manner.

The patterns generated by each test are shown below. The matrices represent portions of one bit plane.

Test 1:

		y-axis							
0000	x-axis	→							
		0	0	1	1	0	0	1	1
		0	0	1	1	0	0	1	1
		1	1	0	0	1	1	0	0
0300		1	1	0	0	1	1	0	0
		↓							

		y-axis							
	x-axis	→							
		1	1	0	0	1	1	0	0
		1	1	0	0	1	1	0	0
		0	0	1	1	0	0	1	1
		0	0	1	1	0	0	1	1
		↓							

Test 2:

	y-axis	
0000	→	0 0 1 1 0 0 1 1
		1 1 0 0 1 1 0 0
x-axis	↓	1 1 0 0 1 1 0 0
0300		0 0 1 1 0 0 1 1

	y-axis	
	→	1 1 0 0 1 1 0 0
		0 0 1 1 0 0 1 1
x-axis	↓	0 0 1 1 0 0 1 1
		1 1 0 0 1 1 0 0

Test 3:

	y-axis	
0000	→	1 0 0 1 1 0 0 1
x-axis	↓	0 1 1 0 0 1 1 0
		0 1 1 0 0 1 1 0
0300		1 0 0 1 1 0 0 1

	y-axis	
	→	0 1 1 0 0 1 1 0
x-axis	↓	1 0 0 1 1 0 0 1
		1 0 0 1 1 0 0 1
		0 1 1 0 0 1 1 0

Test 4:

	y-axis	
0000	→	0 1 1 0 0 1 1 0
x-axis	↓	0 1 1 0 0 1 1 0
		1 0 0 1 1 0 0 1
0300		1 0 0 1 1 0 0 1

	y-axis	
	→	1 0 0 1 1 0 0 1
x-axis	↓	1 0 0 1 1 0 0 1
		0 1 1 0 0 1 1 0
		0 1 1 0 0 1 1 0

6.

ERRORS

Starting the program from address 200 will give a header print-out after the SR has been setup.

The header identifies the information printed when a data error is found. The header appears as:

	FIELD	OCTAL ADR.	GOOD	BAD	TEST
Where:	FIELD	= an octal number (0 to 7) indicating the 4K field containing the error.			
	OCTAL ADR.	= the memory address which contains the incorrect data.			
	GOOD	= what the data in octal, should have been. This will always equal 0000 or 7777.			
	BAD	= the data as read. This will equal the good data except for one or more bits complemented.			
	TEST	= the number (1 to 4) of the test which detected the error.			

After each error print-out the program continues on with the next sequential memory location.

6.1 Error Halts and Description

Placing SR 0 on a 1 during an error print-out will cause a halt at location 2641.
Press CONTINUE to resume testing.

7. RESTRICTIONS

7.1 Starting Restrictions

Start from address 200 to indicate the amount of core memory to test; to setup the SR and to receive a header print-out.

Starting from 207 requires the same operator action, but no header will be printed.

7.2 Operating Restrictions

None

8. MISCELLANEOUS

8.1 Execution Time

The time required to perform all four tests on one 4K memory stack is approximately 26 seconds.

8.2 Applications

For operating convenience, and as an aid to trouble-shooting, the SR may be used to control the program. The switch assignments and their effect on the program are described below. Please note that it is important that the program should be halted before changing the test selection switches. These switches are not sensed by the program during testing.

Halting the program with SR 0 is preferred, rather than with the STOP key. Using the STOP key may result in a halt while the program is in the process of relocating, which is disastrous.

8.2.1 Halt after Test or Error - SR 0

Placing SR 0 on a 1 at any time while the program is running will cause a halt after the current test is completed. The MB will equal 2461 in the current stack containing the program. Press CONTINUE to resume testing, or restart from 200 or 207 to enter new parameters.

Placing SR 0 on a 1 during an error type-out will also cause a halt at location 2461. Proceed exactly as described in the above paragraph.

8.2.2 Inhibit Error Print-out - SR 1

Placing SR 1 on a 1 causes all error print-outs to be inhibited. All other messages will not be inhibited. The program will continue to recognize errors, but will not print any information. SR 1 may be placed on a 1 or 0 while the program is running.

8.2.3 Bell on Error - SR 2

SR 2 on a 1 causes the program to ring the TTY BELL whenever an error is detected. This is convenient when testing with power supply margins. SR 2 has precedence over SR 1 if both should happen to be on a 1. SR 2 may be placed on a 1 or 0 while the program is running.

8.2.4 Test Selection SR 3 through 6

Any one, or any combination of tests may be executed by placing any one or any combination of SR 3 through 6 on a 1. Test selections may be made only when starting from 200 or 207. SR 3 specifies test 1; SR 4 test 2; SR 5 test 3; SR 6 test 4. The test specified by the most significant SR on a 1 will be executed first.

For most PDP-8s, SR 4 will provide the worst case pattern. For most PDP-8/Is, SR 5 will provide the worst case pattern.

If all four switches are on a 0, all four tests will be executed in order starting with test 1. Program relocation is not effected, regardless of the SR settings.

8.2.5 Inhibit Program Relocation - SR 7

The program normally relocates automatically as indicated by the INSTRUCTION FIELD indicators. To retain the program in its current 4K field, place SR 7 on a 1 at any time. Changing SR 7 to a 0 will permit relocation to resume.

8.2.6 SR 8, 9 and 10 - Not Used

8.2.7 Change TEST LIMITS and SR - SR 11

Placing SR 11 on a 1 will cause the program to automatically restart from address 207. The TEST LIMITS and SR may then be changed. SR 11 is sensed only after all specified tests have been completed on all of memory under test.

8.2.8 Loop on Address

A subroutine is provided which may be used to continuously loop on a single location, or a group of consecutive locations. No error checking is performed. The routine performs a read, and immediately follows with a write, on each location. The loop time between two reads, or two writes, is approximately 22.5 μ s.

Operating Procedure:

- a. Set the INSTRUCTION FIELD switches to the current field, and the SR to 1700.
- b. Set the DATA FIELD switches to equal the 4K field number to test.
- c. Press LOAD ADDRESS.
- d. Set the SR to equal the first address of the group.
- e. Press START. A halt will occur at 1703. Set the SR to equal the last address of the group.
- f. Press CONTINUE. The address(s) specified will be looped until stopped by the operator with STOP. SR 0 will not halt this routine.

To resume normal operation, restart the program from 200 or 207 of the current field.

9. PROGRAM DESCRIPTION

The PDP-8, 8/I Extended Memory Checkerboard diagnostic is designed to create worst case memory noise conditions on systems equipped with 8K to 32K words of memory. The program executes four checkerboard patterns, plus their complements, on each 4K memory field. In addition, the program automatically relocates from field to field in order to test all 4K fields from every 4K field. Under normal operation, the amount of core memory tested at one time is that specified by the operator minus the 4K field containing the program. A TTY keyboard input routine is provided to enable the operator to specify the exact number of 4K fields to be tested. A print-out is provided for each error detected by the program.

Further control of the program is given to the operator by means of the SR. The operator may halt the program, inhibit error print-outs, substitute the TTY BELL for error indication, halt after error print-out, select any one or a group of tests, inhibit program relocation, and create an automatic restart to change the amount of memory to test.

A small subroutine is provided which will continuously read and write any single, or a group of locations within any 4K field. The operator must specify the locations by means of the SR.

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0053 0262, 262
0054 0263, 263
0055 0264, 264
0056 7760, 7760

0057 7740
0060 7774
0061 7773
0062 0000
0063 1607
0064 0652
0065 0667
0066 0704
0067 0721
0070 1600
0071 1624
0072 0000
0073 0000
0074 0000
0075 0736
0076 1037
0077 1054
0100 1071
0101 1106
0102 1123
0103 1140
0104 1200
0105 1217
0106 1056
0107 1057
0110 4515
0111 4516
0112 4517
0113 4520
0114 4552
0115 1245
0116 1322
0117 1400
0120 1455
0121 2000
0122 0000
0123 0000
0124 0000
0125 6201
0126 6202
0127 2641
0130 0213
0131 1646
0132 2146
0133 0007
0134 0000
0135 2474
0136 2146
0137 2115

026
7253
4536

0057 7740
0060 7774
0061 7773
0062 0000
0063 1607
0064 0652
0065 0667
0066 0704
0067 0721
0070 1600
0071 1624
0072 0000
0073 0000
0074 0000
0075 0736
0076 1037
0077 1054
0100 1071
0101 1106
0102 1123
0103 1140
0104 1200
0105 1217
0106 1056
0107 1057
0110 4515
0111 4516
0112 4517
0113 4520
0114 4552
0115 1245
0116 1322
0117 1400
0120 1455
0121 2000
0122 0000
0123 0000
0124 0000
0125 6201
0126 6202
0127 2641
0130 0213
0131 1646
0132 2146
0133 0007
0134 0000
0135 2474
0136 2146
0137 2115

0057 7740
0060 7774
0061 7773
0062 0000
0063 1607
0064 0652
0065 0667
0066 0704
0067 0721
0070 1600
0071 1624
0072 0000
0073 0000
0074 0000
0075 0736
0076 1037
0077 1054
0100 1071
0101 1106
0102 1123
0103 1140
0104 1200
0105 1217
0106 1056
0107 1057
0110 4515
0111 4516
0112 4517
0113 4520
0114 4552
0115 1245
0116 1322
0117 1400
0120 1455
0121 2000
0122 0000
0123 0000
0124 0000
0125 6201
0126 6202
0127 2641
0130 0213
0131 1646
0132 2146
0133 0007
0134 0000
0135 2474
0136 2146
0137 2115

0215

PAL10	V141	2-NOV-71	0119	PAGE 1-3
0242	3033	DCA MCWA		/SAVE
0241	7200	CLA		
0242	1033	TAD MCWA		
0243	0030	AND K420		
0244	7440	SEA		/TEST 1 IF NO SKIP
0245	5444	JMP I XTST1		
0246	7200	CLA		
0247	1033	TAD MCWA		
0250	0037	AND K200		
0251	7440	SEA		/TEST 2 IF NO SKIP
0252	5445	JMP I XTST2		
0253	7200	CLA		
0254	1033	TAD MCWA		
0255	0040	AND K100		
0256	7440	SEA		/TEST 3 IF NO SKIP
0257	5446	JMP I XTST3		
0260	7200	CLA		
0261	1033	TAD MCWA		
0262	0041	AND K40		
0263	7440	SEA		/TEST 4 IF NO SKIP
0264	5447	JMP I XTST4		
0265	4531	JMS I XFILD		/RESTORE DATA FIELD
0266	7604	LAS		
0267	0150	AND K1		/CHECK SR 11
0270	7440	SEA		
0271	5211	JMP RSTRT1		
0272	7604	LAS		
0273	0042	AND K20		/INHIBIT MOVE IF A 1
0274	7440	SEA		
0275	5213	JMP RTN1		/GO RELOCATE
0276	5450	JMP I XMOVE		
0277	2645	XSTSR, SETSR		
0300	4451	/TEST 1. WRITE CHECKER PATTERN #1.		
0301	7200	TST1.		
0302	1052	JMS I XSETU		/SET OF TO 1ST FIELD
0303	3062	CLA		
0304	7040	TAD K261		/TEST NUMBER
0305	3010	DCA TNUM		
0306	4463	CMA		/SET ADDRESS COUNT TO 7777
0307	7410	DCA 10		/SEE IF FIELD HAS PROGRAM
0310	5322	JMS I XBANK		/NO. BEGIN WRITING
0311	1345	SKP EXT1		/DONE ALL. NOW READ ALL
0312	3153	JMP EXT1		
0313	4464	TAD KXT1		
0314	4464	DCA EXIT		
0315	4465	JMS I X0011		/WRITE 0011
0316	4465	JMS I X0011		/WRITE 0011 64 TIMES
0316	4465	JMS I X1100		/WRITE 1100 128 TIMES
0316	4465	JMS I X1100		

[illegible]

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0420	5214	JMP ,=4		
0421	4471	JMS I XTBNK	/SETUP FOR NEXT FIELD	
0422	5224	JMP TST2+4		
0423	4520	JMS I XCHK2	/READ EACH FIELD AND CHECK	
0424	5225	JMP TST2C	/NOW WRITE COMPLEMENT	
		/WRITE COMPLEMENT OF PATTERN 2		
0425	4451	JMS I XSETU	/SET DF FOR FIRST FIELD	
0426	7240	CLA CMA		
0427	3010	DCA 10	/SET ADR. COUNT TO 7777	
0430	4463	JMS I XBANK	/SEE IF FIELD HAS PROGRAM	
0431	7410	SKP EXT2C	/WRITE	
0432	5245	JMP EXT2C	/GO READ	
0433	1251	TAD KXT2C		
0434	3153	DCA EXIT		
0435	4465	JMS I X1100	/WRITE 1100	
0436	4464	JMS I X0011	/WRITE 0011 128 TIMES	
0437	4464	JMS I X0011		
0440	4465	JMS I X1100	/WRITE 1100 128 TIMES	
0441	4465	JMS I X1100		
0442	5236	JMP ,=4		
0443	4471	JMS I XTBNK	/SETUP FOR NEXT FIELD	
0444	5226	JMP TST2C+1		
0445	4501	JMS I XCHK2C	/READ EACH FIELD AND CHECK	
0446	5647	JMP I ,+1	/SEE IF TEST 3 IS SELECTED	
0447	0253	EXAM3		
0450	0421	XIT2		
0451	0443	KXT2C, XIT2C		
		/TEST 3. WRITE CHECKER PATTERN #3		
0452	4451	JMS I XSETU	/SETUP FOR 1ST FIELD	
0453	7200	CLA		
0454	1054	TAD K263		
0455	3062	DCA TNUM	/TEST NUMBER	
0456	7240	CLA CMA		
0457	3010	DCA 10	/SET ADR. COUNT TO 7777	
0460	4463	JMS I XBANK	/SEE IF FIELD HAS PROGRAM	
0461	7410	SKP EXT3	/GO WRITE	
0462	5275	JMP EXT3	/GO READ	
0463	1322	TAD KXT3		
0464	3153	DCA EXIT		
0465	4467	JMS I X1001	/WRITE 1001	
0466	4466	JMS I X0110	/WRITE 0110 128 TIMES	
0467	4466	JMS I X0110		
0470	4467	JMS I X1001	/WRITE 1001 128 TIMES	
0471	4467	JMS I X1001		
0472	5266	JMP ,=4		
0473	4471	JMS I XTBNK	/SETUP FOR NEXT FIELD	
		XIT3,		

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2474	5256		JMP TS13+4	
2475	4502		JMS I XCHK3	/READ EACH FIELD AND CHECK
2476	5277		JMP TS13C	/WRITE COMPLEMENT
			/WRITE COMPLEMENT OF PATTERN 3	
0477	4451		JMS I XSETU	/SETUP DF FOR 1ST FIELD
0500	7240		CLA CMA	
0501	3010		DCA 10	/SET ADR. COUNT TO 7777
0502	4463		JMS I XBANK	/SEE IF FIELD HAS PROGRAM
0503	7410		SKP EXT3C	/WRITE
0504	5317		JMP EXT3C	/READ ALL
0505	1323		TAD KXT3C	
0506	3153		DCA EXIT	
0507	4466		JMS I X0110	/WRITE 0110
0510	4467		JMS I X1001	/WRITE 1001 128 TIMES
0511	4467		JMS I X1001	
0512	4466		JMS I X0110	/WRITE 0110 128 TIMES
0513	4466		JMS I X0110	
0514	5310		JMP I=4	
0515	4471		JMS I XTBNK	/SETUP FOR NEXT FIELD
0516	5300		JMP TS13C+1	
0517	4503		JMS I XCHK3C	/READ EACH FIELD AND CHECK
0520	5721		JMP I.+1	/SEE IF TEST 4 IS SELECTED
0521	0260		EXAM4	
0522	0473		XIT3	
0523	0515		KXT3C, XIT3C	
			/TEST 4. WRITE PATTERN #4	
			/	
	0600		+600	
			/	
0600	4451		JMS I XSETU	/SET DF FOR 1ST FIELD
0601	7200		CLA	
0602	1055		TAD K264	
0603	3062		DCA TNUM	/TEST NUMBER
0604	7240		CLA CMA	
0605	3010		DCA 10	/SET ADR. COUNT TO 7777
0606	4463		JMS I XBANK	/SEE IF FIELD HAS PROGRAM
0607	7410		SKP EXT4	/WRITE
0610	5223		JMP EXT4	/GO READ
0611	1245		TAD KXT4	
0612	3153		DCA EXIT	
0613	4466		JMS I X0110	/WRITE 0110
0614	4466		JMS I X0110	/WRITE 0110 64 TIMES
0615	4467		JMS I X1001	/WRITE 1001 128 TIMES
0616	4467		JMS I X1001	
0617	4466		JMS I X0110	
0620	5214		JMP I=4	
0621	4471		JMS I XTBNK	/SETUP FOR NEXT FIELD
			XIT4,	

PAL10	V141	2-NOV-71	0119	PAGE 1-7
0622	5204	JMP TST4+4		
0623	4504	JMS I XCHK4	/READ EACH FIELD AND CHECK	
0624	5225	JMP TST4C	/WRITE COMPLEMENT	
		/WRITE COMPLEMENT OF PATTERN 4		
0625	4451	JMS I XSETU	/SET DF FOR FIRST	
0626	7240	CLA CMA		
0627	3010	DCA I 0	/SET ADR. COUNT TO 7777	
0630	4463	JMS I XBANK	/SEE IF FIELD HAS PROGRAM	
0631	7410	SKP	/WRITE	
0632	5247	JMP EXT4C	/READ	
0633	1246	TAD KXT4C		
0634	3153	DCA EXIT		
0635	4467	JMS I X1001		
0636	4467	JMS I X1001	/WRITE 1001 64 TIMES	
0637	4466	JMS I X0110	/WRITE X0110 128 TIMES	
0640	4466	JMS I X0110		
0641	4467	JMS I X1001		
0642	5236	JMP I-4		
0643	4471	JMS I XTBANK	/SETUP FOR NEXT FIELD	
0644	5226	JMP TST4C+1		
0645	0621	XIT4		
0646	0643	KXT4C, XIT4C		
0647	4505	EXT4C, JMS I XCHK4C	/READ EACH FIELD AND CHECK	
0650	5651	JMP I-1	/SEE IF READY TO MOVE	
0651	0265	EXAM4+5		
		/ROUTINE TO WRITE 0011		
0652	0000	W0011, 0		
0653	1056	TAD M20		
0654	3072	DCA COUNT	/0	
0655	3410	DCA I 10	/0	
0656	3410	DCA I 10	/1	
0657	7040	CMA		
0660	3410	DCA I 10	/1	
0661	7040	CMA		
0662	3410	DCA I 10	/COUNT = 16 OR 32	
0663	2072	ISE COUNT	/LOOP	
0664	5255	JMP W0011+3	/SEE IF END OF FIELD	
0665	4470	JMS I XBANK	/EXIT	
0666	5652	JMP I W0011		
		/ROUTINE TO WRITE 1100		
0667	0000	W1100, 0		
0670	1056	TAD M20		
0671	3072	DCA COUNT		
0672	7040	CMA		
0673	3410	DCA I 10	/1	

```

0674 7040 CMA
0675 3410 DCA I 10 /1
0676 3410 DCA I 10 /0
0677 3410 DCA I 10 /0
0700 2072 ISZ COUNT /-16 OR -32
0701 5272 JMP W1100+3 /LOOP
0702 4470 JMS I XKBNK /SEE IF END OF FIELD
0703 5667 JMP I W1100 /EXIT

```

/ROUTINE TO WRITE 0110

```

0704 0000 W0110, 0
0705 1056 TAD M20
0706 3072 DCA COUNT
0707 3410 DCA I 10 /0
0710 7040 CMA
0711 3410 DCA I 10 /1
0712 7040 CMA
0713 3410 DCA I 10 /1
0714 3410 DCA I 10 /0
0715 2072 ISZ COUNT /-16 OR -32
0716 5307 JMP W0110+3
0717 4470 JMS I XKBNK /SEE IIF END OF FIELD
0720 5704 JMP I W0110 /EXIT

```

/ROUTINE TO WRITE 1001

```

0721 0000 W1001, 0
0722 1056 TAD M20
0723 3072 DCA COUNT
0724 7040 CMA
0725 3410 DCA I 10 /1
0726 3410 DCA I 10 /0
0727 3410 DCA I 10 /0
0730 7040 CMA
0731 3410 DCA I 10 /1
0732 2072 ISZ COUNT /-16 TO -32
0733 5324 JMP W1001+3 /LOOP
0734 4470 JMS I XKBNK /SEE IF END OF FIELD
0735 5721 JMP I W1001 /EXIT

```

/ROUTINE TO READ ALL OF MEMORY 8 TIMES, COMPLEMENTING
/THE PATTERN EACH PASS; NO ERROR CHECKING IS DONE.

```

0736 0000 RDALL, 0
0737 7200 CLA
0740 1057 TAD M40 /-32 DECIMAL
0741 3072 DCA COUNT /COUNTS PASSES THRU MEMORY
0742 7240 CLA CMA
0743 3010 DCA 10 /SET ADR. REGS. TO 777
0744 7040 CMA

```

[illegible]

/SETUP ROUTINES FOR RCHKA

PAL10	V141	2-NOV-71	0119	PAGE 1-10
1037	0000	/		
1040	1110	RCHK1, 0		
1041	3211	TAD JMS1		/JMS1 = JMS I XRD1
1042	1110	DCA RLOPA		
1043	3212	TAD JMS1		/JMS1 = JMS I XRD1
1044	1111	DCA RLOPA+1		
1045	3213	TAD JMS2		
1046	1111	DCA RLOPA+2		
1047	3214	TAD JMS2		
1050	1110	DCA RLOPA+3		
1051	3215	TAD JMS1		
1052	4200	DCA RLOPA+4		/GO READ
1053	5637	JMS RCHKA		/EXIT
		JMP I RCHK1		
1054	0000	/		
1055	1111	RCHK1C, 0		
1056	3211	TAD JMS2		/JMS2 = JMS I XRD2
1057	1111	DCA RLOPA		
1060	3212	TAD JMS2		
1061	1110	DCA RLOPA+1		
1062	3213	TAD JMS1		
1063	1110	DCA RLOPA+2		
1064	3214	TAD JMS1		
1065	1111	DCA RLOPA+3		
1066	3215	TAD JMS2		
1067	4200	DCA RLOPA+4		/GO READ
1070	5634	JMS RCHKA		/EXIT
		JMP I RCHK1C		
1071	0000	/		
1072	1110	RCHK2, 0		
1073	3211	TAD JMS1		/JMS1 = JMS I XRD1
1074	1111	DCA RLOPA		/JMS I XRD2
1075	3212	TAD JMS2		
1076	1111	DCA RLOPA+1		
1077	3213	TAD JMS2		/JMS I XRD1
1100	1110	DCA RLOPA+2		
1101	3214	TAD JMS1		
1102	1110	DCA RLOPA+3		
1103	3215	TAD JMS1		
1104	4200	DCA RLOPA+4		/GO READ
1105	5671	JMS RCHKA		/EXIT
		JMP I RCHK2		
1106	0000	/		
1107	1111	RCHK2C, 0		
1110	3211	TAD JMS2		
1111	1110	DCA RLOPA		/JMS I XRD1
1112	3212	TAD JMS1		
1113	1110	DCA RLOPA+1		
1114	3213	TAD JMS1		
1115	1111	DCA RLOPA+2		
1116	3214	TAD JMS2		
1117	1111	DCA RLOPA+3		
1120	3215	TAD JMS2		
1121	4200	DCA RLOPA+4		/GO READ
		JMS RCHKA		

/EXIT

JMP I RCHK2C

1122 5706

/ RCHK3, 0
1123 0000
1124 1113
1125 3211
1126 1112
1127 3212
1130 1112
1131 3213
1132 1113
1133 3214
1134 1113
1135 3215
1136 4200
1137 5723
TAD JMS4
DCA RLOPA
TAD JMS3
DCA RLOPA+1
TAD JMS3
DCA RLOPA+2
TAD JMS4
DCA RLOPA+3
TAD JMS4
DCA RLOPA+4
JMS RCHKA
JMP I RCHK3
/JMS I XRD4
/JMS I XRD3
/JMS I XRD4
/GO READ
/EXIT

/ RCHK3C, 0
1140 0000
1141 1112
1142 3211
1143 1113
1144 3212
1145 1113
1146 3213
1147 1112
1150 3214
1151 1112
1152 3215
1153 4200
1154 5740
TAD JMS3
DCA RLOPA
TAD JMS4
DCA RLOPA+1
TAD JMS4
DCA RLOPA+2
TAD JMS3
DCA RLOPA+3
TAD JMS3
DCA RLOPA+4
JMS RCHKA
JMP I RCHK3C
/JMS I XRD3
/JMS I XRD4
/GO READ
/EXIT

*1200
/ RCHK4, 0
1200 0000
1201 4531
1202 1112
1203 3637
1204 1112
1205 3640
1206 1113
1207 3641
1210 1113
1211 3642
1212 1112
1213 3643
1214 4644
1215 4636
1216 5600
JMS I XFILD
TAD JMS3
DCA I XLOPA
TAD JMS3
DCA I XLOPB
TAD JMS4
DCA I XLOPC
TAD JMS4
DCA I XLOPD
TAD JMS3
DCA I XLOPE
JMS I XCFL
JMS I XCHKA
JMP I RCHK4
/JMS I XRD3
/JMS I XRD3
/JMS I XRD4
/GO READ
/EXIT

/ RCHK4C, 0
1217 0000
1220 4531
1221 1113
1222 3637
1223 1113
JMS I XFILD
TAD JMS4
DCA I XLOPA
TAD JMS4
/JMS I XRD4

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1224	3640	DCA I XLOPB	
1225	1112	TAD JMS3	
1226	3641	DCA I XLOPC	
1227	1112	TAD JMS3	
1230	3642	DCA I XLOPD	
1231	1113	TAD JMS4	
1232	3643	DCA I XLOPE	
1233	4644	JMS I XCFL	
1234	4636	JMS I XCHKA	
1235	5617	JMP I RCHK4C	
			/GO READ
			/EXIT
1236	1000	RCHKA	
1237	1011	XLOPA	
1240	1012	XLOPB	
1241	1013	XLOPC	
1242	1014	XLOPD	
1243	1015	XLOPE	
1244	1722	XCFL	
			PAUSE

7/61-6S EXTENDED CHECKERBOARD - TAPE 2
/READ ROUTINES FOR 0011: 1100: 0110 AND 1001

ADDRESS	INSTRUCTION	OPERAND	REMARKS
1245	0000		
1246	1056		
1247	3072		
1250	1060		
1251	3073		
1252	1141		
1253	3074		
1254	1522		
1255	7040		
1256	3522		
1257	2074		
1260	5254		
1261	2073		
1262	7410		
1263	5266		
1264	2122		
1265	5252		
1266	1122		
1267	1060		
1270	3010		
1271	7100		
1272	1410		
1273	7440		
1274	4521		
1275	7100		
1276	1410		
1277	7440		
1300	4521		
0	TAD M20	/-16	
	DCA COUNT		
	TAD M4	/-4	
	DCA FLCNT	/-8	
	TAD M10		
	DCA LOOP		
	TAD I MEMADR		
	CMA		
	DCA I MEMADR		
	ISZ LOOP		/COMPLEMENT 8 TIMES
	JMP I-4		/DONE 4 ADRS; WHEN SKIP
	ISZ FLCNT		
	SKP		
	JMP I+3		
	ISZ MEMADR		
	JMP CLOP1		
	TAD MEMADR		
	TAD M4		
	DCA I0		/SUBTRACT 4
	CLL		/NOW USE AUTO-INDEX
	TAD I 10		
	SZA		/0
	JMS I XRROR		/PRINT ERROR
	CLL		
	TAD I 10		
	SZA		/0
	JMS I XRROR		/PRINT ERROR

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1301	7120	STL		
1302	1410	TAD I 10		
1303	7040	CMA	/1	
1304	7440	SZA		
1305	4521	JMS I XRROR	/PRINT ERROR	
1306	7120	STL		
1307	1410	TAD I 10		
1310	7040	CMA	/1	
1311	7440	SZA		
1312	4521	JMS I XRROR	/PRINT ERROR	
1313	2072	ISZ COUNT		
1314	5320	JMP I +4		
1315	4470	JMS I XKBK	/SEE IF END OF FIELD	
1316	2122	ISZ MEMADR		
1317	5645	JMP I RD1		
1320	2122	ISZ MEMADR	/KEEP READING	
1321	5250	JMP RD1+3		
1322	0000	RD2,		
1323	1056	TAD M20	/-16	
1324	3072	DCA COUNT		
1325	1060	TAD M4	/-4	
1326	3073	DCA FLCNT		
1327	1141	TAD M10	/-8	
1330	3074	DCA LOOP	/READ	
1331	1522	TAD I MEMADR		
1332	7040	CMA		
1333	3522	DCA I MEMADR	/COMPLEMENT 4 TIMES	
1334	2074	ISZ LOOP		
1335	5331	JMP I-4	/DONE 4 ADRS. WHEN SKIP	
1336	2073	ISZ FLCNT		
1337	7410	SKP I-3		
1340	5343	JMP I-3	/INCREMENT ADDRESS	
1341	2122	ISZ MEMADR		
1342	5327	JMP CLOP2		
1343	1122	TAD MEMADR		
1344	1060	TAD M4		
1345	3010	DCA 10	/NOW USE AUTO-INDEX	
1346	7120	STL		
1347	1410	TAD I 10		
1350	7040	CMA	/1	
1351	7440	SZA		
1352	4521	JMS I XRROR	/PRINT ERROR	
1353	7120	STL		
1354	1410	TAD I 10		
1355	7040	CMA	/1	
1356	7440	SZA		
1357	4521	JMS I XRROR	/PRINT ERROR	
1360	7100	CLL		
1361	1410	TAD I 10		

PAL10	V141	2-NOV-71	0119	PAGE 1-14
1362	7440		SZA	/0
1363	4521		JMS I XRROR	/PRINT ERROR
1364	7100		CLL	
1365	1410		TAD I 10	/0
1366	7440		SZA	/PRINT ERROR
1367	4521		JMS I XRROR	
1370	2072		ISZ COUNT	
1371	5375		JMP I+4	/SEE IF END OF FIELD
1372	4470		JMS I XBNK	
1373	2122		ISZ MEMADR	
1374	5722		JMP I RD2	/KEEP READING
1375	2122		ISZ MEMADR	
1376	5325		JMP RD2+3	
1400				
1401	0000		TAD M20	/-16
1402	1056		DCA COUNT	
1403	3072		TAD M4	/-4
1404	1060		DCA FLCNT	/-8
1405	3073		TAD M10	/READ
1406	1141		DCA LOOP	
1407	3074		TAD I MEMADR	
1408	1522		CMA	
1410	7040		DCA I MEMADR	/COMPLEMENT 8 TIMES
1411	3522		ISZ LOOP	/DONE 4 IF 0
1412	2074		JMP I+4	
1413	5207		ISZ FLCNT	
1414	2073		SKP I+3	
1415	7410		JMP I+3	/DO NEXT
1416	5221		ISZ MEMADR	
1417	2122		JMP CLOP3	
1420	5205			
1421	1122		TAD MEMADR	
1422	1060		TAD M4	/USE AUTO-INDEX
1423	3010		DCA 10	
1424	7100		CLL	/0
1425	1410		TAD I 10	/PRINT ERROR
1426	7440		SZA	
1427	4521		JMS I XRROR	/1
1430	7120		STL	
1431	1410		TAD I 10	/PRINT ERROR
1432	7040		CMA	
1433	7440		SZA	/PRINT ERROR
1434	4521		JMS I XRROR	
1435	7120		STL	/1
1436	1410		TAD I 10	/PRINT ERROR
1437	7040		CMA	
1440	7440		SZA	/PRINT ERROR
1441	4521		JMS I XRROR	
1442	7100		CLL	

```

1443 1410 TAD I 10
1444 7440 SZA
1445 4521 JMS I XRROR
1446 2072 ISZ COUNT
1447 5253 JMP I+4
1450 4470 JMS I XKBK
1451 2122 ISZ MEMADR
1452 5600 JMP I RD3
/
1453 2122 ISZ MEMADR
1454 5203 JMP RD3+3

```

```

1455 0000 /
1456 1056 RD4,
1457 3072 TAD M20
1460 1060 DCA COUNT
1461 3073 TAD M4
1462 1141 DCA FLCNT
1463 3074 TAD M10
1464 1522 DCA LOOP
1465 7040 TAD I MEMADR
1466 3522 CMA
1467 2074 DCA I MEMADR
1470 5264 ISZ LOOP
1471 2073 JMP I+4
1472 7410 ISZ FLCNT
1473 5276 SKP
1474 2122 JMP I+3
1475 5262 ISZ MEMADR
1476 1122 JMP CLOP4
1477 1060 TAD MEMADR
1500 3010 TAD M4
1501 7120 DCA 10
1502 1410 STL
1503 7040 TAD I 10
1504 7440 CMA
1505 4521 SZA
1506 7100 JMS I XRROR
1507 1410 CLL
1510 7440 TAD I 10
1511 4521 SZA
1512 7100 JMS I XRROR
1513 1410 CLL
1514 7440 TAD I 10
1515 4521 SZA
1516 7120 JMS I XRROR
1517 1410 STL
1520 7040 TAD I 10
1521 7440 CMA
1522 4521 SZA
1523 2072 JMS I XRROR
1524 5330 ISZ COUNT
1525 4470 JMP I+4
JMS I XKBK
/SEE IF END OF FIELD

```

```

/COMPLEMENT 8 TIMES
/DONE 4 ADRS. WHEN SKIP
/INCREMENT ADDRESS
/USE AUTO=INDEX
/1
/PRINT ERROR
/0
/PRINT ERROR
/0
/PRINT ERROR
/1
/PRINT ERROR
/1
/PRINT ERROR

```

PAL10 V141
 1526 2122
 1527 5655
 1530 2122
 1531 5260

ISZ MEMADR
 JMP I RD4
 ISZ MEMADR
 JMP RD4+3

/ROUTINE TO CHECK FOR END OF FIELD

*1600
 CKBNK, 0

1600 0000
 1601 7200
 1602 1010
 1603 7040
 1604 7640
 1605 5600
 1606 5553

CLA
 TAD 10
 CMA
 SZA CLA
 JMP I CKBNK
 JMP I EXIT

/NOT DONE
 /DONE

/ROUTINE TO SEE IF TESTED FIELD HAS PROGRAM

CBANK, 0

1607 0000
 1610 6224
 1611 3223
 1612 6214
 1613 7041
 1614 1223
 1615 7640
 1616 5607
 1617 4471
 1620 5607
 1621 2207

RIF
 DCA SAVIF
 RDF
 CIA
 TAD SAVIF
 SZA CLA
 JMP I CBANK
 JMS I XTBNK
 JMP I CBANK
 ISZ CBANK

/READ INST. FIELD
 /SAVE
 /READ DATA FIELD

/EQUAL IF AC=0
 /DOESN'T HAVE PROGRAM
 /INCREMENT DATA FIELD
 /TEST NEW FIELD
 /DONE ALL CAUSE PROGRAM NOW
 /IN HIGHEST FIELD
 /EXIT

JMP I CBANK
 SAVIF, 0

/ROUTINE TO SET DF FOR NEXT FIELD

NXTBNK, 0

1624 0000
 1625 7200
 1626 6214
 1627 7041
 1630 1124
 1631 7640
 1632 5235
 1633 2224
 1634 5242
 1635 6214
 1636 1034
 1637 1125
 1640 3241
 1641 6201

CLA
 RDF
 CIA
 TAD LAST1
 SZA CLA
 JMP ,+3
 ISZ NXTBNK
 JMP ,+6
 RDF
 TAD K10
 TAD KCDF
 DCA ,+1
 CDF 00

/READ DATA FIELD

/C(LAST1) = LAST TO TEST
 /ALL DONE IF 0

/EXIT

/INCREMENT DATA FIELD
 /ADD ,6201

/CHANGE TO NEW DATA FIELD

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 /CHECK SWITCH REGISTER
 1642 7634 LAS /CHECK HALT
 1643 7712 SPA CLA /GO HALT, SR0=1
 1644 4527 JMS I XHLT
 1645 5624 JMP I NXTBKN /EXIT
 /RESTORE DATA FIELD AND CHECK SR
 /FEILD, 0
 1646 2000 CLA
 1647 7200 RDF
 1650 6214 DCA DATFLD /SAVE TESTED FIELD#
 1651 3014 RIF
 1652 6224 TAD KCDF
 1653 1125 DCA +1
 1654 3255 CDF 00 /MAKE DATA AND INST FIELD EQUAL
 1655 6201 CLA
 1656 7200 JMP I FEILD
 1657 5646

/ *1700
 /START HERE TO LOOP ON ADDRESS
 /
 1700 1700
 1701 7604 CLA /READ LOWER LIMIT
 1702 3123 DCA FIRST1
 1703 7402 HLT /NOW SETUP UPPER LIMIT
 1704 7604 LAS
 1705 3124 DCA LAST1
 1706 1123 TAD FIRST1
 1707 3122 DCA MEMADR
 1710 1522 TAD I MEMADR /READ
 1711 3522 DCA I MEMADR /WRITE
 1712 1122 TAD MEMADR
 1713 7041 CIA
 1714 1124 TAD LAST1
 1715 7650 SNA CLA
 1716 5306 JMP OVER
 1717 2122 ISZ MEMADR
 1720 5310 JMP WRLOP
 1721 7402 HLT
 /CFLO, 0
 1722 2000 CLA
 1723 7200 TAD DATFLD /TEST FIELD
 1724 1014 TAD KCDF
 1725 1125 DCA +1
 1726 3327 CDF 00 /RESTORE TEST FIELD
 1727 6201 CLA
 1730 7200 JMP I CFLO
 1731 5722 /EXIT

PAL10	VI41	2-NOV-71	0119	PAGE 1-18
		/PRINT ERROR ROUTINE		
2000	2000	2		/READING 1'S IF LINK = 1
2001	2000	SEL		
2002	7430	CMA		/SAVE BAD DATA
2003	7040	DCA BAD		
2004	3363	SEL		/SAVE GOOD DATA
2005	7430	CMA		
2006	7040	DCA GOOD		/OCTAL ADDRESS
2007	3364	TAD 10		/RESTORE DATA FIELD
2010	3365	DCA OCADR		/DATA FIELD
2011	4531	JMS I XFILD		/LAST = FIELD WITH LAST ERROR
2012	1014	TAD DATFLD		/SAME IF 0
2013	7041	CIA		/DON'T STORE
2014	1366	TAD LAST		
2015	7650	SNA CLA		/TABLE POINTER
2016	5233	JMP SW2		
2017	1014	TAD DATFLD		/END OF TABLE IF = 0
2020	3366	DCA LAST		/RESTORE POINTER
2021	1022	TAD ERNRD		/INCREMENT POINTER
2022	7041	CIA		/STORE IN TABLE
2023	1021	TAD ENTBL		
2024	7640	SZA CLA		
2025	5230	JMP .+3		
2026	1020	TAD ERBL		
2027	3022	DCA ERNRD		
2030	1014	TAD DATFLD		
2031	2022	ISZ ERNRD		
2032	3422	DCA I ERNRD		
		/SW2,		
2033	7604	LAS		
2034	7006	RTL CLA		/SR2 ON A 1 = RING BELL
2035	7700	SMA SW1		
2036	5242	JMP SW1		/RING BELL
2037	1367	TAD K207		
2040	4346	JMS PRERR		
2041	5311	JMP SW0		
2042	7604	LAS		
2043	7004	RAL		
2044	7700	SMA CLA		/SR1 A 1 = NO PRINT
2045	5254	JMP EPRNT		
2046	1014	TAD DATFLD		
2047	1125	TAD KODF		
2050	3251	DCA .+1		
2051	6201	CDF 00		/SET TO TESTED FIELD
2052	7200	CLA		
2053	5600	JMP I ERROR		
		/EPRNT,		
2054	4354	JMS CRLF		/CR,LF
2055	1014	TAD DATFLD		/TEST NUMBER
2056	7012	RTR		
2057	7010	RAR		

```
2060 1142 TAD K260
2061 4346 JMS PRERR
2062 1142 TAD M14
2063 3074 DCA LOOP
2064 4315 JMS SPING
2065 1365 TAD OCADR
2066 3134 DCA CHAR
2067 4326 JMS PROCTL
2070 1141 TAD M10
2071 3074 DCA LOOP
2072 4315 JMS SPING
2073 1364 TAD GOOD
2074 3134 DCA CHAR
2075 4326 JMS PROCTL
2076 1061 TAD M5
2077 3074 DCA LOOP
2100 4315 JMS SPING
2101 1363 TAD BAD
2102 3134 DCA CHAR
2103 4326 JMS PROCTL
2104 1061 TAD M5
2105 3074 DCA LOOP
2106 4315 JMS SPING
2107 1062 TAD TNUM
2110 4346 JMS PRERR
```

```
/PRINT
/=12 DECIMAL
```

```
/SPACE 12
/OCTAL ADDR
/SAVE
```

```
/PRINT
/=8 DECIMAL
```

```
/SPACE 8
```

```
/PRINT
/=5
```

```
/SPACE 5
```

```
/PRINT
```

```
/SPACE 5
/TEST NUMBER
/PRINT
```

```
/SW0, LAS SPA CLA /CH CK SR0
2111 7604 SPA CLA /GO HALT
2112 7710 JMS I XHLT
2113 4527 JMP EREXT
2114 5246
```

```
/PRINT SPACES
```

```
/SPING, 0 TAD K240 /SPACE
2115 0000 TLS /PRINT
2116 1370 TSF
2117 6046
```

```
2120 6041 JMP ,=1
2121 5320 ISE LOOP
2122 2074 JMP SPING+2
2123 5317 CLA
2124 7200 JMP I SPING
2125 5715 /EXIT
```

```
/PRINT OCTAL
```

```
2126 0000 PROCTL, 0 /=4 /DIGIT COUNTER
2127 1060 TAD M4
2130 3074 DCA LOOP
2131 1134 POSITN, TAD CHAR
2132 7104 CLL RAL
2133 7006 RTL
2134 3134 DCA CHAR
```

PAL10	V141	2 NOV=71	0119	PAGE 1=20
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2135	1134	TAD CHAR		
2136	7004	RAL		
2137	0133	AND K7		
2140	1142	TAD K260		/MAKE ASCII
2141	4346	JMS PRERR		/PRINT ONE
2142	2074	ISZ LOOP		
2143	5331	JMP POSITN		/DO NEXT
2144	7200	CLA		
2145	5726	JMP I PROCTL		/EXIT

		/PRINT A NUMBER		
		PRERR, 0		
2146	0000	TLS		
2147	6046	TSF		
2150	6041	JMP I=1		
2151	5350	CLA		
2152	7200	JMP I PRERR		/EXIT
2153	5746			

		/CARRIAGE RETURN, LINE FEED		
		CRLF, 0		
2154	0000	CLA		/CR
2155	7200	TAD K215		
2156	1143	JMS PRERR		/LF
2157	4346	TAD K212		
2160	1371	JMS PRERR		
2161	4346	JMP I CRLF		
2162	5754			

		BAD, 0		
2163	0000	GOOD, 0		
2164	0000	OCADR, 0		
2165	0000	LAST, 0		
2166	0000	K207, 207		
2167	0207	K240, 240		
2170	0240	K212, 212		
2171	0212			

		*2200		
		/ROUTINE TO SET DF TO FIRST TEST FIELD		
		SETU1, 0		
2200	0000	CLA		
2201	7200	TAD FIRST1		/FIRST TO TEST
2202	1123	TAD KCDF		
2203	1125	DCA I+1		
2204	3205			

		CDF 00		/CHANGE TO TEST FIELD
2205	6201	JMP I SETU1		/EXIT
2206	5600			

		/ROUTINE TO ACCEPT TEST LIMITS FROM		
		/KEYBOARD INPUT		

PAL10	V141	2-NOV-71	0119	PAGE 1-21
2207	0000	SLMTS, 2	JMS I XFILD	
2210	4531		JMS I XCRLF	/CR, LF
2211	4547		JMS I XTLIM	/PRINT TEST LIMITS
2212	4751		JMS I XCRLF	/CR, LF
2213	4547		JMS KEYIN	/GO ACCEPT INPUT
2214	4303		JMS LEGAL	/SEE IF IT'S LEGAL
2215	4314		TAD CHAR	
2216	1134		AND K7	/MASK AC 9-11
2217	0133		CLL RAL	
2220	7104		RTL	/POSITION TO AC 6-8
2221	7006		DCA FIRST1	/FIRST TO TEST
2222	3123		JMS KEYIN	/WAIT FOR COMMA
2223	4303		TAD CHAR	/GET INPUT
2224	1134		CIA	
2225	7041		TAD K254	
2226	1353		SNA	/OK IF 0
2227	7450		JMP +3	
2230	5233		JMS QUERY	/PRINT QUESTION MARK
2231	4344		JMP SLMTS+2	
2232	5211		JMS KEYIN	/WAIT FOR 2ND
2233	4303		JMS LEGAL	/SEE IF IT'S LEGAL
2234	4314		TAD CHAR	
2235	1134		AND K7	/MASK AC 9-11
2236	0133		CLL RAL	
2237	7104		RTL	/POSITION TO AC 6-8
2240	7006		DCA LAST1	/LAST TO TEST
2241	3124		TAD FIRST1	
2242	1123		CIA LAST1	
2243	7041		TAD LAST1	
2244	1124		SMA OKAS	/1ST IS > LAST IF NEG
2245	7500		JMP OKAS	
2246	5256		CLA	
2247	7200			
2250	1123		TAD FIRST1	
2251	3134		DCA CHAR	/LAST NOW IS FIRST
2252	1124		TAD LAST1	/FIRST IS NOW LAST
2253	3123		DCA FIRST1	
2254	1134		TAD CHAR	
2255	3124		DCA LAST1	
2256	7200	OKAS,	CLA LAST1	
2257	1124		CIA LAST1	
2260	7041		TAD FIRST1	/SEE IF EQUAL
2261	1123		SEA	/YES IF 0
2262	7440		JMP ALOK	/NOW SEE IF IT HAS PROGRAM
2263	5273		TAD FIRST1	
2264	1123		CIA	/CURRENT FIELD
2265	7041		TAD INSFLD	/NO IF A 1
2266	1016		SEA CLA	
2267	7640		JMP ALOK	/PRINT PROGRAM LOCATION
2270	5273		JMS I XLCA	/AND START OVER
2271	4752		JMP SLMTS+2	/WAIT FOR C.R.
2272	5211		JMS KEYIN	
2273	4303	ALOK,		

PAL10 V141 2274 1134 TAD CHAR
 2275 7041 CIA
 2276 1143 TAD K215
 2277 7450 SNA
 2300 5607 JMP I SLMTS
 2301 4344 JMS QUERY
 2302 5211 JMP SLMTS+2

2303 0000 / KEYIN:
 2304 6032 KCC
 2305 6031 KSF
 2306 5305 JMP :=1
 2307 6036 KRB
 2310 3134 DCA CHAR
 2311 1134 TAD CHAR
 2312 4536 JMS I XPERR
 2313 5703 JMP I KEYIN

/NOT A C.R. IF A SKIP

/PRINT QUESTION MARK
 /START OVER

6211
 2314 0000 / LEGAL:
 2315 1134 TAD CHAR
 2316 7041 CIA
 2317 1144 TAD K377
 2320 7650 SNA CLA
 2321 5211 JMP SLMTS+2
 2322 1134 TAD CHAR
 2323 0145 AND K370
 2324 7041 CIA
 2325 1142 TAD K260
 2326 7650 SNA CLA
 2327 5714 JMP I LEGAL
 2330 1134 TAD CHAR
 2331 7041 CIA
 2332 1353 TAD K254
 2333 7650 SNA CLA
 2334 5714 JMP I LEGAL
 2335 1134 TAD CHAR
 2336 7041 CIA
 2337 1143 TAD K215
 2340 7650 SNA CLA
 2341 5714 JMP I LEGAL
 2342 4344 JMS QUERY
 2343 5211 JMP SLMTS+2

/RUB-OUT IF 0

/A COMMA IF 0

/A C.R. IF 0

/QUERY
 /START OVER

2344 0000 / QUERY:
 2345 4547 JMS I XCRLF
 2346 1146 TAD K277
 2347 4536 JMS I XPERR
 2350 5744 JMP I QUERY

/PRINT QUERY MARK

2351 2446 / XLIM,
 2352 2400 XLCAT,
 2353 0254 K254,
 254 TLIM,
 254 LOCAT

```
/
/PRINT FIELD PROGRAM IS IN
/
*2400
/
LOCAT, 2
2400 0000
2401 7200
2402 1016
2403 7012
2404 7010
2405 0133
2406 1142
2407 3244
2410 1217
2411 3012
2412 1412
2413 7450
2414 5600
2415 4536
2416 5212

          CLA      INSFLD      /CURRENT FIELD
          TAD      RTR
          RAR
          AND K7
          TAD K260
          DCA FLDN
          TAD PRGAM
          DCA 12
          TAD I 12
          SNA
          JMP I LOCAT
          JMS I XPERR
          JMP PLOCT

          PLOCT,
          TAD I 12
          SNA
          JMP I LOCAT
          JMS I XPERR
          JMP PLOCT

          /DONE IF 0
          /EXIT
          /PRINT

          /PRGAM,
          320
          322
          317
          307
          322
          301
          315
          240
          311
          323
          240
          311
          316
          240
          306
          311
          305
          314
          304
          240
          0
          0

          /P
          /R
          /O
          /G
          /R
          /A
          /M
          /I
          /S
          /I
          /N
          /F
          /I
          /E
          /L
          /D
          /X
          /TERMINATOR

          FLDN,
          320
          322
          317
          307
          322
          301
          315
          240
          311
          323
          240
          311
          316
          240
          306
          311
          305
          314
          304
          240
          0
          0

          /TLIMIT, 0
          CLA
          TAD TSTL
          DCA 12
          TAD I 12
          SNA
          JMP I TLIMIT

          PLIMIT,
          1412
          7450
          5646

          /DONE IF 0
          /DONE IF 3
```

JMS I XPERR
JMP PLINT

```

2455 4536
2456 5252
2457 2457
2460 0324
2461 0305
2462 0323
2463 0324
2464 0240
2465 0314
2466 0311
2467 0315
2470 0311
2471 0324
2472 0323
2473 0000

/ TSTL.
324 /T
305 /E
323 /S
324 /T
240 /
314 /L
311 /I
315 /M
311 /I
324 /T
323 /S
0 /TERMINATOR

/ HEADER ROUTINE
/ PHDR.
0 JMS I XCRLF /CR, LF
TAD FILD
DCA 12
TAD I 12 /PRINT FIELD
SNA /DONE IF 0
JMP I +3
JMS I XPERR
JMP PFILD
TAD M5
DCA LOOP /SPACE 5
JMS I XPING
TAD OTLDR
DCA 12

```

```

2474 0000
2475 4547
2476 1332
2477 3012
2500 1412
2501 7450
2502 5305
2503 4536
2504 5300
2505 1061
2506 3074
2507 4537
2510 1341
2511 3012

POGDR, TAD I 12 /PRINT OCTAL ADR
SNA /DONE IF 0
JMP I +3
JMS I XPERR
JMP POCOR
TAD M5
DCA LOOP /SPACE 5
JMS I XPING
TAD GOOD
DCA 12
TAD I 12 /PRINT GOOD
SNA /DONE IF 0
JMP I +3
JMS I XPERR
JMP PGOOD
BSPCE /NEXT PAGE
FILD. /F
306

```

```

2512 1412
2513 7450
2514 5317
2515 4536
2516 5312
2517 1061
2520 3074
2521 4537
2522 1355
2523 3012
2524 1412
2525 7450
2526 5731
2527 4536
2530 5324
2531 2600
2532 2532
2533 0306

```

/I
/E
/L
/D

311
305
314
304
0

/ OYLDOR,

/O
/C
/T
/A
/L
/A
/D
/R

2541
317
303
324
301
314
240
301
304
322
256
0

/ GOOD,

/G
/O
/O
/D

307
317
317
304
0

/ EXHDR, JMP I PHDR

/ *2600

/ BSPACE,

/SPACE 5

TAD M5
DCA LOOP
JMS I XPING
TAD BADD
DCA 12
TAD I 12

/PRINT BAD
/DONE IF 0

PBAD,

SNA
JMP I +3
JMS I XPERR
JMP PBAD
TAD M5
DCA LOOP
JMS I XPING

/SPACE 5

/

/PRINT TEST
/DONE IF 0

TAD TSTN
DCA 12
TAD I 12

PTSTN,

SNA
JMP I +3
JMS I XPERR
JMP PTSTN
JMS I XCRLF
JMP I XPHDR

/CR, LF
/EXIT

2534 0311
2535 0305
2536 0314
2537 0304
2540 0000

2541
2542 0317
2543 0303
2544 0324
2545 0301
2546 0314
2547 0240
2550 0301
2551 0304
2552 0322
2553 0256
2554 0000

2555
2556 0307
2557 0317
2560 0317
2561 0304
2562 0000

2563 5674

2600

1061
3074
4537
1234
3012
1412
7450
5212
4536
5205
1061
3074
4537

1226
3012
1412
7450
5224
4536
5217
4547
5644

2600
2601
2602
2603
2604
2605
2606
2607
2610
2611
2612
2613
2614
2615
2616
2617
2620
2621
2622
2623
2624
2625

```
2626 2626 TSTN, 324 /T
2627 2324 305 /E
2630 0305 323 /S
2631 0323 324 /T
2632 0324 0 /
2633 0000 /
2634 2634 BADD, 302 /B
2635 0302 301 /A
2636 0301 304 /D
2637 0304 0 /
2640 0000 /
2641 0000 HALT, 0 /
2642 7402 HLT /
2643 5641 JMP I HALT /RESTART HERE OR RTRN1
2644 2563 XPHDR, EXHDR /

/
/
/ WAIT HERE TO SETUP SR: TYPE CARRIAGE RETURN
/ AFTER SETTING SR.
/
SETSR, 0 /
JMS I XFILD /RESTORE DATA FIELD
JMS I XCRLF /CR, LF
TAD STSR
DCA 12
TAD I 12 /PRINT SETUP SR
SNA /DONE IF 0
JMP I+3
JMS I XPERR
JMP PSTSR
KRB
KSF
JMP I+4
KRB
JMS I XPERR
JMP I SETSR
LAS
DCA MCWA
JMP WTCR
WTCR,
/
STSR,
323 /S
305 /E
324 /T
325 /U
320 /P
240 /S
323 /S
```


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3053	1422	EQUAL,	TAD I ERWRD	/GET ERROR FIELD
3054	7041		CIA	
3055	1123		TAD FIRST1	
3056	7650		SNA CLA	/DON'T MOVE IF = TO FIRST
3057	5530		JMP I XRTN	/START OVER
3060	1422		TAD I ERWRD	
3061	7650		SNA CLA	/IS IT FIELD 0?
3062	5266		JMP SUB1	/YES
3063	1016		TAD INSFLD	/CURRENT NEXT
3064	1141		TAD M10	/SUBTRACT 1 FROM DF
3065	3151		DCA NXLOC	

3066	1020	SUB1,	TAD ERTBL	/RESTORE TABLE POINTER
3067	3022		DCA ERWRD	
3070	1151		TAD NXLOC	
3071	7041		CIA	
3072	1016		TAD INSFLD	/NEXT = CURRENT NEXT IF 0
3073	7650		SNA CLA	
3074	5253		JMP EQUAL	/NEW CURRENT FIELD
3075	1151		TAD NXLOC	
3076	3016		DCA INSFLD	/IS IT = LOWEST FIELD
3077	1016		TAD INSFLD	/YES
3100	7041		CIA	/CURRENT NEW FIELD
3101	1123		TAD FIRST1	/SUBTRACT 1 FROM DF
3102	7650		SNA CLA	/NEXT FIELD LOWER
3103	5231		JMP CKERR	
3104	1016		TAD INSFLD	
3105	1141		TAD M10	
3106	3151		DCA NXLOC	
3107	5231		JMP CKERR	

3110	7200	STMV,	CLA	/RESTORE TABLE POINTER
3111	1020		TAD ERTBL	
3112	3022		DCA ERWRD	
3113	6224		RIF	
3114	3723		DCA I XSRCE	
3115	1723		TAD I XSRCE	
3116	7041		CIA	
3117	1016		TAD INSFLD	/DON'T MOVE IF EQUAL
3120	7650		SNA CLA	/START OVER
3121	5530		JMP I XRTN	/GO MOVE
3122	5724		JMP I XMVE	

3123	3323	XSRCE,	SOURCE	
3124	3307	XMVE,	MOVE	
3125	3200	XTMV,	NXTMV	

3200	3200	*3200		
3200	7600	NXTMV,	7600	

PAL10	V141	2-NOV-71	0119	PAGE 1-30
3201	6224			
3202	3323			
3203	2022	CHNXT,		/CURRENT FIELD
3204	1200			/POINTER +1
3205	7041			
3206	1422			
3207	7650			
3210	5225			/NO ERRORS RECORDED IF 0
3211	1422	CKNXT,		/INITIALIZE MOVE
3212	7041			
3213	1151			
3214	7650			/ERROR IN NEW FIELD IF 0
3215	5255			/TRY NEXT LOWER FIELD
3216	1022			
3217	7041			
3220	1021			
3221	7650			/DONE WITH TABLE IF 0
3222	5225			/INITIALIZE MOVE
3223	2022			/POINTER +1
3224	5211			
3225	1020	/STNXT,		
3226	3022			/RESTORE TABLE POINTER
3227	1151			/NEXT LOWER FIELD
3230	7041			
3231	1016			
3232	7650			/NEXT=CURRENT IF 0
3233	5242			
3234	1151			
3235	7041			
3236	1123			/NEXT = LOWEST IF 0
3237	7640			/MOVE TO LOWEST TEST FIELD
3240	5247			
3241	5302			
3242	1151	CKNT,		
3243	7041			
3244	1123			
3245	7650			
3246	5275			/NEXT = LOWEST IF 0
3247	1151	STNX,		/SETUP TO MOVE TO HIGHEST
3250	3016			/NEXT LOWER FIELD
3251	1016			/IS NOW CURRENT FIELD
3252	1141			/SUBTRACT 1 FROM NEW
3253	3151			/NEW NEXT LOWER FIELD
3254	5307			/GO MOVE
3255	1020	/SUB2,		
3256	3022			/RESTORE TABLE POINTER
3257	1151			/NEXT LOWER FIELD
3260	7450			/FIELD 0 IF 0
3261	5530			/START OVER CAN'T MOVE
3262	1141			/SUBTRACT 1

PAL10	V141	2-NOV-71	2119	PAGE 1-31	
3263	3151		DCA NXLOC	/NOW # 2 FIELDS LOWER	
3264	1151		TAD NXLOC		
3265	7041		CIA		
3266	1016		TAD INSFLD	/CURRENT FIELD	
3267	7640		SZA CLA	/ARE THEY EQUAL	
3270	5203		JMP CHNXT	/NO	
3271	1151		TAD NXLOC	/YES	
3272	7450		SNA	/DOES IT # FIELD 0	
3273	5203		JMP CHNXT	/YES	
3274	5262		JMP SUB2+5	/NO	
3275	1124		TAD LAST1	/VERY LAST TO TEST	
3276	3151		DCA NXLOC	/MAKE IT NEXT FIELD	
3277	1124		TAD LAST1		
3300	3016		DCA INSFLD		
3301	5203		JMP CHNXT		
3302	1151		TAD NXLOC		
3303	3016		DCA INSFLD		
3304	6224		RIF		
3305	3323		DCA SOURCE		
3306	3015		DCA FLAGS	/CLEAR BIT 11	

3307	1125		TAD KODF	/6201	
3310	1323		TAD SOURCE	/CURRENT FIELD	
3311	3323		DCA SOURCE	/SOURCE NOW # CDF N	
3312	1125		TAD KODF	/6201	
3313	1016		TAD INSFLD	/NEW FIELD	
3314	3327		DCA DESTN	/DESTN NOW # CDF N	
3315	1323		TAD SOURCE		
3316	7041		CIA		
3317	1327		TAD DESTN		
3320	7650		SNA CLA		
3321	5530		JMP I XRTN		
3322	3074		DCA LOOP		
3323	0000		SOURCE,	/4K COUNTER	
3324	1474		TAD I LOOP	/WILL # CDF N	
3325	3347		DCA SAVGD	/TAKE FROM HERE	
3326	1347		TAD SAVGD	/SAVE INSTRUCTION	
3327	0000		DESTN,	/GET IT BACK	
3330	3474		DCA I LOOP	/PUT IN HERE	
3331	1474		TAD I LOOP	/GET INFORMATION STORED	
3332	7041		CIA		
3333	1347		TAD SAVGD	/COMPARE TO THIS VALUE	
3334	7650		SNA CLA	/WERE THEY THE SAME	
3335	5340		JMP I +3	/YES CONTINUE	
3336	7402		HLT	/NO, RELOCATION ERROR	
3337	5323		JMP SOURCE	/TRY SAME AGAIN	
3340	2074		ISZ LOOP	/DONE 4K WHEN SKIP	
3341	5323		JMP SOURCE	/KEEP MOVING	
3342	1126		TAD KCIF	/6202	

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3343	1016			
3344	3345		TAD INSFLD	/NEW FIELD
3345	6202		DCA :+1	
3346	5530		CIF 00	/CHANGE TO NEW FIELD
			JMP I XRTN	/EXIT TO RTN1 IN
3347	0000			/NEW FIELD
			SAVGD, 0	
			\$	

4000
4100
4200
4300
4400
4500
4600
4700
5000
5100
5200
5300
5400
5500
5600
5700
6000
6100
6200
6300
6400
6500
6600
6700
7000
7100
7200
7300
7400
7500
7600
7700

PAL10	V141	2=NOV=71	0119	PAGE 1=35					
ALAW	2220	FLCNT	0073	LEGAL	2314	RD4	1455		
ALOK	2273	FLOD	2444	LOCAT	2400	RDALL	0736		
BAD	2153	FLOAD	0017	LOOP	0074	RDF	6214		
BADD	2634	GENRAN	2716	M10	0140	RDLOP	0753		
BEGIN	3200	GOOD	2555	M14	0140	RIF	6224		
BSPCE	2600	GOOD	2164	M20	0056	RLOPA	1011		
CBANK	1607	HALT	2641	M4	0060	RSTR1	0211		
CDF	6201	INSFLD	0016	M40	0057	R1N1	0213		
CDON1	0760	JMS1	0110	M5	0061	R1T	1020		
CFLD	1722	JMS2	0111	MCHA	0033	SAVGD	3347		
CHAR	0134	JMS3	0112	MEMADR	0122	SAVIF	1623		
CHNXT	3203	JMS4	0113	MOVE	3307	SETSR	2645		
CIF	6202	JMS5	0114	MVBK	3302	SETU1	2200		
CKBNK	1600	K1	0150	NXLOC	0151	SLMTS	2207		
CKERR	3031	K10	0034	NXTBNK	1624	SOURCE	3323		
CKNT	3242	K100	0040	NXTHI	3275	SPING	2115		
CKNXT	3211	K177	2756	NXTMV	3200	STALL	2702		
CLOP1	1252	K1K	1036	OCADR	2165	STMV	3110		
CLOP2	1327	K20	0042	OKAS	2256	STNX	3247		
CLOP3	1405	K200	0037	OTLDR	2541	STNXT	3225		
CLOP4	1462	K207	2167	OVER	1706	STSR	2670		
CMOVE	3000	K212	2171	PBAD	2605	SUB1	3066		
CNXT	3037	K215	0143	PFILD	2500	SUB2	3255		
COUNT	0072	K240	2170	PGOOD	2524	SW0	2111		
CRLF	2194	K254	2353	PHDR	2474	SW1	2042		
DATFLD	0014	K260	0142	PLINT	2452	SW2	2033		
DESTN	3327	K261	0052	PLOCT	2412	TBLRAN	2755		
ENTBL	0021	K262	0053	POCDR	2512	TOM20	0106		
EPNRT	2054	K263	0054	POSITN	2131	TOM40	0107		
EQUAL	3053	K264	0035	PRERR	2146	TLINT	2446		
EXEXT	2046	K277	0146	PRGAM	2417	TNUM	0062		
ERROR	2000	K370	0145	PROCTL	2126	TST1	0300		
ERTBL	0020	K377	0144	PTSR	2652	TST1C	0324		
ERWRD	0022	K40	0041	PYSTN	2617	TST2	0400		
EXAM1	0241	K400	0036	QUERY	2344	TST2C	0425		
EXAM2	0246	K7	0133	RANCON	2741	TST3	0452		
EXAM3	0253	K740	0035	RANDEX	2742	TST3C	0477		
EXAM4	0260	KCDF	0125	RANTAB	2754	TST4	0600		
EXHDR	2563	KCIF	0126	RANTAD	2734	TST4C	0625		
EXIT	0153	KEYIN	2303	RANTBL	2743	TSTL	2457		
EXT1	0322	KRXT	1035	RCHK1	1037	TSTN	2626		
EXT1C	0343	KXT1	0345	RCHK1C	1054	W0011	0652		
EXT2	0423	KXT1C	0346	RCHK2	1071	W0110	0704		
EXT2C	0445	KXT2	0450	RCHK2C	1106	W1001	0721		
EXT3	0475	KXT2C	0451	RCHK3	1123	W1100	0667		
EXT3C	0517	KXT3	0522	RCHK3C	1140	WRLOP	1710		
EXT4	0623	KXT3C	0523	RCHK4	1200	W1CR	2660		
EXT4C	0647	KXT4	0645	RCHK4C	1217	X0011	0064		
FEILD	1646	KXT4C	0646	RCHKA	1000	X0110	0066		
FILD	2532	LAST	2166	RD1	1245	X1001	0067		
FIRST1	0123	LAST1	0124	RD2	1322	X1100	0065		
FLAGS	0015	LASTX	0154	RD3	1400	XBANK	0063		

XCFL	V141		
XCHK1	1244		
XCHK1C	0076		
XCHK2	0077		
XCHK2C	0100		
XCHK3	0101		
XCHK3C	0102		
XCHK4	0103		
XCHK4C	0104		
XCHKA	0105		
XCHKA	1236		
XCRLF	0147		
XFILD	0131		
XHDR	0135		
XHLT	0127		
XIT1	0320		
XIT1C	0341		
XIT2	0421		
XIT2C	0443		
XIT3	0473		
XIT3C	0515		
XIT4	0621		
XIT4C	0643		
XKBNK	0070		
XL CAT	2392		
XLMTS	0043		
XLOPA	1237		
XLOPB	1240		
XLOPC	1241		
XLOPD	1242		
XLOPE	1243		
XMOVE	0050		
XMOVE	3124		
XPERR	0136		
XPHDR	2644		
XPING	0137		
XPRER	0132		
XRALL	0075		
XRD1	0115		
XRD2	0116		
XRD3	0117		
XRD4	0120		
XRROR	0121		
XRTN	0130		
XSALL	0132		
XSETU	0051		
XSRCE	3123		
XSTSR	0277		
XTBNK	0071		
XTLIM	2351		
XTMV	3125		
XTST1	0044		
XTST2	0045		

XTST3 0046
XTST4 0047

ERRORS DETECTED: 0

LINKS GENERATED: 0

RUN-TIME: 13 SECONDS

2K CORE USED