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PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E3, E11, E22, E27	DEC 7437N	IC 1905004	3
2	E4, E23, E26	DEC 7450N	IC 1905550	3
3	E5, E10	DEC 7453N	IC 1905582	2
4	E6	DEC 8619A	IC 1905713	1
5	E7, E15, E29, E22	DEC 74H74N	IC 1909667	4
6	E8, E16, E21, E28	DEC 74H74N	IC 1909667	4
7	E9	DEC 74H74N	IC 1909667	4
8	E13	DEC 74H32N	IC 1905059	1
9	E14, E24	DEC 7410N	IC 1905576	2
10	E16	DEC 74H00N	IC 1905056	1
11	E25	DEC 7460N	IC 1905581	1
12	E30	DEC 7420N	IC 1905577	1
13	C11, C26	0.01MFD, 100V, 20%	CAP 1000100	26
14	C27, C28	0.02MFD, 35V, 20%	CAP 1000067	2

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

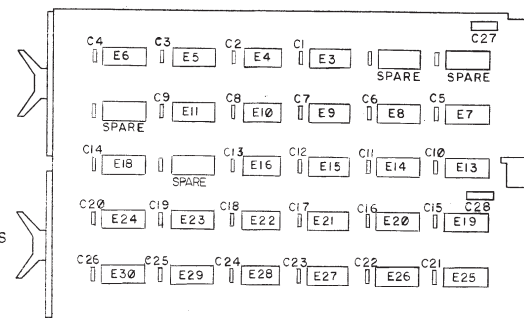
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
6251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT



PIN NOMENCLATURE

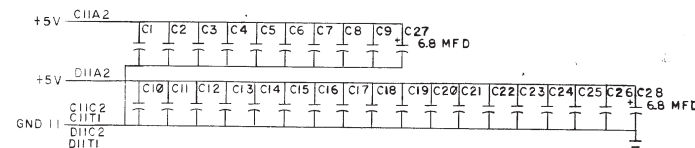
MODULE PROCESSOR

A

C

B

D



REVISIONS	CHANGE NO	REV
1	1	1

DEC FORM NO 100-100-A

FIRST USED ON OPTION/MODEL PDP 11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES 5:100 5:1/64 5:1/32 FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	DATE 3-2-70 DATE 3-2-70 DATE 3-2-70 DATE 3-2-70	DATE 3-2-70 DATE 3-2-70 DATE 3-2-70 DATE 3-2-70	DATE 3-2-70 DATE 3-2-70 DATE 3-2-70 DATE 3-2-70	DATE 3-2-70 DATE 3-2-70 DATE 3-2-70 DATE 3-2-70
MATERIAL	NEXT HIGHER ASSY	TITLE FLAG CNTL		
FINISH	A-ML-KAI-0	SIZE CODE DICS M822-0-1		
SCALE	1 OF 3	NUMBER K12-1		
SHEET	2	REV B		
ETCH REV		DIST.		

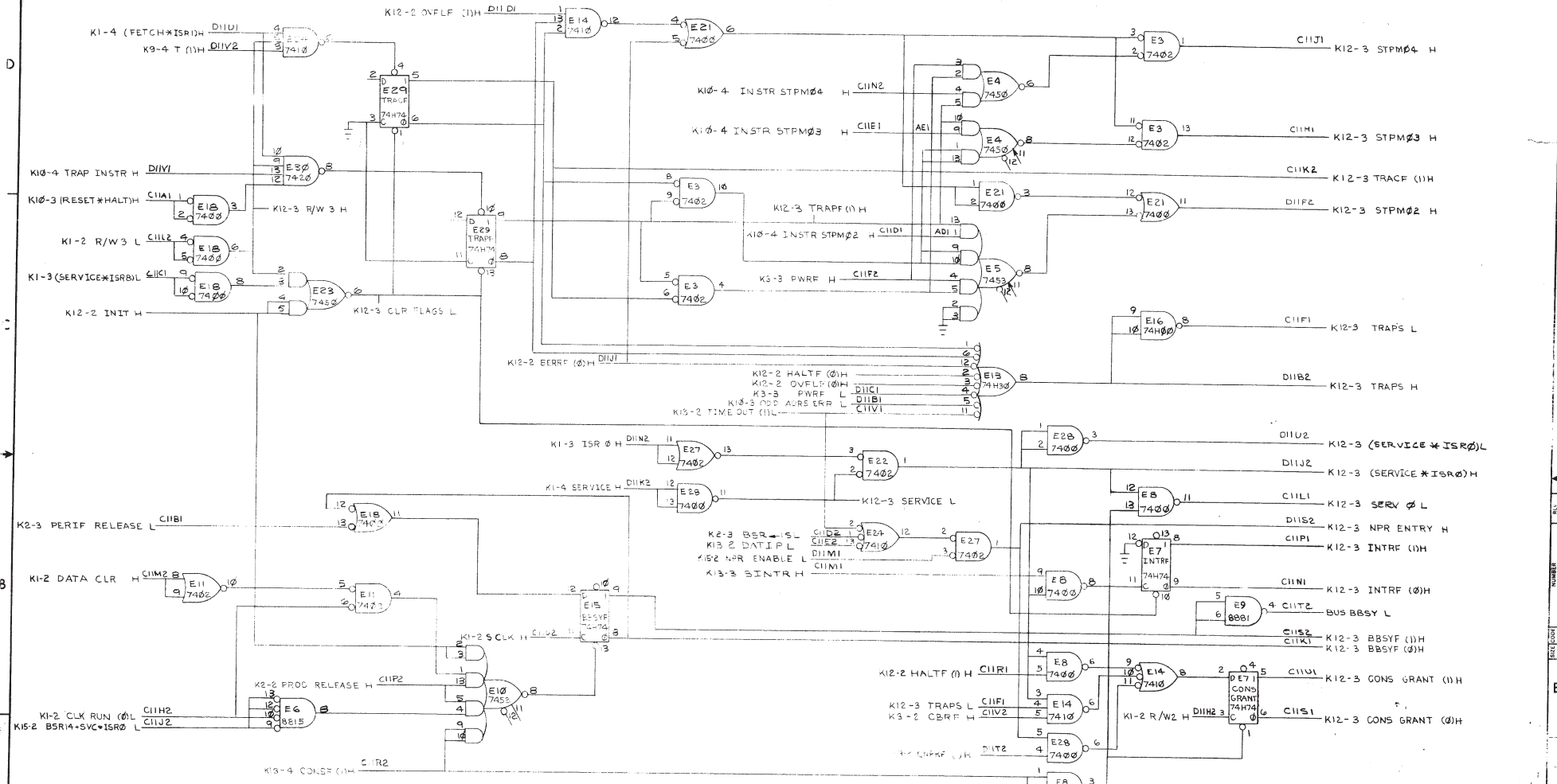
B.

1

1

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1-0-228W 50 2



REV	CHANGE NO.	DATE
1	1	10-1-70

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11					
UNLESS OTHERWISE SPECIFIED		DRN	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED		DATE	12-11-68	DIGITAL EQUIPMENT CORPORATION	
DIMENSION IN INCHES		DATE	9-18-70	MAYNARD MASSACHUSETTS 01940	
TOLERANCES		ENG	DATE	TITLE	
DECIMALS FRACTIONS ANGLES		DATE	3-6-70	FLAG CNTL	
= .005 = .004 = .003		PROJ ENG	DATE	M822	
FINAL SURFACE QUALITY		DATE	3-6-70	K12-3	
REMOVE BURRS AND BREAK SHARP CORNERS		PROD	DATE	D/C S M822-01	
MATERIAL		NEXT HIGHER ASSY			
FINISH		A-ML-KA11-0			
SCALE		NONE			
SHEET		3 OF 3			

10-2-70