

This drawing and specifications herein are the property of Digital Equipment Corporation and shall not be reproduced or copied or used in whole or in part as the basis for the manufacture or sale of items without written permission.

PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1E7E1B,E27E45E54	8251B	C. 19 79594	6
2	E2E5E6E8E16E17E20E21E24E37E39	DEC 7400N	C. 19 74575	16
3	E40E44E49E52E55			
4	E3E4E11E14E15E22E25E34E41E43	DEC 7402N	C. 19 09004	14
5	E46E47E50E51			
6	E9E10E26E57			
7	E12E28E30E36E53E58	DEC 7410N	C. 19 05550	6
8	E13E32E55E60	DEC 7420N	C. 19 05577	4
9	E19E31E33E48E56	DEC 7410N	C. 19 05576	5
10	E28E39	DEC 7450N	C. 19 06582	2
11	E28E42E61	DEC 8815A	C. 19 09713	3
12	E35	DEC 7460N	C. 19 05581	1
13	C1-C33, C35-C61	0.1MFD, 100V, 20%	CAP. 10 01610	60
14	C62-C64, C84	68MFD, 35V, 20%	CAP. 10 00067	4

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

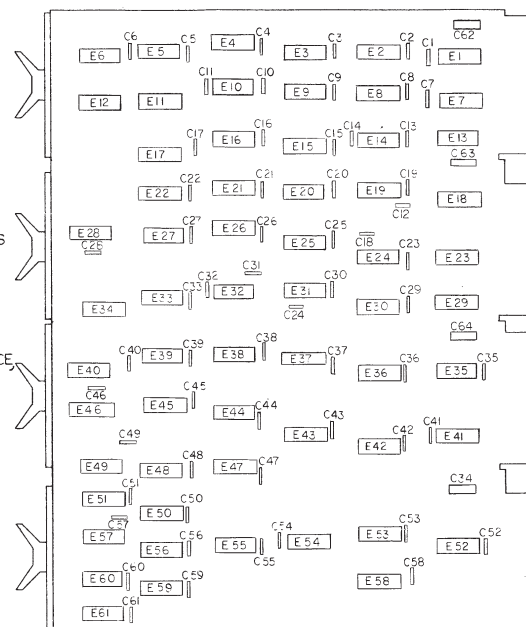
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01MFD

COMPONENT PLACEMENT



PIN NOMENCLATURE

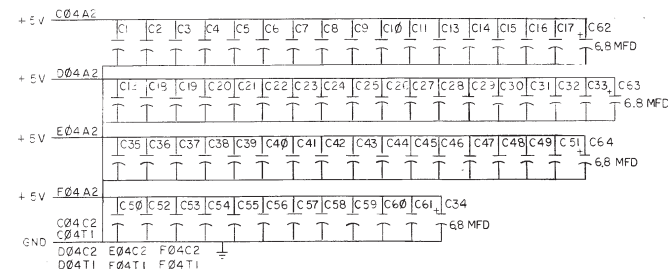
MODULE PROCESSOR

A C

B D

C E

D F



FIRST USED ON OPTION / MODEL PDP 11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES = .005 ± 1/16 ± 0°30' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	DATE 2-4-70	DATE 3-6-70	DATE 3-6-70	DATE 3-6-70
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				
IR DECODE				
M726 KIØ-1				
NEXT HIGHER ASSY A-ML-KAI1-Ø				
SCALE 1/4" = 1"				
SHEET 1 OF 4				
DIST.				

REVISIONS
CHANGE NO.
CHK

DEC FORM NO
DPO 100A

