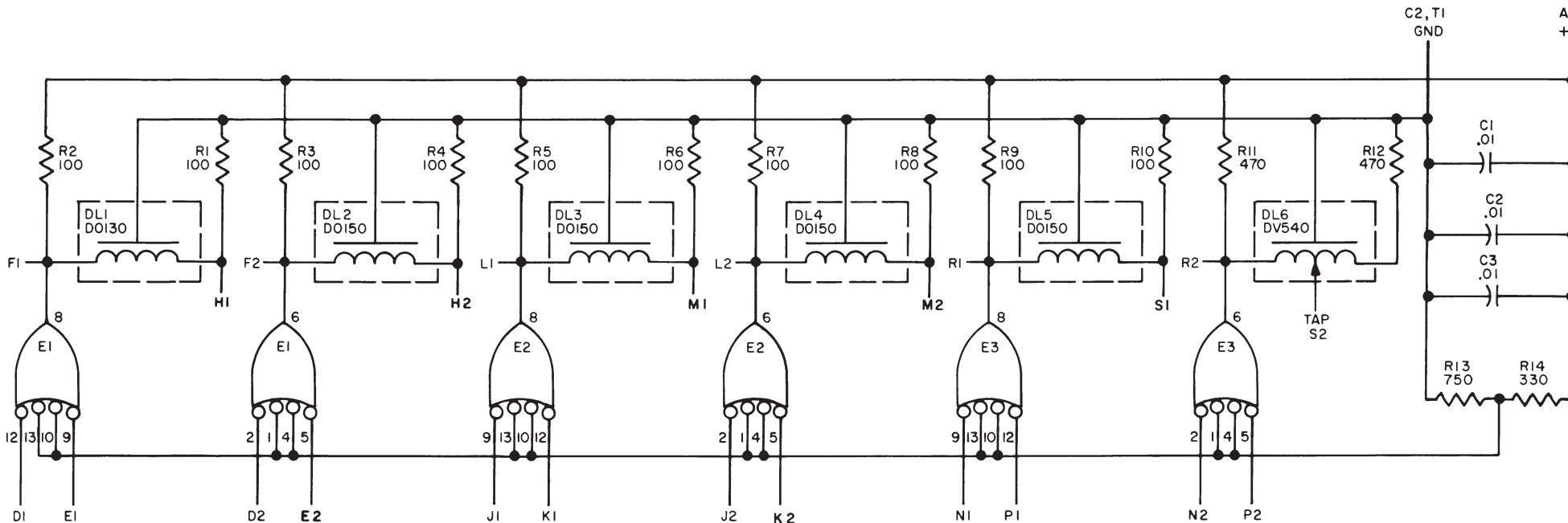


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION



M312-3

UNLESS OTHERWISE INDICATED:  
 IC'S ARE DEC 74H40N  
 PIN 7 ON EACH IC = GND  
 PIN 14 ON EACH IC = +5V  
 RESISTORS ARE 1/4W, 5%

<table border="1"> <tr><th>REVISIONS</th></tr> <tr><td>CHK CHG NO. REV.</td></tr> <tr><td>00001 B</td></tr> <tr><td>00002 C</td></tr> <tr><td>00003 D</td></tr> </table>				REVISIONS	CHK CHG NO. REV.	00001 B	00002 C	00003 D	<table border="1"> <tr><td>DRN. B. BUTLER</td><td>DATE 4/2/69</td></tr> <tr><td>CHK'D M. Mombail</td><td>DATE 4/3/69</td></tr> <tr><td>ENG R.E.O. Carr</td><td>DATE 4/5/69</td></tr> <tr><td>PROD</td><td>DATE 8/8/69</td></tr> </table>		DRN. B. BUTLER	DATE 4/2/69	CHK'D M. Mombail	DATE 4/3/69	ENG R.E.O. Carr	DATE 4/5/69	PROD	DATE 8/8/69	<table border="1"> <tr><th colspan="4">TRANSISTOR &amp; DIODE CONVERSION CHART</th></tr> <tr><th>DEC</th><th>EIA</th><th>DEC</th><th>EIA</th></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> </table>				TRANSISTOR & DIODE CONVERSION CHART				DEC	EIA	DEC	EIA																			<table border="1"> <tr><td colspan="4">TITLE DELAY MODULE M312</td></tr> <tr><td>SIZE B</td><td>CODE CS</td><td>NUMBER M312-0-1</td><td>REV. D</td></tr> <tr><td colspan="3">PRINTED CIRCUIT REV. C</td><td> </td></tr> </table>		TITLE DELAY MODULE M312				SIZE B	CODE CS	NUMBER M312-0-1	REV. D	PRINTED CIRCUIT REV. C			
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