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PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1 E2 E3 E4 E5 E6 E7 E8	DEC 7402N	I.C. 1505004	8
2	E3 E4 E5 E6 E7 E8 E9 E10 E11 E12 E13 E14 E15 E16 E17 E18 E19 E20 E21 E22 E23 E24 E25	7481N	I.C. 1505714	16
3	E7 E10 E15 E22	DEC 7402N	I.C. 1505004	4
4	E10 E21	DEC 7402N	I.C. 1505004	2
5	E10	DEC 7402N	I.C. 1505004	1
6	E17	DEC 7402N	I.C. 1505004	1
7	Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8	DEC 50029B	TRANSISTOR 1523100	8
8	R1 R4 R7 R10 R13 R16 R19 R22	330 1/4 W 5%	RES. 1300235	8
9	R3 R6 R9 R12 R15 R18 R21 R24	1.5 K 1/4 W 5%	RES. 1300235	8
10	R20 R40	1 K 1/4 W 5%	RES. 1300355	16
11	R31 R50	3.5 K 1/4 W 5%	RES. 1300452	16
12	C1 C40	.01 MFD 120V 20%	CAP. 1201610	40
13	C41 C42	5.8 MFD 35V 20%	CAP. 1200067	2
14	R2 R5 R6 R11 R14 R17 R20 R23	100 1/4 W 5%	RES. 1300224	8

NOTES:

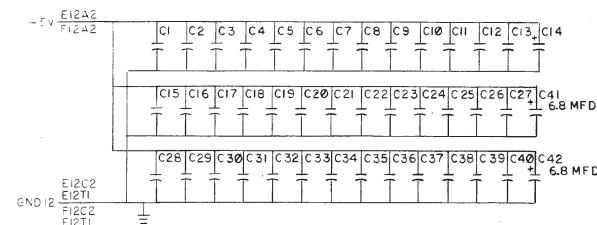
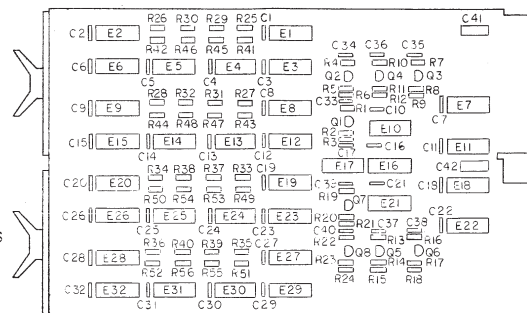
- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
- ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
- PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
- DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
- GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

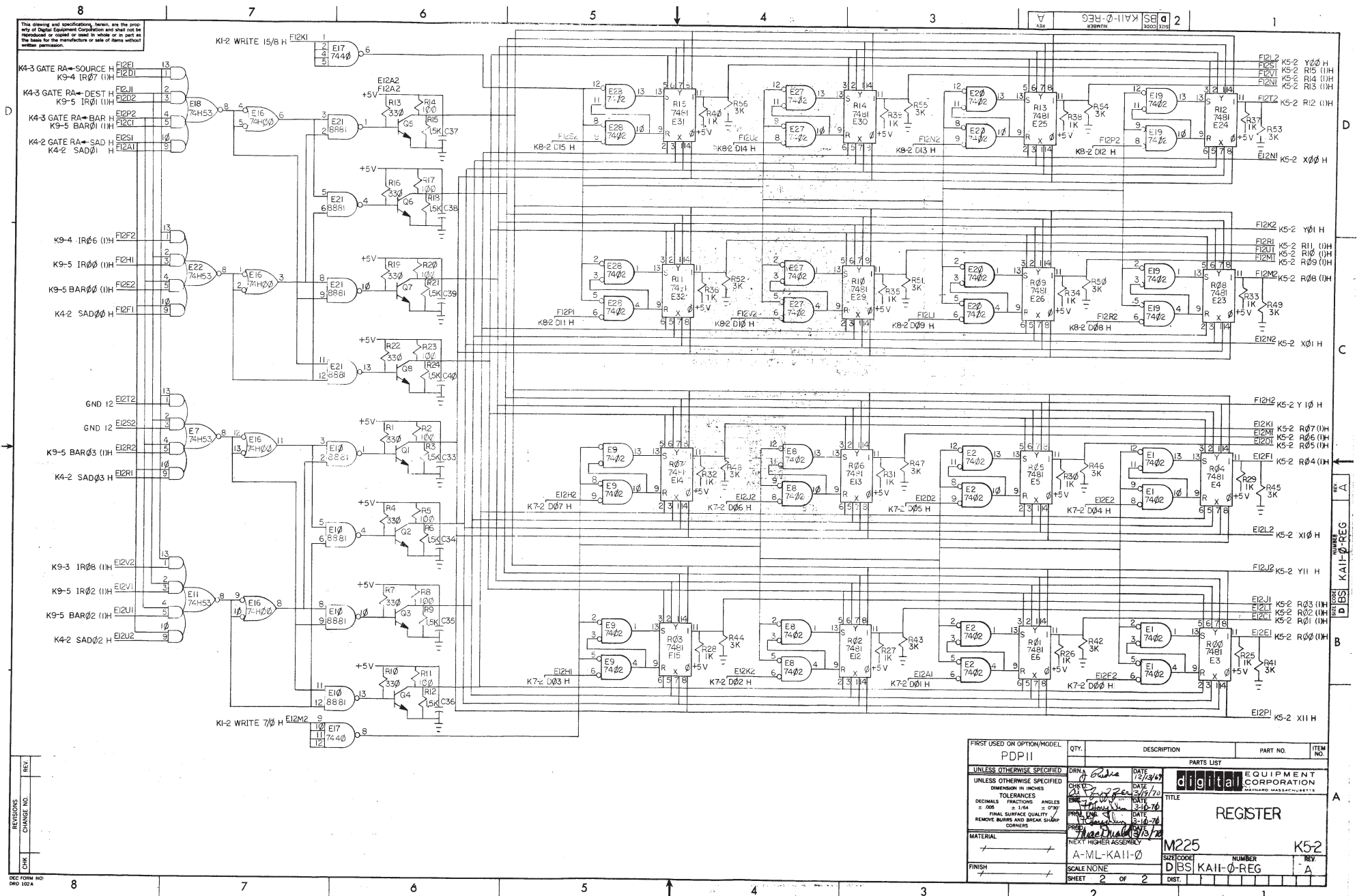
- UNLESS OTHERWISE NOTED RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT

PIN NOMENCLATURE



FIRST USED USED OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
UNLESS OTHERWISE SPECIFIED	DATE	PARTS LIST		
UNLESS OTHERWISE SPECIFIED	DATE	digital CORPORATION		
UNLESS OTHERWISE SPECIFIED	DATE	EQUIPMENT		
UNLESS OTHERWISE SPECIFIED	DATE	CORPORATION		
UNLESS OTHERWISE SPECIFIED	DATE	WATFORD MASSACHUSETTS		
UNLESS OTHERWISE SPECIFIED	DATE	REGISTER		
UNLESS OTHERWISE SPECIFIED	DATE	M225 K5-1		
UNLESS OTHERWISE SPECIFIED	DATE	A-ML-KAI1-0		
UNLESS OTHERWISE SPECIFIED	DATE	DBS KAI1-0-REG		
UNLESS OTHERWISE SPECIFIED	DATE	A		



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPII					
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		DIGITAL EQUIPMENT CORPORATION			
DIMENSION IN INCHES		TITLE			
TOLERANCES		REGISTER			
DECIMALS FRACTIONS ANGLES		M225			
= .005 = 1/64 = 0°30'		K5-2			
FINAL SURFACE QUALITY		A-ML-KAI-0			
REMOVE BURRS AND BREAK SHARP CORNERS		DBS KAI-0-REG			
MATERIAL		SCALE NONE			
NEXT HIGHER ASSEMBLY		SHEET 2 OF 2			
FINISH		DIST. 1 2 3 4 5 6 7 8 9 10 11 12			