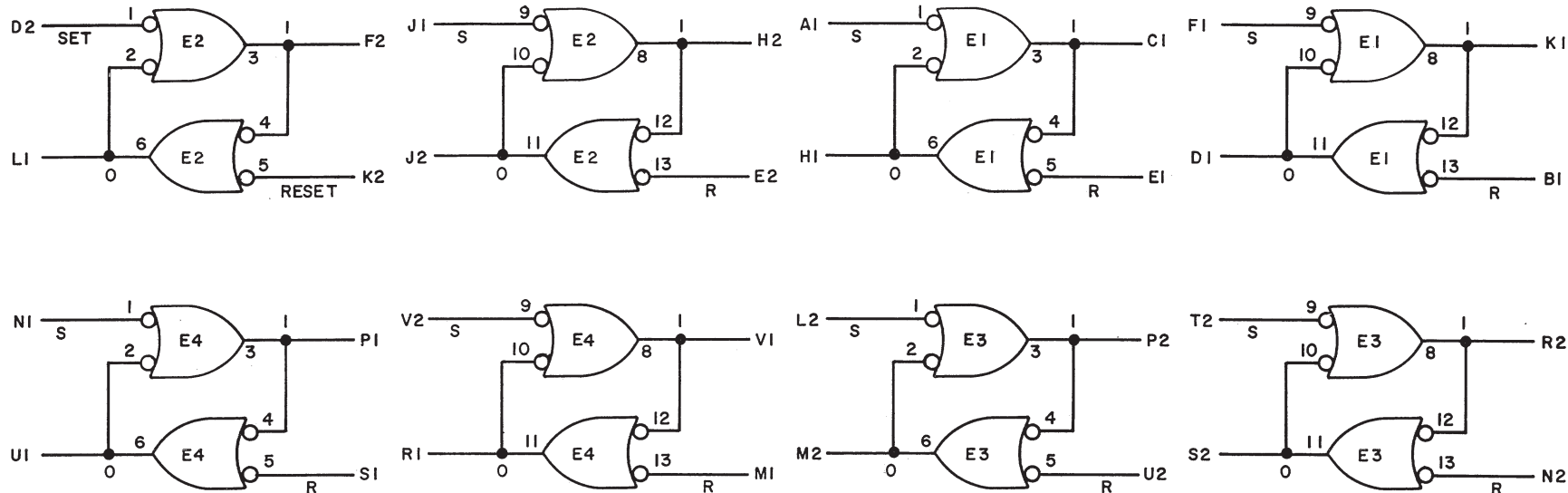
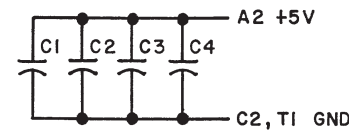


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NOTES
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V



C1 THRU C4	CAP. .01MFD 50V-0+20% DISC	1000001
E1 THRU E4	INTEGRATED CKT. DEC7400N	1905575
REFERENCE DESIGNATION	PARTS LIST	A-PL-M203-0-0
	DESCRIPTION	PART NO.

REVISIONS CHK NO. REV. 12 6808 A MM 00001 B		DRN. M. Haller CHK'D. 7/11/67 ENG. M. S. G. 7/11/67 PROD. DATE	DATE 7-11-67 DATE 7/11/67 DATE	TRANSISTOR & DIODE CONVERSION CHART <table border="1"> <tr> <th>DEC</th> <th>EIA</th> <th>DEC</th> <th>EIA</th> </tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td><td> </td></tr> </table>	DEC	EIA	DEC	EIA																	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE SET-RESET FLIP FLOP MODULE M203 SIZE B CODE CS NUMBER M203-0-1 REV. B PRINTED CIRCUIT REV. C
DEC	EIA	DEC	EIA																							