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FIELD MAINTENANCE PRINT SET

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TABLE OF CONTENTS

B-TC-6300B-0-DBU	6300B MICRO VAX II WORLD BOX (TC)
D-UA-6300B-0-DBU	6300B MICRO VAX II WORLD BOX
K-PL-6300B-0-DBP	6300B MICRO VAX II WORLD BOX (PL)
B-DD-6300B-0-DBU	6300B MICRO VAX II WORLD BOX (DD ONLY)
B-DD-M7606-0-0	MICRO VAX II CPU (DD ONLY)
D-UA-M7606-0-0	MICRO VAX II CPU
K-PL-M7606-0-DBP	MICRO VAX II CPU (PL)
D-CS-M7606-0-1	MICRO VAX II CPU
B-DD-5416744-0-0	FUNCTION SEL/SLU CONSOLE CONN (DD ONLY)
D-UA-5416744-0-0	FUNCTION SEL/SLU CONSOLE CONN
K-PL-5416744-0-DBP	FUNCTION SEL/SLU CONSOLE CONN (PL)
D-CS-5416744-0-1	FUNCTION SEL/SLU CONSOLE CONN
B-DD-M9047-0-0	Q BUS GRANT CONTINUITY CARD (DD ONLY)
D-UA-M9047-0-0	Q BUS GRANT CONTINUITY CARD
D-CS-M9047-0-1	Q BUS GRANT CONTINUITY CARD
A-PS-1700712-0-0	CABLE ASSY, 20 PIN
A-PS-1700624-0-0	CABLE CONSOLE BACKPLANE
K-PL-7022382-0-DBP	ACCESSORIES KIT
A-PS-1700301-0-0	CABLE ASSY, 7 COND
MP-02049-01	PRINT SET BA123-A BASIC ENCLOSURE

UNIT VARIATIONS COVERED BY THIS PRINT SET

6300B-A2
6300B-A3
6300B-B2
6300B-B3
6300B-C2
6300B-C3
6300B-D2
6300B-D3

6300B FIELD MAINTENANCE PRINT SET

DIGITAL EQUIPMENT
CORPORATION

PRINT SET PART
NUMBER MP-02071-01
REV A1

FILE NAME: TC-6300B-0-DBUA

REVISION HISTORY	REV.	A
	ECO NUMBER	INITIAL
	DATE	
	DRN.	D. HEALY
	CHK'D	D. HEALY
	DES. ENG.	J. NICHOLS
RESP. ENG.	K. WORTMAN	
FIELD SERVICE		
TOP DOC.	B-DD-6300B-0-DBU	

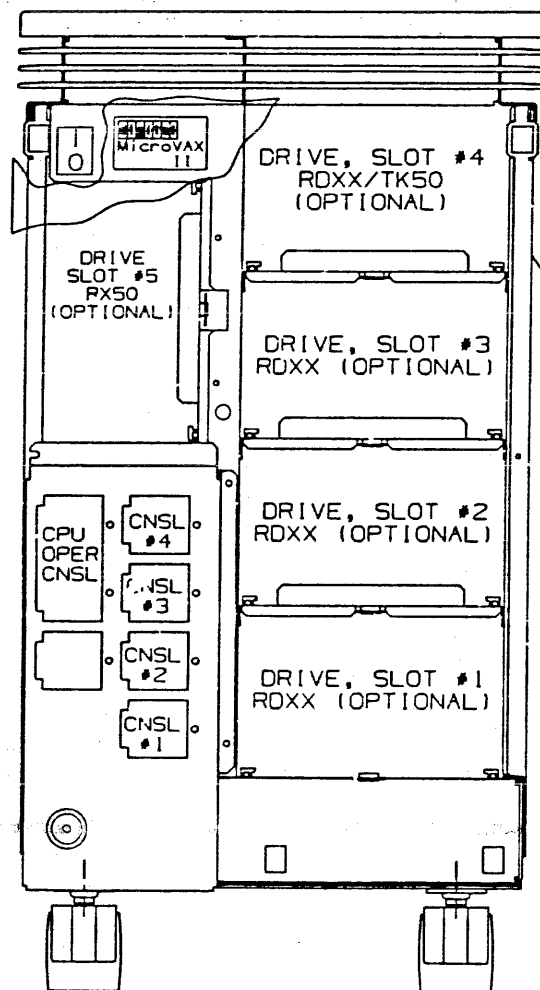
DATE	25FEB85	TITLE	6300B MICRO VAX II WORLD BOX
DATE	19MAR85	SIZE CODE	B TC
DATE	19MAR85	NUMBER	6300B-0-DBU
DATE	19MAR85	REV.	A
SHEET 1 OF 1		MLO1	

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LEGEND		
PART NO.	REV	VARIATION
6300B-A2	A2	1 MB FP 120V
6300B-A3	A2	1 MB FP 240V
6300B-B2	A2	1 MB NO FP 120V
6300B-B3	A2	1 MB NO FP 240V
6300B-C2	A2	256 KB FP 120V
6300B-C3	A2	256 KB FP 240V
6300B-D2	A2	256 KB NO FP 120V
6300B-D3	A2	256 KB NO FP 240V

NOTES:

- BACK PANEL CONFIGURATION RULES & MASS STORAGE CONFIGURATION RULES: REF D-AR-7022380-0-DBU
- ITEM #5 NOT SHOWN ON THIS ASSY.



VIEW A-A
(FRONT)
(SEE SHEET 2)
SHOWN WITHOUT FRONT
OR SIDE PANELS

LAYER #6: WORK LAYER (SHT 4)
LAYER #5: WORK LAYER (SHT 3)
LAYER #4: FORMAT LAYER (SHT 2,3 & 4)
LAYER #3: WORK LAYER (SHT 2)
LAYER #2: FORMAT LAYER (SHT 1)
LAYER #1: WORK LAYER (SHT 1)
PLOT SCALE: .50
SYSTEM #: 20
PROGRAM VERSION: UGRAF D4.1
FILE NAME: 6300B-0-DBUB
TITLE: 6300B MICRO VAX II WORLD BOX

PLOT AT .50

CAUTION: OFF SHEET PARTS LIST EXISTS
REFER TO K-PL-6300B-0-DBP
(ML 863)

DESCRIPTION		DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)				
DIMENSION RANGE IN INCHES	OVER 0.2 TO 12.0	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0	OVER 80.0 TO 160.0
	±.004	±.008	±.012	±.016
APPLICABLE DIMENSION RANGE	±.004	±.008	±.012	±.016
THIRD ANGLE PROJECTION	DATE	22 JAN 85	TITLE	
DO NOT SCALE DRAWING	DATE	19 MAR 85	6300B MICRO VAX II WORLD BOX	
REMOVE BURRS AND BREAK SHARP CORNERS	DATE	19 MAR 85	DOCUMENT NUMBER	
MATERIAL	DATE	19 MAR 85	D UA 6300B-0-DBU B	
FINISH	DATE	19 MAR 85	SCALE 1/2 SHEET 1 OF 4	

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FOR MODULE
UTILIZATION
SEE SHEET 4

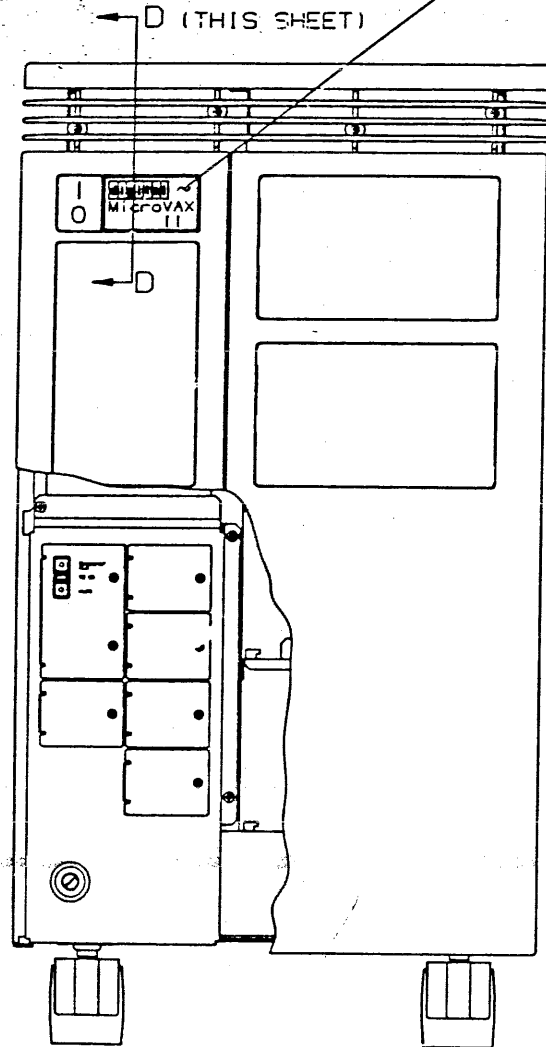
14
15 OR 16
OR 25

REVISIONS		
CHK	CHANGE NO	REV

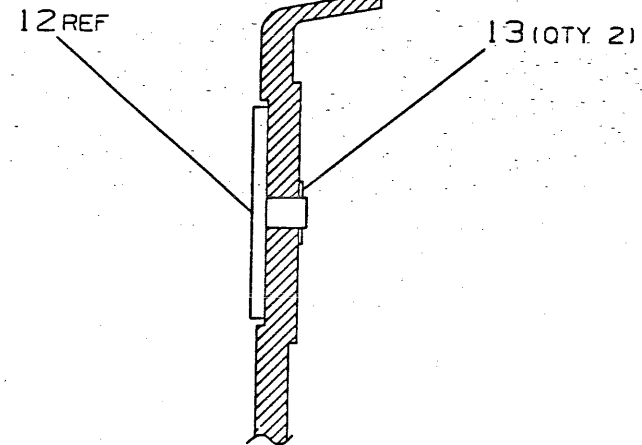
TITLE		6300B MICRO VAX	SIZE CODE	NUMBER	REV
		11 WORLD BOX	D UA	6300B-0-DBU	B
SCALE	1/2	SHEET	2	OF	4
		DIST			

PLOT AT .50

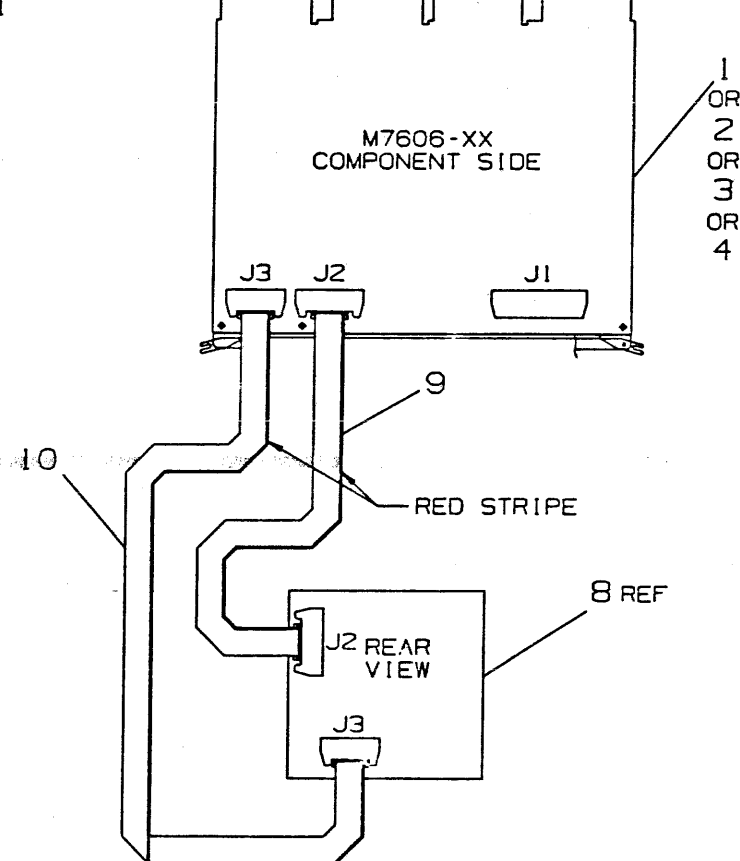
THE FEDERAL COPYRIGHT LAWS. 1988



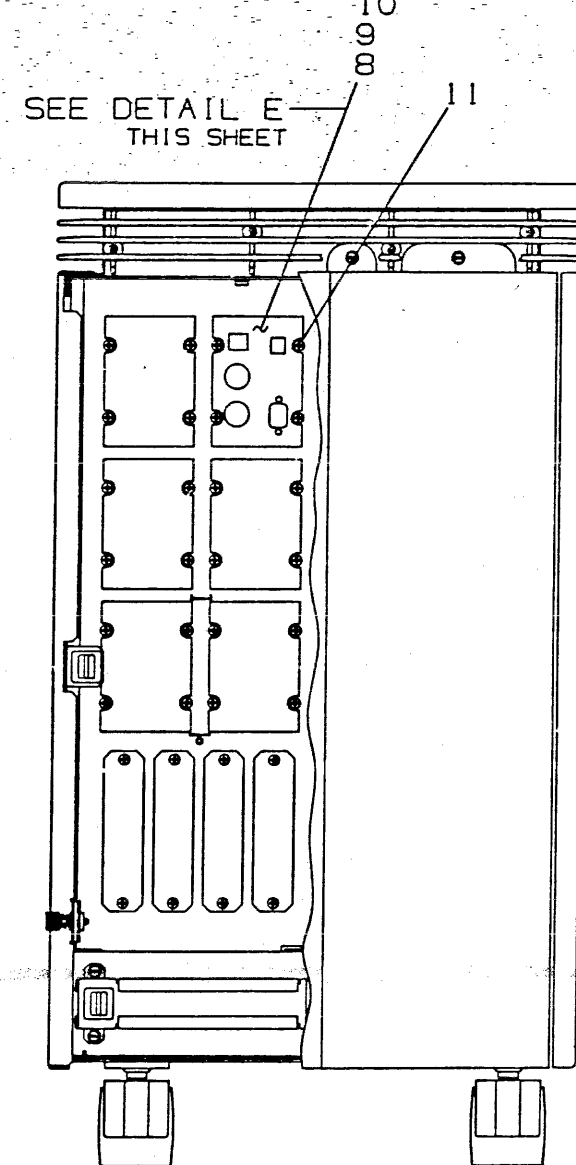
VIEW A-A
SEE SHT 2



SECTION D-D
SCALE 2/1



DETAIL "E"



VIEW B-B
SEE SHT 2

REVISIONS		
CHK	CHANGE NO	REV

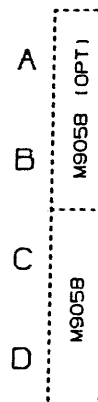
PLOT AT .5000

TITLE		6300B MICRO VAX 11 WORLD BOX		SIZE CODE DUA		NUMBER 6300B-0-0BU		REV B	
SCALE 1/2		SHEET 3 OF 4		DIST					

MLOI

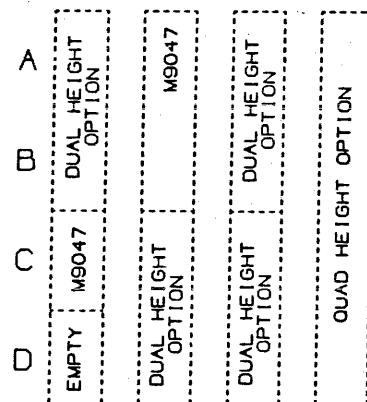
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THE MODULE UTILIZATION FOR 13TH SLOT SEE NOTE 9



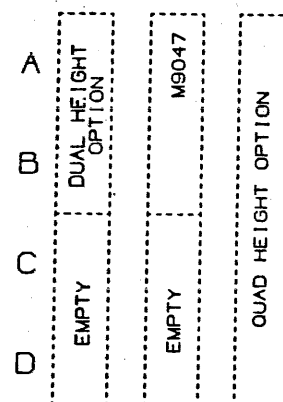
13

POSSIBLE MODULE UTILIZATIONS FOR SLOTS 5-12 ARE AS FOLLOWS (SEE NOTES 7 AND 8 THIS SHT):



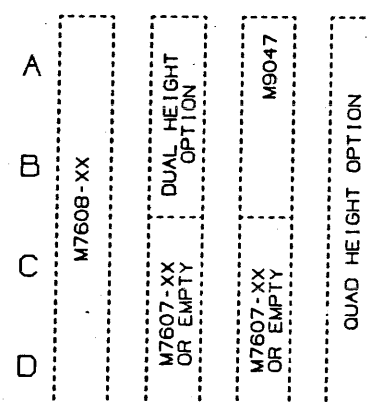
5-12

POSSIBLE MODULE UTILIZATIONS FOR SLOT 4 ARE AS FOLLOWS



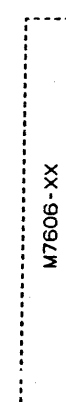
4

POSSIBLE MODULE UTILIZATIONS FOR SLOTS 2 AND 3 ARE AS FOLLOWS (SEE NOTES 5, 6, 7 AND 8 THIS SHT):



2-3

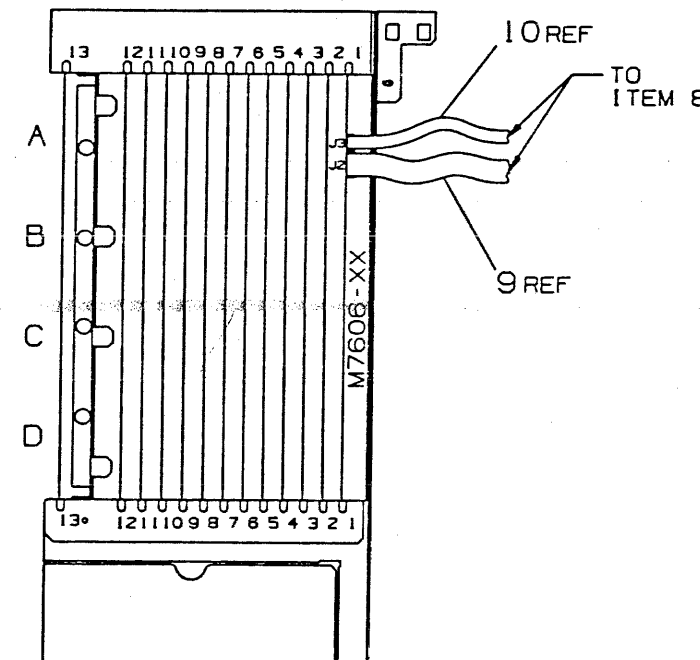
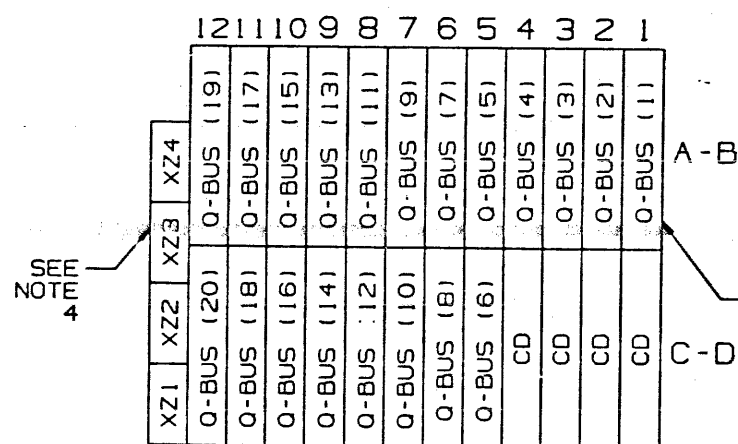
THE MODULE UTILIZATION FOR THE FIRST SLOT IS AS FOLLOWS:



1

NOTES:

- BACKPLANE VIEWS ARE FROM THE MODULE SIDE
- THE AB ROWS OF SLOTS 1-12 AND THE CD ROWS OF SLOTS 5-12 ARE EACH LABELED "Q-BUS" BECAUSE THEY ARE INTERCONNECTED PER THE Q-BUS SPECIFICATION. THE NUMBER IN PARENTHESIS SHOWS THE PATH OF INTERRUPT AND DMA GRANT CONTINUITY; INCREASING VALUE DENOTES DECREASING PRIORITY.
- THE CD ROWS OF SLOTS 1-4 ARE LABELED "CD" BECAUSE THEY ARE CONNECTED PER THE "CD INTERCONNECT" SPECIFICATION. THIS INTERCONNECT CONNECTS SELECTED SIDE TWO PINS OF A GIVEN SLOT TO SIDE ONE PINS OF THE NEXT SLOT.
- THE BACKPLANE INCLUDES FOUR RESISTOR PACKS IN LOCATIONS XZ1, XZ2, XZ3 AND XZ4. THESE RESISTOR PACKS PROVIDE AN ADDITIONAL 120 OHMS OF TERMINATION TO THE Q-BUS LINES.
- THE SYSTEM MAY CONTAIN UP TO TWO MS630 MEMORY MODULES, LOCATED IN SLOTS 2 AND 3 (A SINGLE MS630 MODULE MUST BE LOCATED IN SLOT 2). A QUAD HEIGHT MS630 (M7608-A(X) OR M7608-B(X)) FILLS THE ENTIRE SLOT. A DUAL HEIGHT MS630 (M7607-A(X)) OCCUPIES THE CD ROWS ONLY.
- IF SLOTS 2 AND 3 (OR SLOT 3) ARE NOT USED FOR MS630 MEMORY MODULE(S), AND ARE NOT REQUIRED FOR Q-BUS OPTIONS, THEN RESERVE THEM (IT) FOR FUTURE MEMORY MODULE EXPANSION WITH M9047 GRANT CONTINUITY CARD(S) IN ROW AB.
- QUAD HEIGHT Q-BUS OPTIONS MAY BE ADDED TO ANY OPEN SLOTS, AND DUAL HEIGHT Q-BUS OPTIONS MAY BE ADDED TO ANY OPEN "Q-BUS" HALF SLOTS. ALL UNUSED "Q-BUS" HALF SLOTS WHICH PRECEED THE LAST OPTION MUST CONTAIN GRANT CONTINUITY MODULES (M9047) TO PASS GRANTS TO MODULES OF A LOWER PRIORITY. UNUSED "CD" HALF SLOTS ARE LEFT VACANT. (NOTES 2 AND 3 IDENTIFY THE "Q-BUS" AND "CD" HALF SLOTS).
- FOR MORE INFORMATION ON BACKPLANE SLOT ASSIGNMENTS, REFER TO THE SYSTEM TECHNICAL MANUAL.
- REF D-AR-7022380-0-DBU FOR MODULE UTILIZATION REQUIREMENTS.



MODULE UTILIZATION

PLOT AT .50

REVISIONS		
CHK	CHANGE NO	REV

TITLE 6300B MICRO VAX II WORLD BOX		SIZE/COOP D/UA	NUMBER 6300B-0-DBU	REV B
SCALE 1/2	SHEET 4	OF 4	DIST	

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION							
						A2	A3	B2	B3	C2	C3	D2	D3
						VARIATION REVISION LEVEL:						A2	A2
1	1	B-DD-M7606-0-0	M7606-AA		MICROVAX II W/1MB,FP,INCLUDES TI	1	1	-	-	-	-	-	-
2	2	B-DD-M7606-0-0	M7606-BA		M7606-AA W/NO FP (CPU,1MB) = M76	-	-	1	1	-	-	-	-
3	3	B-DD-M7606-0-0	M7606-CA		M7606-AA W/256KB MEM (CPU,256KB,	-	-	-	-	1	1	-	-
4	4	B-DD-M7606-0-0	M7606-DA		M7606-AA W/256KB MEM,NO FP = M76	-	-	-	-	-	-	1	1
5	5	K-PL-7022382-0-DBP	7022382-02		BA123-A ACCESSORY KIT	1	1	1	1	1	1	1	1
6	6	B-DD-7022380-0-DBU	7022380-01		BA123-A BASIC ENCLOSURE (120V)	1	-	1	-	1	-	1	-
7	7	B-DD-7022380-0-DBU	7022380-02		BA123-A BASIC ENCLOSURE (240V)	-	1	-	1	-	1	-	1
8	8	B-DD-5416744-0-0	5416744-01		FUNCTION SEL/SLU CONSOLE CONNECT	1	1	1	1	1	1	1	1
9	9	A-PS-1700712-0-0	1700712-02	A	WIRE HARN ASSY 20COND 30AWG 20SK	1	1	1	1	1	1	1	1
10	10	A-PS-1700624-0-0	1700624-01	A	CABLE ASSY,10COND,FLAT,10(2X05)I	1	1	1	1	1	1	1	1
11	11	A-PS-9010174-0-0	9010174-00	C	SCREW,SEMS PAN PHIL 6-	4	4	4	4	4	4	4	4
12	12	D-MD-7431480-0-0	7431480-03	A	MEDALLIAN SYSTEM LOGO (630QB)	1	1	1	1	1	1	1	1
13	13	A-PS-9009257-0-0	9009257-02	D	RING,RETAINER EXTERNAL FOR .188	2	2	2	2	2	2	2	2
14	14	A-PS-3617880-0-0	3617880-02		LABEL,FCC,CLASS A,PROCESSOR	1	1	1	1	1	1	1	1
15	15	A-PS-3624471-0-0	3624471-01	A	LABEL,BUSINESS COMP BA123-A2	1	-	1	-	1	-	1	-
16	16	A-PS-3624471-0-0	3624471-02	A	LABEL,BUSINESS COMP BA123-A3	-	1	-	1	-	1	-	1
17	17	A-PS-1700198-0-0	1700198-00	B	PWR CORD,TERM 3-18 250	-	REF	-	REF	-	REF	-	REF
18	18	A-PS-1700199-0-0	1700199-00	A	PWR CORD,TERM 3 250	-	REF	-	REF	-	REF	-	REF
19	19	A-PS-1700209-0-0	1700209-00	A	PWR CORD,TERM 3- .75MM 5A 250	-	REF	-	REF	-	REF	-	REF
20	20	A-PS-1700210-0-0	1700210-01	A	PWR CORD,TERM 3- .75MM 250V 6	-	REF	-	REF	-	REF	-	REF
21	21	A-PS-1700310-0-0	1700310-01	A	PWR CORD,TERM 3- .75MM 250	-	REF	-	REF	-	REF	-	REF
22	22	A-PS-1700364-0-0	1700364-01	A	PWR CORD,TERM 3- .75MM 250V	-	REF	-	REF	-	REF	-	REF
23	23	D-AR-7022380-0-DBU			BA123-A SYSTEM ARRANGEMENT	REF	REF	REF	REF	REF	REF	REF	REF
24	24	B-DD-M9047-0-0	M9047-00		QBUS GRANT CONTINUITY,1ST USED I	A/R	A/R	A/R	A/R	A/R	A/R	A/R	A/R
25	25	A-PS-3617674-0-0	3617674-22		LABEL,SERIAL/POWER,W/UL & CSA MA	A/R	A/R	A/R	A/R	A/R	A/R	A/R	A/R

REVISION HISTORY			BASIC PART NO: 630QB		DRN: D. RICHARD		DATE: 19-FEB-85		D I G I T A L				
ENG	ECO NUMBER	REV	SECTION A OF A										
JUN	630QB-ML001	B	SECTION VARIATION INDEX		CHK'D: D. HEALY		DATE: 19-MAR-85		TITLE PARTS LIST				
			{A}A2,A3,B2,B3,C2,C3, D2,D3						630QB MICRO VAX II WORLD BOX				
			{B}		DES.ENG: J. NICHOLS		DATE: 19-MAR-85		DOCUMENT NUMBER				
			{C}						SIZE CODE NUMBER REV				
			{D}		RESP.ENG.: J. NICHOLS		DATE: 19-MAR-85		K PL 630QB-0-DBP B				
			{E}		MFG.ENG.: K. WORTMAN		DATE: 19-MAR-85		RELEASE DATE: 07-MAY-85				
			{F}		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #				
					D-UA-630QB-0-DBU		B-DD-630QB-0-DBU		ML863B.PLS 14				

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LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION							
					A2	A3	B2	B3	C2	C3	D2	D3
VARIATION REVISION LEVEL:					A2	A2	A2	A2	A2	A2	A2	A2

26 NOTE: DEPENDING ON RAM VENDOR, MODULE MAY BE STAMPED AD/AH,BC/BH, CC/CH, DC/DH

D	I	G	I	T	A	L	TITLE	630QB MICRO VAX II WORLD BOX	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	630QB-0-DBP	B

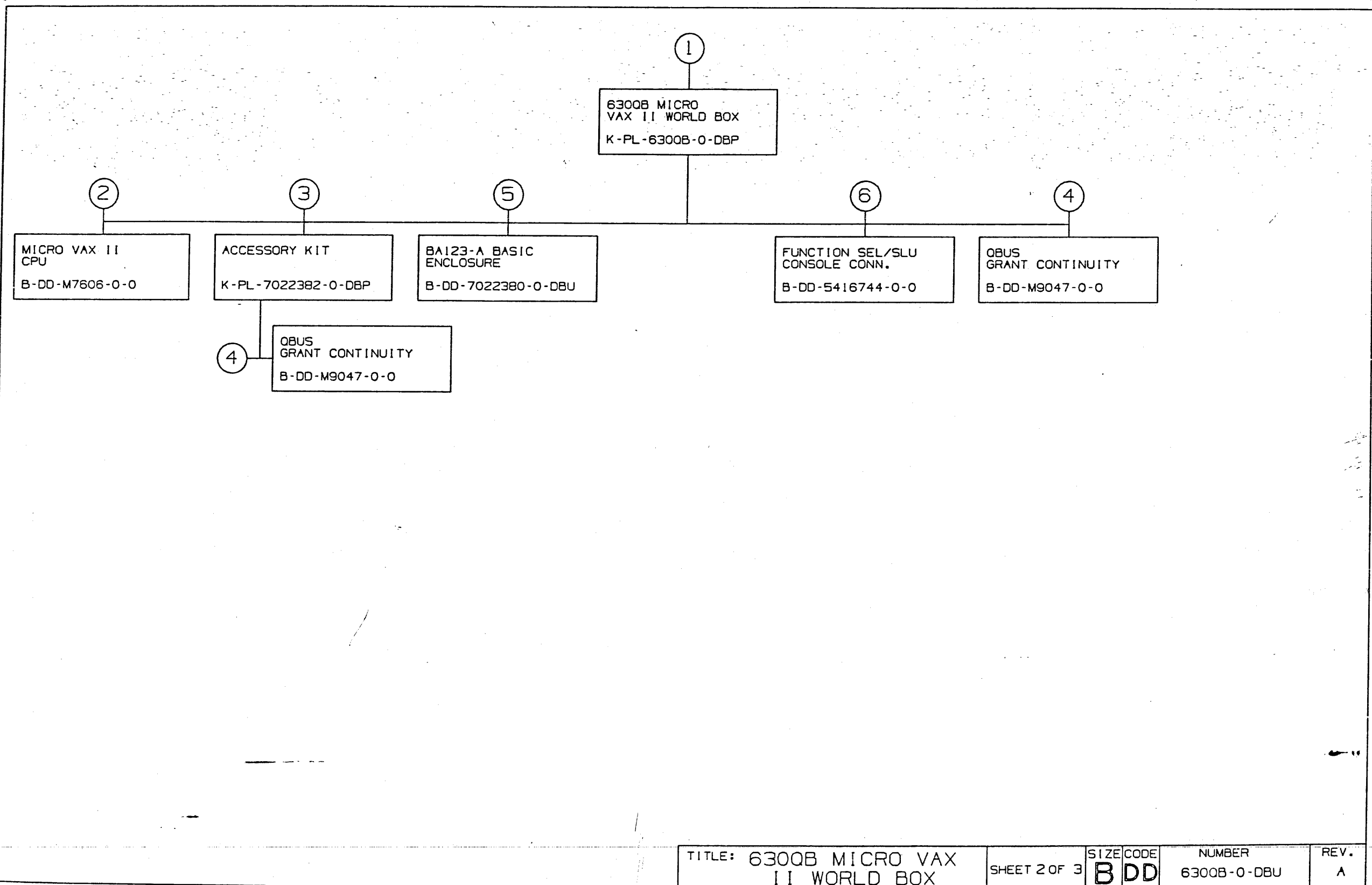
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[illegible]

FILE NAME: DD-630QB-0-DBUA

REVISIONS		CHANGE NO.	REV.	6300B MICRO VAX II WORLD BOX SIZE CODE NUMBER REV
CHK	INITIAL	A		
USED ON OPTION/MODEL		DRN. D. HEALY	DATE 25FEB85	
		CHK'D D. HEALY	DATE 19MAR85	
		PROJ. ENG. J. NICHOLS	DATE 19MAR85	
		PROD. K. WORTMAN	DATE 19MAR85	
SHEET 1 OF 3		TOP DOC. B-DD-6300B-0-DBU		DIST. B DD 6300B-0-DBU A

MLO $\bar{1}$



FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
1	MP-02071-01	6300B MICRO VAX II WORLD BOX (MP)	-				
	B-TC-6300B-0-DBU	6300B MICRO VAX II WORLD BOX (TC)	-				
	D-UA-6300B-0-DBU	6300B MICRO VAX II WORLD BOX	E/M				
	K-PL-6300B-0-DBP	6300B MICRO VAX II WORLD BOX (PL)	E/M				
	A-PS-1700712-0-0	CABLE ASSY, 20 PIN	E/M				
	A-PS-1700624-0-0	CABLE CONSOLE BACKPLANE	E/M				
	D-MD-7431480-0-DBU	MEDALLIAN SYSTEM LOGO (6300B)	E/M				
	A-PS-3617880-0-0	LABEL, FCC CLASS A PROCESSOR	M				
	A-PS-3624471-0-0	LABEL, BUSINESS COMPUTER	M				
2	B-DD-M7606-0-0	MICRO VAX II CPU	E/M				
3	K-PL-7022382-0-DBP	ACCESSORY KIT	E/M				
	A-PS-1700301-0-0	CABLE ASSY, 7 COND	E/M				
	A-PS-3622092-0-0	LABEL, LCPS CONFIGURATION	M				
	A-PS-3624251-0-0	LABEL, MULTI LINGUAL F. PANEL	M				
4	B-DD-M9047-0-0	0 BUS GRANT CONTINUITY CARD	E/M				
5	B-DD-7022380-0-DBU	BA123-A BASIC ENCLOSURE	E/M				
6	B-DD-5416744-0-0	FUNCTION SEL/SLU CONSOLE CONN.	E/M				

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL

digital

TITLE: 6300B MICRO VAX
II WORLD BOX

SHEET 3 OF 3

SIZE CODE
BDD

NUMBER
6300B-0-DBU

REV.
A

										H REV		0-0-9094W M7606-0-0 NUM.BER		DD CODE		B SIZE						
DRAWING NO.		NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																	
			M7606	KA630	B1	C1	D1	E1	F1	H2	H2											
D-UA-M7606-0-0		1		KA630 UNIT ASSEMBLY	A	A	B	B	B	B	B											
K-PL-M7606-0-DBP		2		KA630 PARTS LIST	A	B	C	D	E	F	H											
K-PC-M7606-0-DBJ		1		P.C. DESIGN DATA BASE	E	E	E	E	E	E	E											
			5016523-01	ETCHED CIRCUIT BOARD	E1	E1	E1	E1	E1	E1	E1											
B-DD-5016523-0-0		1		DRAWING DIRECTORY	A	A	A	A	A	A	A											
B-CS-M7606-0-1		1		M7606 DRAWING DIRECTORY	-	B	B	B	B	B	B											
B-CS-M7606-0-2		1		MICROVAX II SYSTEM	-	B	B	B	B	B	B											
B-CS-M7606-0-3		1		KA630-UVAX ON Q22 BUS	-	B	B	B	B	B	B											
B-CS-M7606-0-4		1		UVAX & FPU	-	B	B	B	B	B	B											
D-CS-M7606-0-5		1		UVAX & FPU PINOUTS	-	-	-	-		A	A											
B-CS-M7606-0-6		1		ADDRESS LATCH/LOCAL MEMORY DECODE	-	B	B	B	B	B	B											
B-CS-M7606-0-7		1		MEMORY SUBSYSTEM	-	B	B	B	B	B	B											
B-CS-M7606-0-8		1		Q22 BUS INTERFACE GATE ARRAY	-	B	B	B	B	B	B											
B-CS-M7606-0-9		1		Q22 BUS INTERFACE GATE ARRAY	-	B	B	B	B	B	B											
B-CS-M7606-0-10		1		DC380 PAD ASSIGNMENT TOP VIEW LL5320 IN 144 PIN GRID ARRAY	-	B	B	B	B	B	B											
B-CS-M7606-0-11		1		REFRESH LOGIC/COUNTER	-	B	B	B	B	B	B											
B-CS-M7606-0-12		1		DIVIDE BY 12	-	B	B	B	B	B	B											
B-CS-M7606-0-13		1		SYNCHRONOUS 3 BIT COUNTER	-	B	B	B	B	B	B											
NOTES: NOTE: MODULE PART REV H' WAS USED FOR VAXSTATION FIELD UPGRADE KIT ONLY. M7606 ECO-00006 REWORKED VARIATION M7606 AH TO EITHER M7606-ZP (NEW NEC) M7606-ZP (MITSUBIRSHI) M7606 OR M7606-ZF (HITACHI). THERE WAS NO PART REV CHANGE.					REVISION HISTORY	REV.	A	B	C	D	E	F	H									
						ECO NO.	INIT	MLO01	MLO02	MLO03	MLO04	MLO05	MLO06									
						DATE	5/84	5/85	6/85	7/85	8/85	1/86	3/86									
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								CHK'D E.LANDRY		DATE 5/17/84												
								DES. ENG. B.MASKAS		DATE 5/17/84		DOCUMENT NUMBER										
								RESP. ENG. B.MASKAS		DATE 5/17/84												
								MFG. ENG. B.SCHULTE		DATE 9/24/84										SIZE B		CODE DD
												SHEET 1 OF 4										

										H REV		0-0-909/M NUMBER		DD CODE		B SIZE					
DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																	
B-CS-M7606-0-14	1		VECTOR HACK	-	B	B	B	B	B	B											
B-CS-M7606-0-15	1		INVERTING MUX LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-16	1		4 to 1 MUX	-	B	B	B	B	B	B											
B-CS-M7606-0-17	1		Q-BUS SUPPORT LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-18	1		BLK MD CTR LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-19	1		TOGGLE FLOP	-	B	B	B	B	B	B											
B-CS-M7606-0-20	1		Q-BUS SUPPORT LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-21	1		Q-BUS SUPPORT LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-22	1		Q-BUS SUPPORT LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-23	1		Q-BUS SUPPORT LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-24	1		POWER BUFFER MACRO	-	B	B	B	B	B	B											
B-CS-M7606-0-25	1		BIDIRECT BUFFER	-	B	B	B	B	B	B											
B-CS-M7606-0-26	1		MUX LOGIC	-	B	B	B	B	B	B											
B-CS-M7606-0-27	1		TRANSLATION MAP GROUP	-	B	B	B	B	B	B											
B-CS-M7606-0-28	1		KA630 Q-BUS INTERFACE	-	B	B	B	B	B	B											
B-CS-M7606-0-29	1		UVAX INTERFACE GATE ARRAY	-	B	B	B	B	B	B											
B-CS-M7606-0-30	1		DC379 PAD ASSIGNMENT TOP VIEW LL5320 IN 144 PIN GRIP ARRAY	-	B	B	B	B	B	B											
B-CS-M7606-0-31	1		UVAX INTERFACE GATE ARRAY DATA PATH		B	B	B	B	B	B											
B-CS-M7606-0-32	1		UVDAL I/O BUFFERS, ADDR LATCHES	-	B	B	B	B	B	B											
NOTES:				REVISION HISTORY		REV.	A	B	C	D	E	F	G								
						ECO NO.	INIT	ML001	ML002	ML003	ML004	ML005	ML006								
						DATE	5/84	5/85	6/85	7/85	8/85	1/86	3/86								
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						CHK'D		E.LANDRY		DATE		5/17/84									
						DES. ENG.		B.MASKAS		DATE		5/17/84									
						RESP. ENG.		B.MASKAS		DATE		5/17/84									
						MFG. ENG.		B.SCHULTE		DATE		9/24/84									
TITLE										KA630											
										DOCUMENT NUMBER											
										SIZE	CODE	NUMBER				REV.					
										B	DD	M7606-0-0				H					
										SHEET 2 OF 4											

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																				
B-CS-M7606-0-33	1		ADDRESS DECODER	-	B	B	B	B	B	B														
B-CS-M7606-0-34	1		EXCEPTIONS AND INTERRUPTS	-	B	B	B	B	B	B														
B-CS-M7606-0-35	1		UVAX INPUTS AND I/O PINS	-	B	B	B	B	B	B														
B-CS-M7606-0-36	1		BOOT/DIAG REG., MEM ERR ADDR REG.	-	B	B	B	B	B	B														
B-CS-M7606-0-37	1		EPR BUS, X DAL BUS	-	B	B	B	B	B	B														
B-CS-M7606-0-38	1		INTERNAL DATA BUSES	-	B	B	B	B	B	B														
B-CS-M7606-0-39	1		MISC. CONTROL STROBES	-	B	B	B	B	B	B														
B-CS-M7606-0-40	1		RESET COUNTER, POWER UP/DOWN CNTRL	-	B	B	B	B	B	B														
B-CS-M7606-0-41	1		MEMORY SYSTEM ERROR REGISTER	-	B	B	B	B	B	B														
B-CS-M7606-0-42	1		TIME OF YEAR (TOY) CLOCK	-	B	B	B	B	B	B														
B-CS-M7606-0-43	1		CONSOLE SERIAL LINE INTERFACE	-	B	B	B	B	B	B														
B-CS-M7606-0-44	1		LEDs AND CONFIGURATION CONNECTOR	-	B	B	B	B	B	B														
B-CS-M7606-0-45	1		DECOUPLING CAPACITORS	-	B	B	B	B	B	B														
B-CS-M7606-0-46	1		KA630 STATE MACHINES	-	B	B	B	B	B	B														
B-CS-M7606-0-47	1		UVAX CYCLE CONTROLLER	-	B	B	B	B	B	B														
B-CS-M7606-0-48	1		MEMORY SEQUENCER	-	B	B	B	B	B	B														
B-CS-M7606-0-49	1		MEMORY SEQUENCER SUPPORT	-	B	B	B	B	B	B														
B-CS-M7606-0-50	1		Q22 BUS STATE MACHINES	-	B	B	B	B	B	B														
D-CS-M7606-0-51	1		KA630 MEMORY ARBITER LISTING	-	-	-	-	-	A	A														
NOTES:				REVISION HISTORY		A	B	C	D	E	F	H												
				ECO NO.	INIT	MLO01	MLO02	MLO03	MLO04	MLO05	MLO06													
				DATE	5/84	5/85	6/85	7/85	8/85	1/86	3/86													
				THIS DRAWING AND THE SPECIFICATIONS CONTAINED HEREIN ARE CONFIDENTIAL AND PROPRIETARY. THEY ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. THIS IS AN UNPUBLISHED WORK PROTECTED UNDER THE FEDERAL COPYRIGHT LAWS. 1984																DRN. D.DROZD		DATE 5/17/84	TITLE KA630	
				CHK'D E.LANDRY		DATE 5/17/84																		
				DES. ENG. B.MASKAS		DATE 5/17/84	DOCUMENT NUMBER																	
				RESP. ENG. B.MASKAS		DATE 5/17/84																		
MFG. ENG. B.SCHULTE		DATE 9/24/84	SIZE B	CODE DD	NUMBER M7606-0-0	REV H																		
		SHEET 3		OF 4																				

H REV										0-0-9097M NUMBER										DD CODE		B SIZE	
DRAWING NO.		NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																		
B-CS-M7606-0-52		1		KA630 MEMORY SYSTEM ARBITER STATE FLOW DIAGRAMS	-	B	B	B	B	B	B												
B-CS-M7606-0-53		1		KA630 MEMORY SYSTEM ARBITER STATE FLOW DIAGRAMS	-	B	B	B	B	B	B												
D-CS-M7606-0-54		1		KA630 LOCAL I/O CONTROL MACHINE	-	-	-	-	-	A	A												
				UL TESTING	-	-	-	-	-														
B-CS-M7606-0-55		1		KA630 LOCAL I/O BUS CONTROL	-	B	B	B	B	B	B												
D-CS-M7606-0-56		1		KA630 Q-BUS ARBITRATION CONTROL MACHINE LISTING	-	-	-	-	-	A	A												
B-CS-M7606-0-57		1		Q-BUS ARBITRATION CONTROLLER DETAILED CONTROL FLOW DIAGRAM	-	B	B	B	B	B	B												
D-CS-M7606-0-58		1		Q22 BUS MASTER CONTROL MACHINE LISTING	-	-	-	-	-	A	A												
B-CS-M7606-0-59		1		Q22 BUS MASTER CONTROL MACHINE FLOW DIAGRAM	-	B	B	B	B	B	B												
D-CS-M7606-0-60		1		Q22 BUS SLAVE CONTROL MACHINE LISTING	-	-	-	-	-	A	A												
B-CS-M7606-0-61		1		Q22 BUS SLAVE CONTROL MACHINE FLOW DIAGRAM	-	B	B	B	B	B	B												
B-CS-M7606-0-62		1		Q22 BUS SLAVE CONTROL MACHINE FLOW DIAGRAM	-	B	B	B	B	B	B												
D-CS-M7606-0-63		1		IKKL RAS DECODE FROM (ETS) LISTING	-	-	-	-	-	A	A												
D-CS-M7606-0-64		1		PALASM LISTING FOR PALISLSA DEVICES	-	-	-	-	-	A	A												
D-CS-M7606-0-65		1		MNEMONIC DICTORY	-	-	-	-	-	A	A												
K-DO-M7606-0-0		24		M7606 CROSS REF LIST	-	A	A	A	A	A	A												
NOTES:					REVISION HISTORY	REV.	A	B	C	D	E	F	H										
						ECO NO.	INIT	MLO01	MLO02	MLO03	MLO04	MLO05	MLO06										
						DATE	5/84	5/85	6/85	7/85	8/85	1/86	3/86										
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					CHK'D	E.LANDRY	DATE	5/17/84															
					DES. ENG.	B.MASKAS	DATE	5/17/84	DOCUMENT NUMBER M7606-0-0														
					RESP. ENG.	B.MASKAS	DATE	5/17/84															
					MFG. ENG.	B.SCHULTE	DATE	9/24/84											SIZE	B DD	NUMBER	M7606-0-0	REV
										SHEET 4 OF 4													

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REWORK INSTRUCTIONS

1. COMPONENT DELETES

2-1 DELETE E21 P/N 23035E6-00

2-2 DELETE E22 P/N 23034E6-C0

2. COMPONENT ADDS.

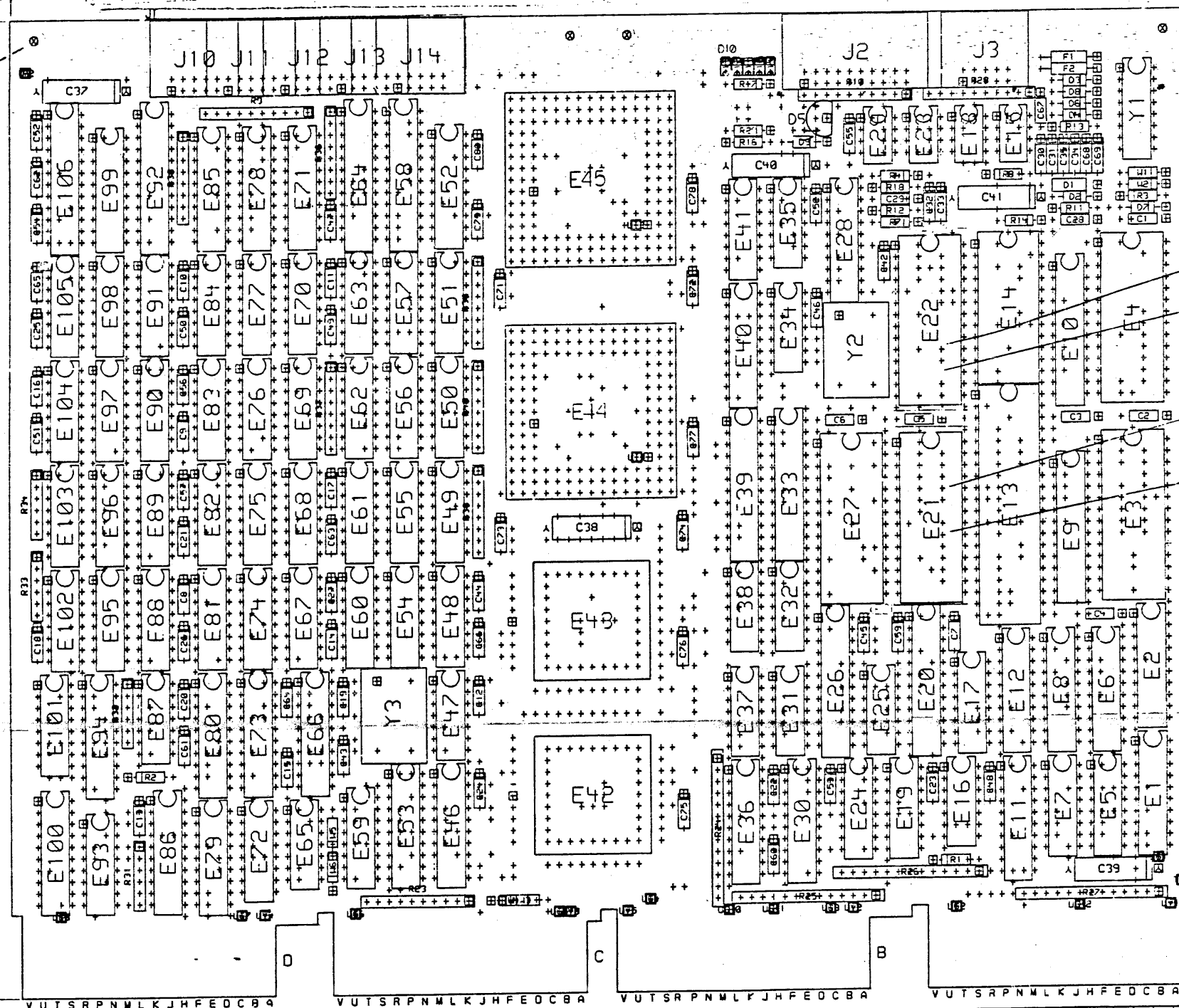
2-3 DELETE E21 P/N 23063E6-00

2-4 DELETE E22 P/N 23062E6-00

94-
(QTY. 4)

0-43 REF

COMPONENT SIDE VIEW



NOTES: 1. NOT INSTALLED

STEP E	↑ Y AXIS _____ STEP ____ TIMES
REPEAT	→ X AXIS _____ STEP ____ TIMES

CHK	CHANGE NO	REV
7-10	M7606-MLOO	B
6-10	8-10-10-10	
7-10	8-10-10-10	
8-10	8-10-10-10	
9-10	8-10-10-10	
10-10	8-10-10-10	

ETCH REV. 51

SIGNATURES	DATE	digital	
CRN.			
CHK'D.		TITLE	
MECH. ENG.			
PROJ. ENG.			
PROD. MTH.			
SCALE	SIZE CODE		NUMBER
SHT.	OF	0	UAM 7606
NEXT HIGHER ASSY.			

MS# 275

21-0-9097W50 D

D

C

B

2

→

1

1

1

17

1

1

17

1

B

→

1

1

10 MNEMONIC DICTIONARY

```

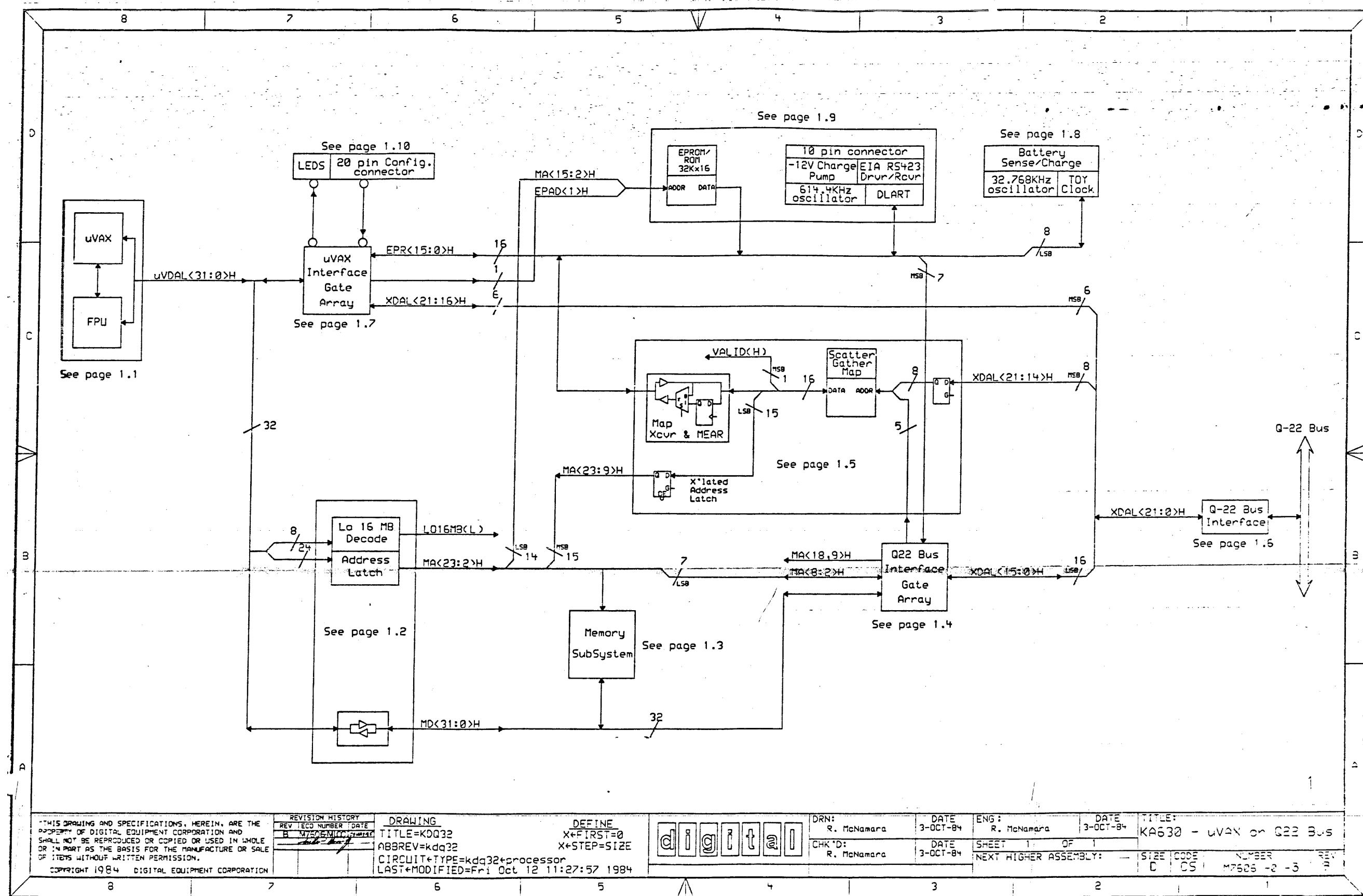
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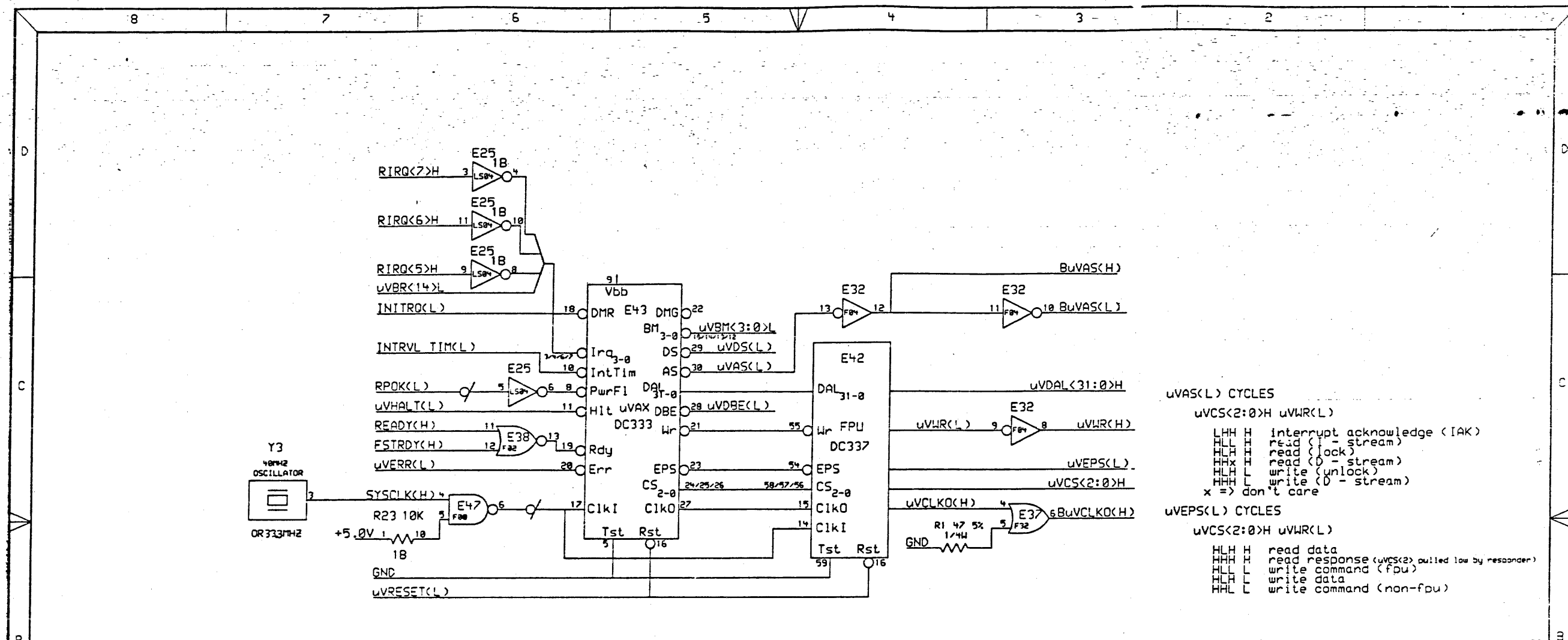
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digital

DRN.	DATE	ENG.	DATE
MASKAS & McNamara	8-DEC-87	MASKAS & McNamara	8-DEC-
CHK'D	DATE		
MASKAS & McNamara	8-DEC-87	5-EE	5-
TOP DOCUMENT NUMBER:			

TITLE:		M7506 DRAWING DIRECTORY	
SIZE CODE	DATE	BY	CHK.
5	05	M7506	





uVAS<L> CYCLES

uVCS<2:0>H uVWR<L>

```

LHH H interrupt acknowledge (IAK)
HLL H read (stream)
HLH H read (lock)
HLH H read (stream)
HLH H write (unlock)
HHH H write (stream)
x => don't care

```

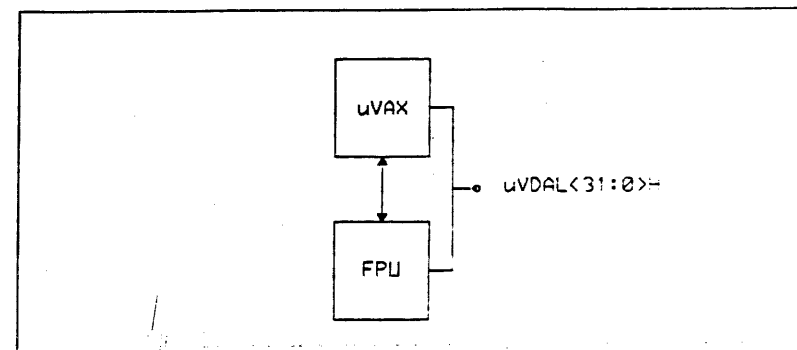
uVEPS<L> CYCLES

uVCS<2:0>H uVWR<L>

```

HLH H read data
HHH H read response (uVCS<2> pulled low by responder)
HLH H write command (fpu)
HLH H write data
HHH H write command (non-fpu)

```



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REV	ECO NUMBER	DATE
1		3-10-84

DRAWING
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ABBREV=UVAX+FPU
CIRCUIT+TYPE=UVAX/FPU
LAST*MODIFIED=Fri Nov 9 18:39:57 1984

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X*STEP=SIZE

digital

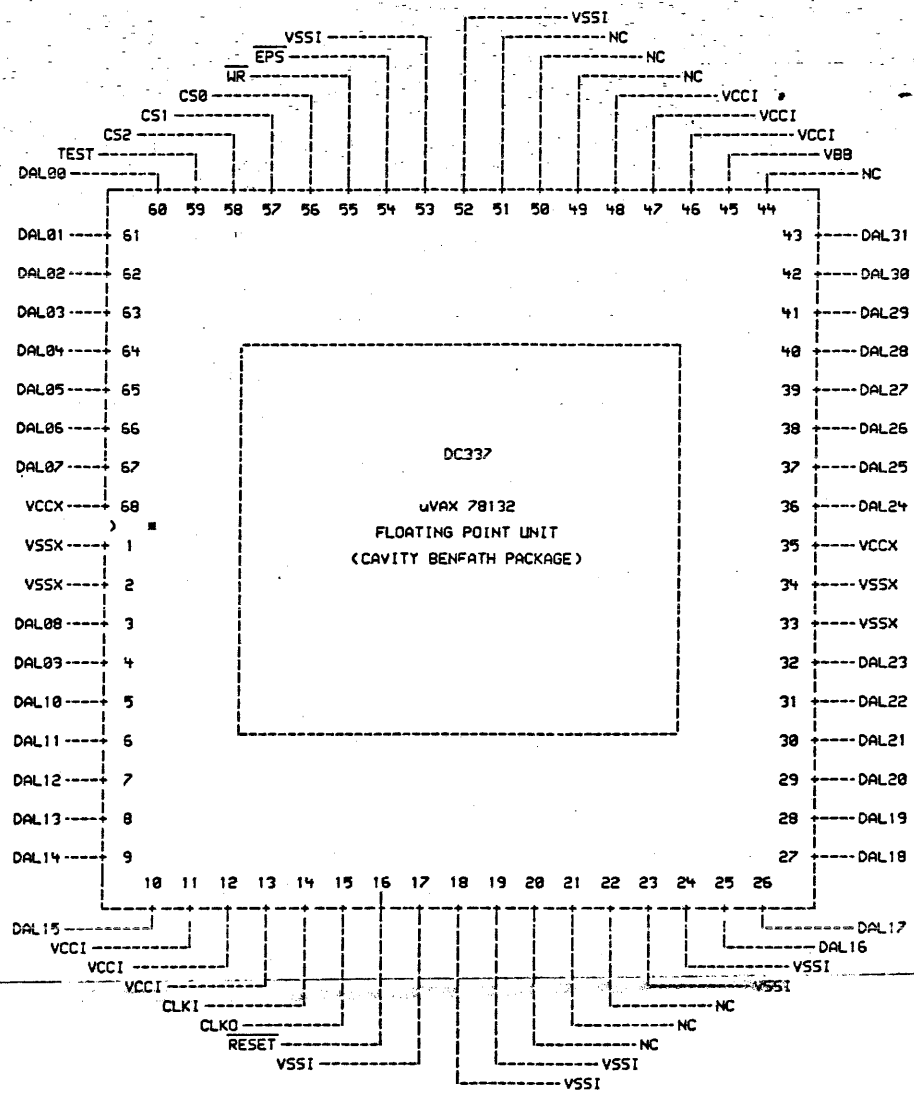
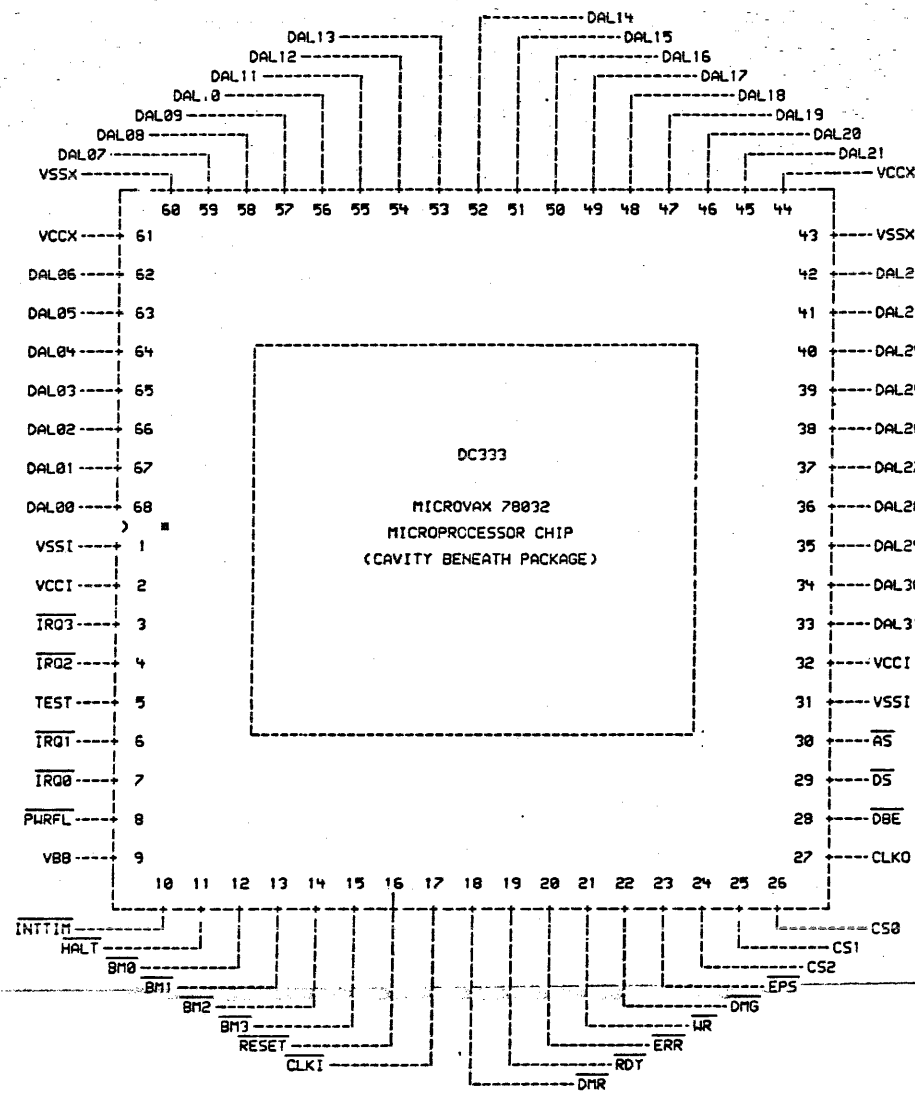
DRN:
BARRY MASKAS
CHK'D:
BARRY MASKAS

DATE
3-OCT-84
DATE
3-OCT-84

ENG:
BARRY MASKAS
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

DATE
3-OCT-84

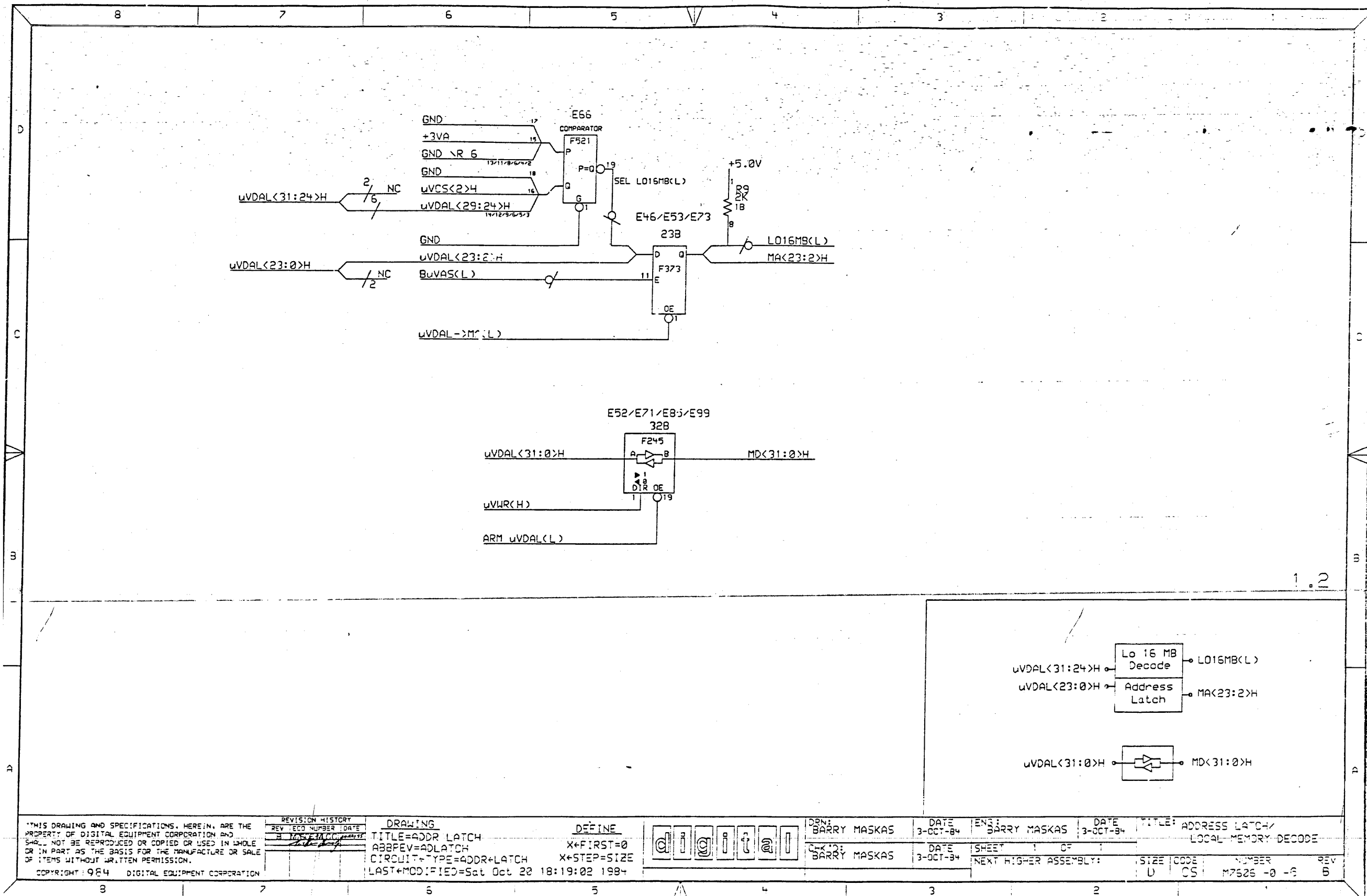
TITLE:
UVAX & FPU
SIZE CODE
D 105
NUMBER
17526 -2 -1
FE.
B



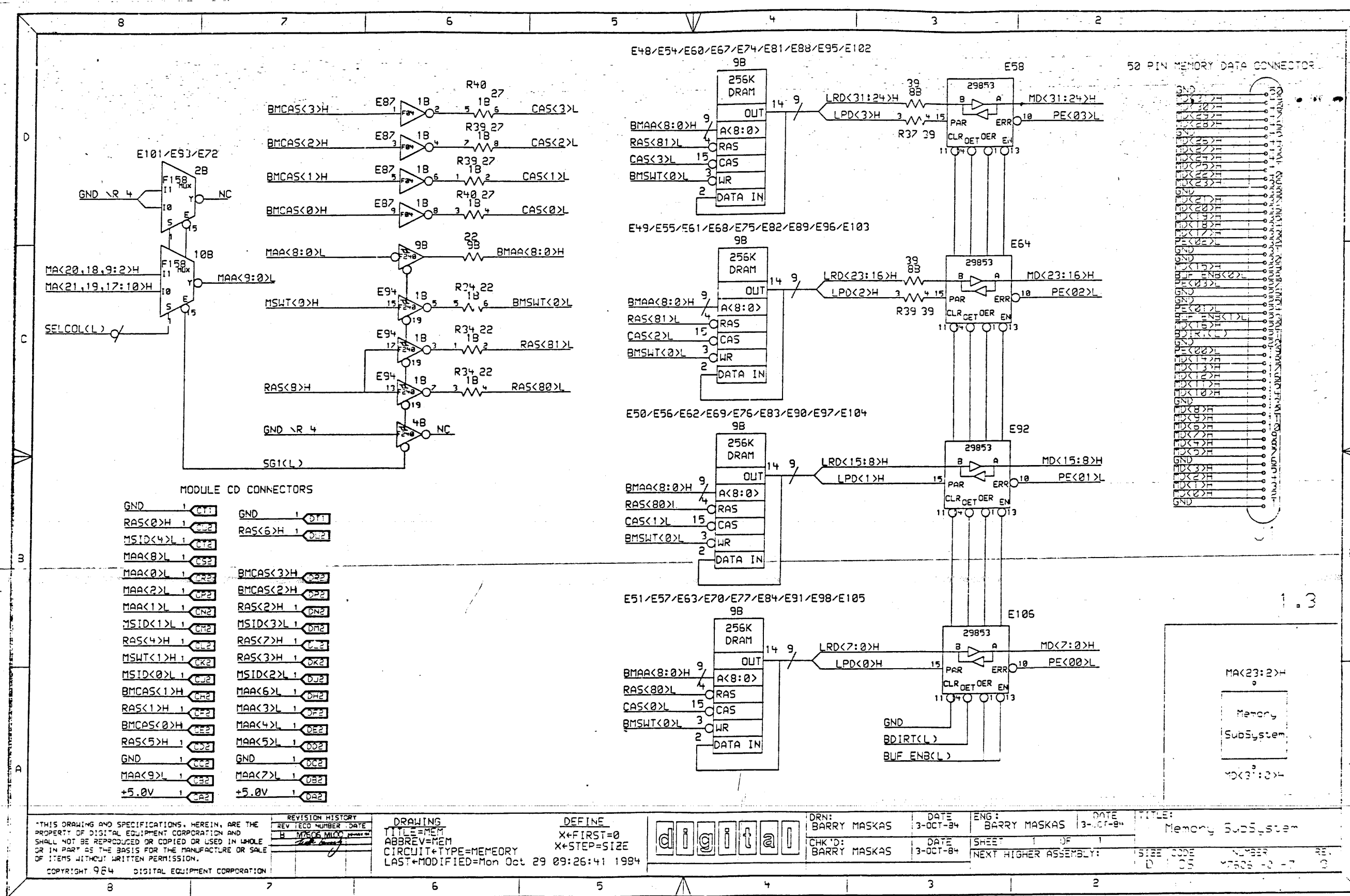
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REVISIONS	
CHK	CHANGE NO. REV

digital SUDCOM: (BOWEN)DC333.DRW 131 JUL-85 12:55 FIRST USED ON OPTION/MODEL:	DATE 12-AUG-85 CHK'D R. MCNAMARA DATE 12-AUG-85 SHEET 1 OF 1	ENG. MCNAMARA DATE 12-AUG-85 BOARD LOCATION:	TITLE: UVAX & FPU PINOUTS	SIZE D CODE CS NUMBER M7606-0-5 REV. A
--	--	--	---------------------------	--



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						TITLE=ADDR LATCH		X*FIRST=0				CIRCUIT TYPE=ADDR+LATCH		X*STEP=SIZE		SHEET 1 OF 1		NEXT HIGHER ASSEMBLY:		SIZE CODE	
						LAST MODIFIED=Sat Oct 20 18:19:02 1984														NUMBER	
																				U CS M7526 -0 -6	
																				REV	
																				B	



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REVISION HISTORY		
REV	TECO NUMBER	DATE
1	MEMO5 M100	10/29/84

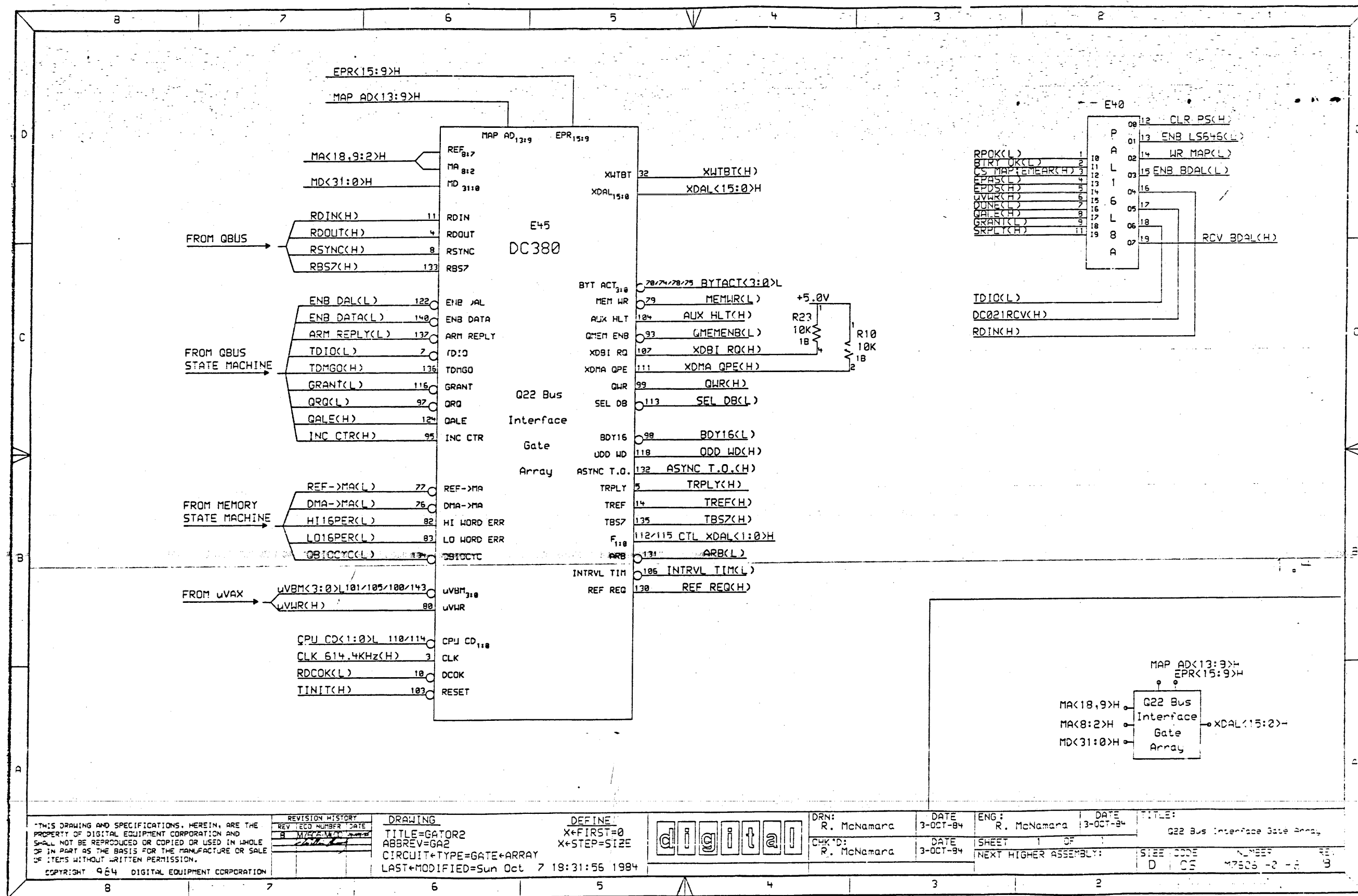
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LAST+MODIFIED=Mon Oct 29 09:26:41 1984

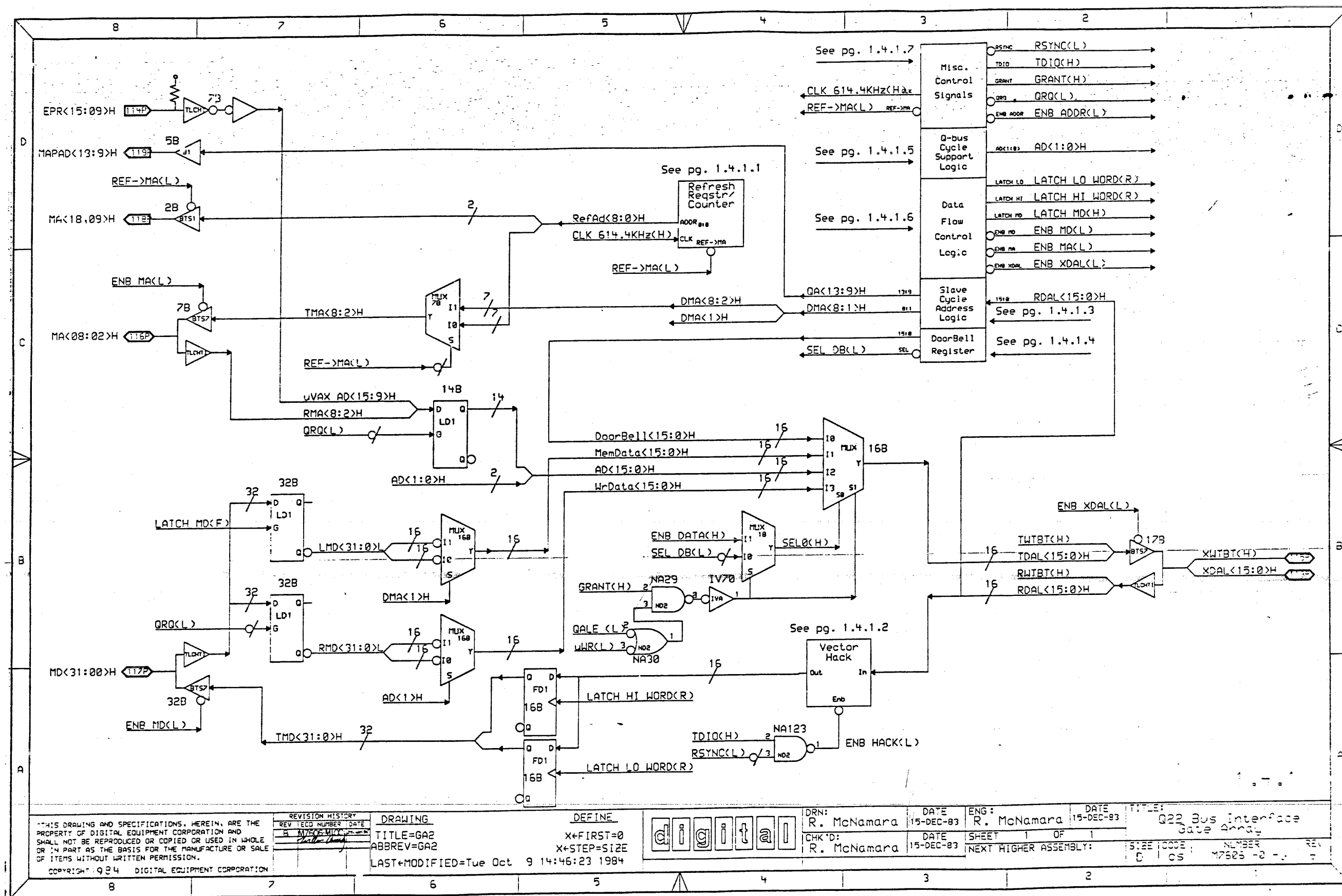
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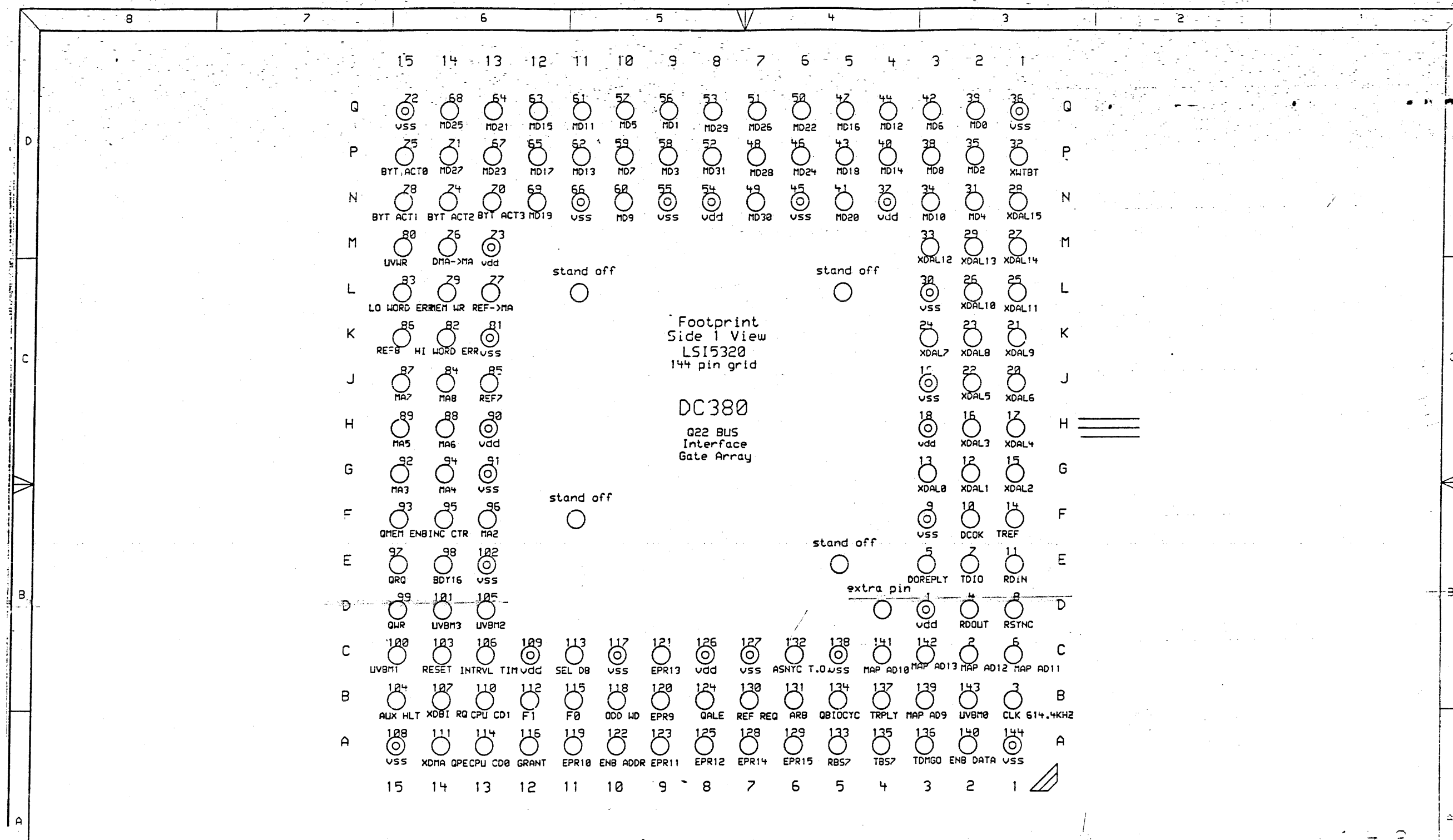
digital

DRN: BARRY MASKAS	DATE 3-OCT-84	ENG: BARRY MASKAS	DATE 3-OCT-84
CHK'D: BARRY MASKAS	DATE 3-OCT-84	SHEET 1 OF 1	
		NEXT HIGHER ASSEMBLY:	

TITLE: Memory Subsystem	
SIZE CODE	NUMBER
D 105	7505-2-7







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REVISION HISTORY		
REV	ECO NUMBER	DATE
1	1	18-MAY-84

digitai

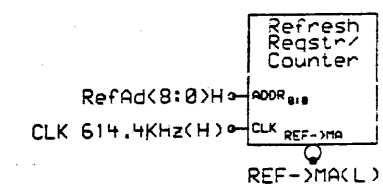
DRN: R. McNamara
CHK'D: R. McNamara

DATE 18-MAY-84
DATE 18-MAY-84

ENG: R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

DATE 18-MAY-84

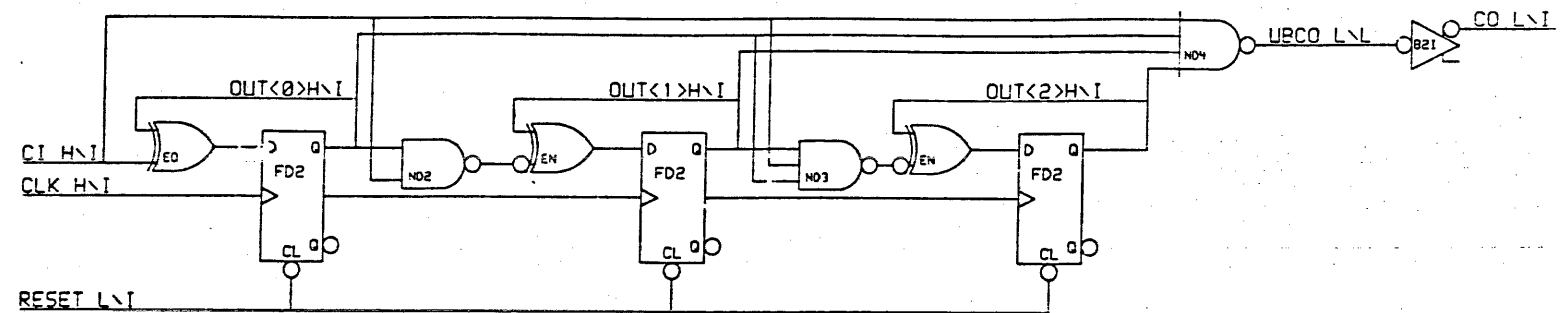
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LSI5320 IN 144 PIN GRID ARRAY
SIZE CODE NUMBER
D CS M7625 -2 -10 3



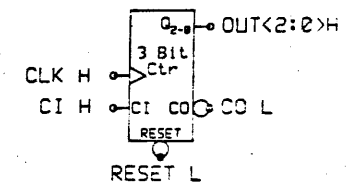
Copyright 1964 Digital Equipment Corporation

LAST MODIFIED=Wed Oct 10 19:06:07 1984

SIZE	CODE	NUMBER	REV
0	CS	M7625 -0 -11	B



1.4.1.1.2



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REVISION HISTORY		
REV	ECO NUMBER	DATE
1	17805	10/24/84

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 CIRCUIT+TYPE=3BITCTR
 LAST+MODIFIED=Sat Oct 20 18:47:47 1984

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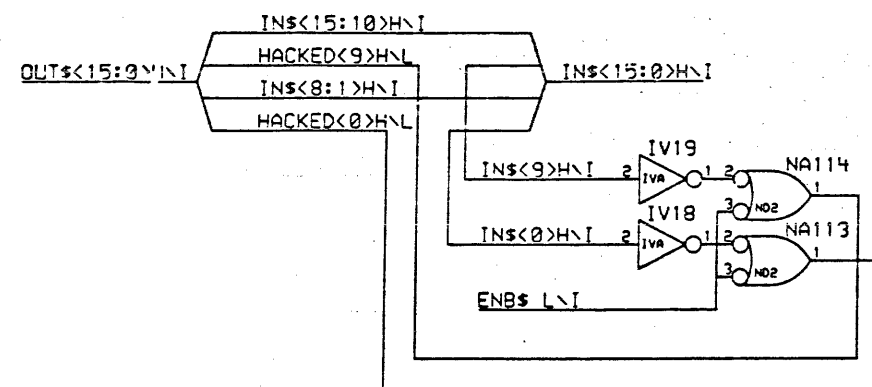
digital

DRN:
 R. McNamara
 CHK'D:
 R. McNamara

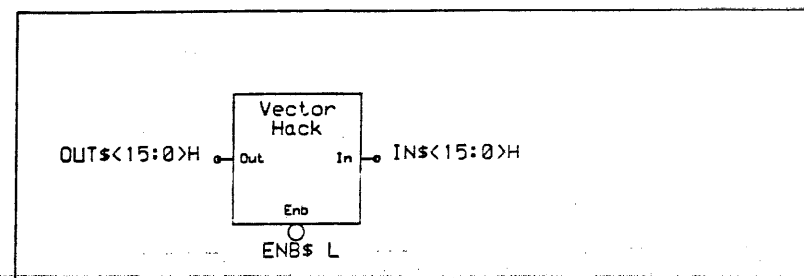
DATE
 6-NOV-83
 DATE
 6-NOV-83
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY:

ENG:
 R. McNamara
 TITLE:
 Synchronous 3 Bit Counter
 SIZE CODE
 D 05

DATE
 6-NOV-83
 REVISION
 1



1.4.1.2



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REV	ECO	NUMBER	DATE
1		1	10/10/84

DRAWING
TITLE=VECTOR HACK
ABBREV=VCTRCK
LAST*MODIFIED=Wed Oct 10 19:16:55 1984

DEFINE
X*FIRST=0
X*STEP=SIZE

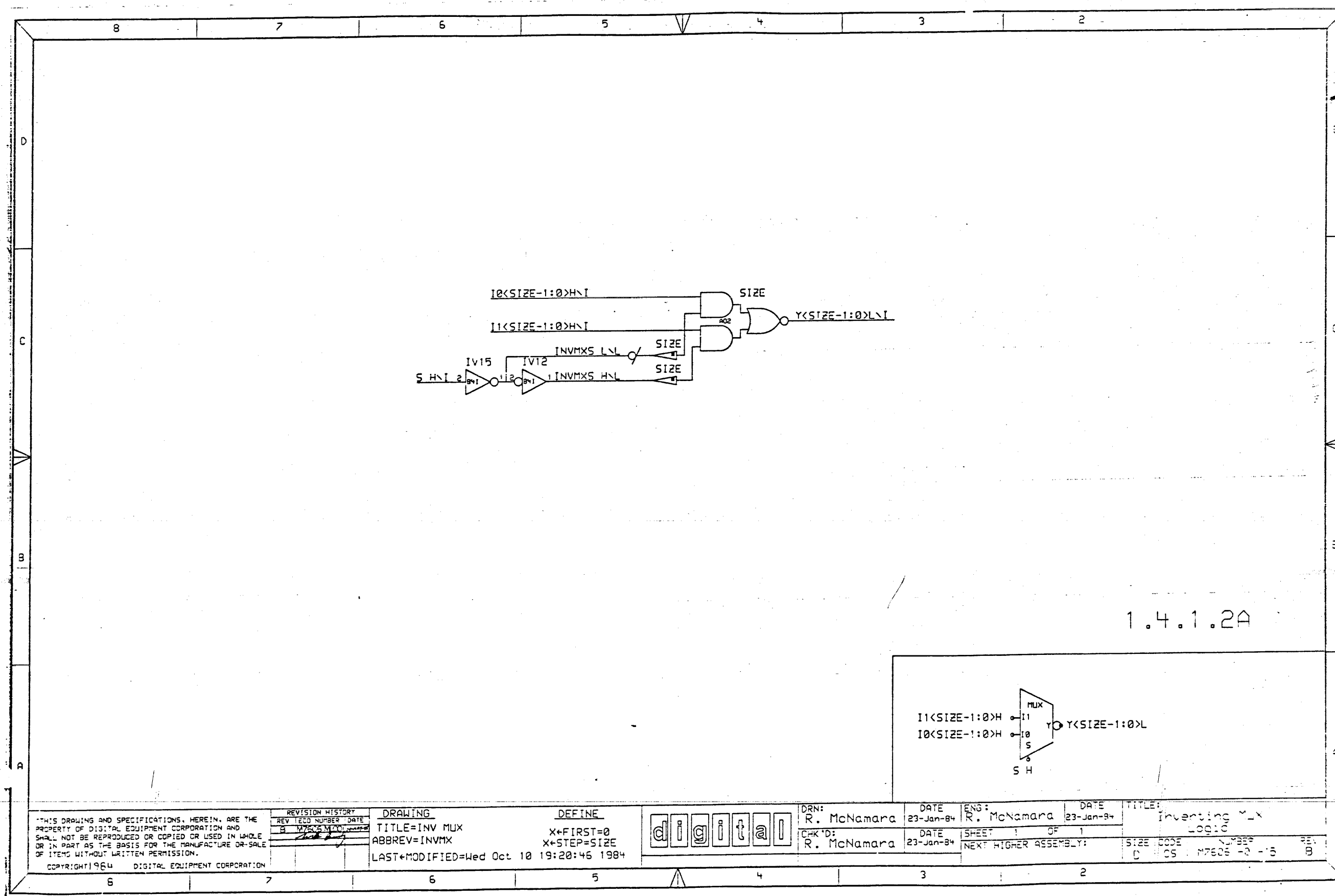
digital

DRN:
R. McNamara
CHK'D:
R. McNamara

DATE
9-NOV-83
DATE
9-NOV-83

ENG:
R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

TITLE:
Vector Hack
SIZE CODE
0 CS 47505 -2 -4



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REVISION HISTORY		
REV	TECD NUMBER	DATE
B	W565M001	1984

DRAWING
TITLE=INV MUX
ABBREV=INVMX
LAST*MODIFIED=Wed Oct 10 19:20:46 1984

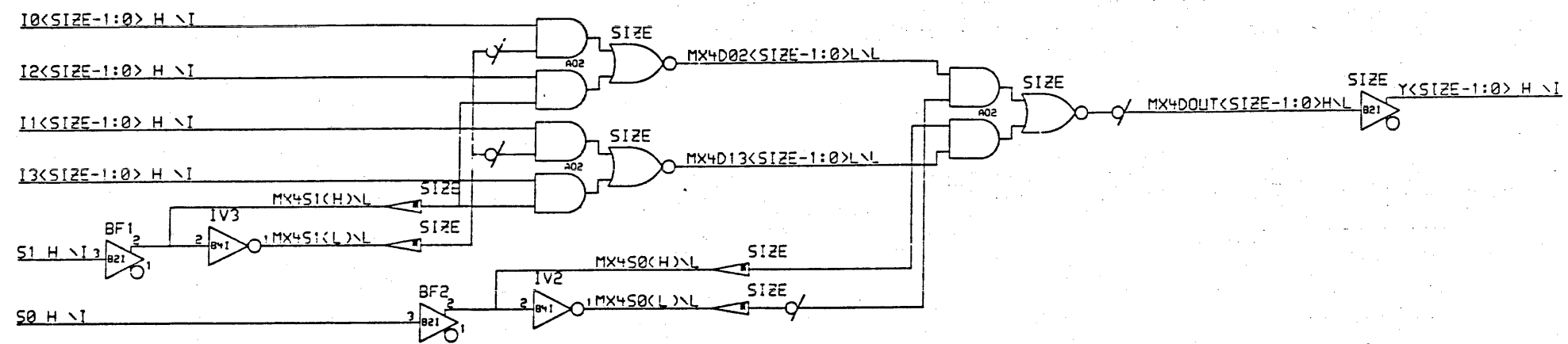
DEFINE
X*FIRST=0
X*STEP=SIZE
digital

DRN:
R. McNamara
CHK'D:
R. McNamara

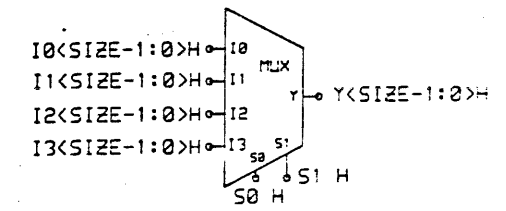
DATE
23-Jan-84
DATE
23-Jan-84

ENG:
R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

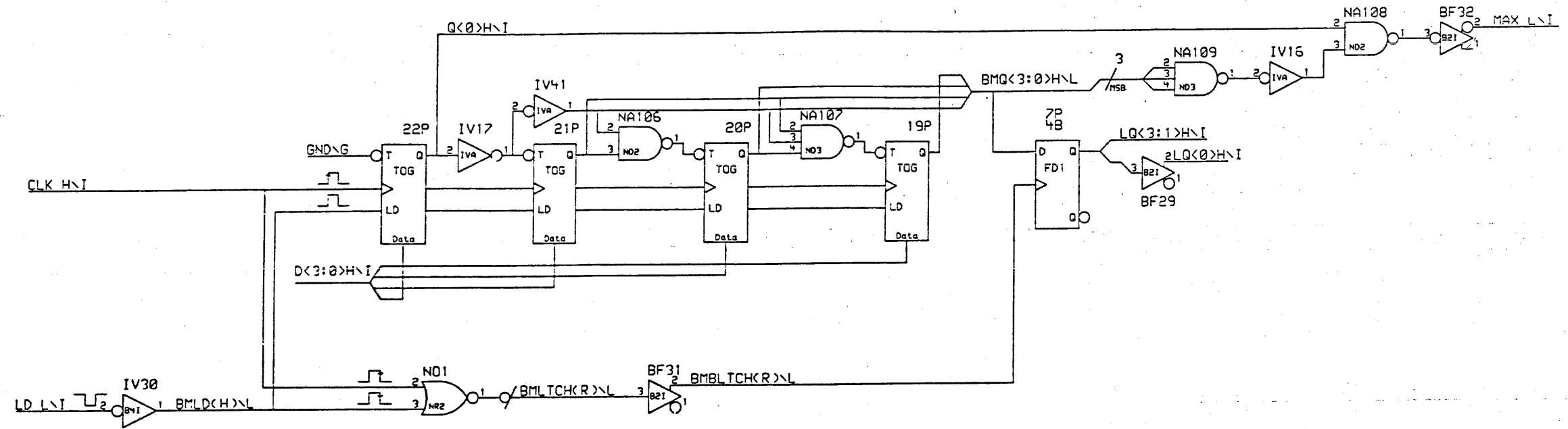
DATE
23-Jan-84
TITLE:
Inverting Mux Logic
SIZE CODE
D CS M7625 -2 -5



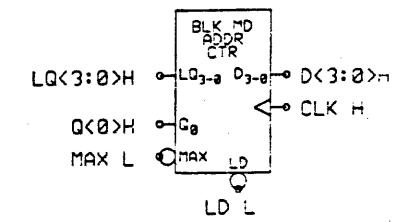
1.4.1.2B



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	REV	ECD NUMBER	DATE	TITLE=4MUX		X+FIRST=0	R. McNamara	12-JAN-84	R. McNamara	10-JAN-84	4 to 1 MUX
	1		M7625-10-10-84	ABBREV=4MX		X+STEP=SIZE	CHK'D:	DATE	SHEET	OF	
				LAST+MODIFIED=Wed Oct 10 19:23:47 1984		R. McNamara	10-JAN-84	NEXT HIGHER ASSEMBLY:	SIZE CODE	CS	7625-10-6



1.4.1.3.1



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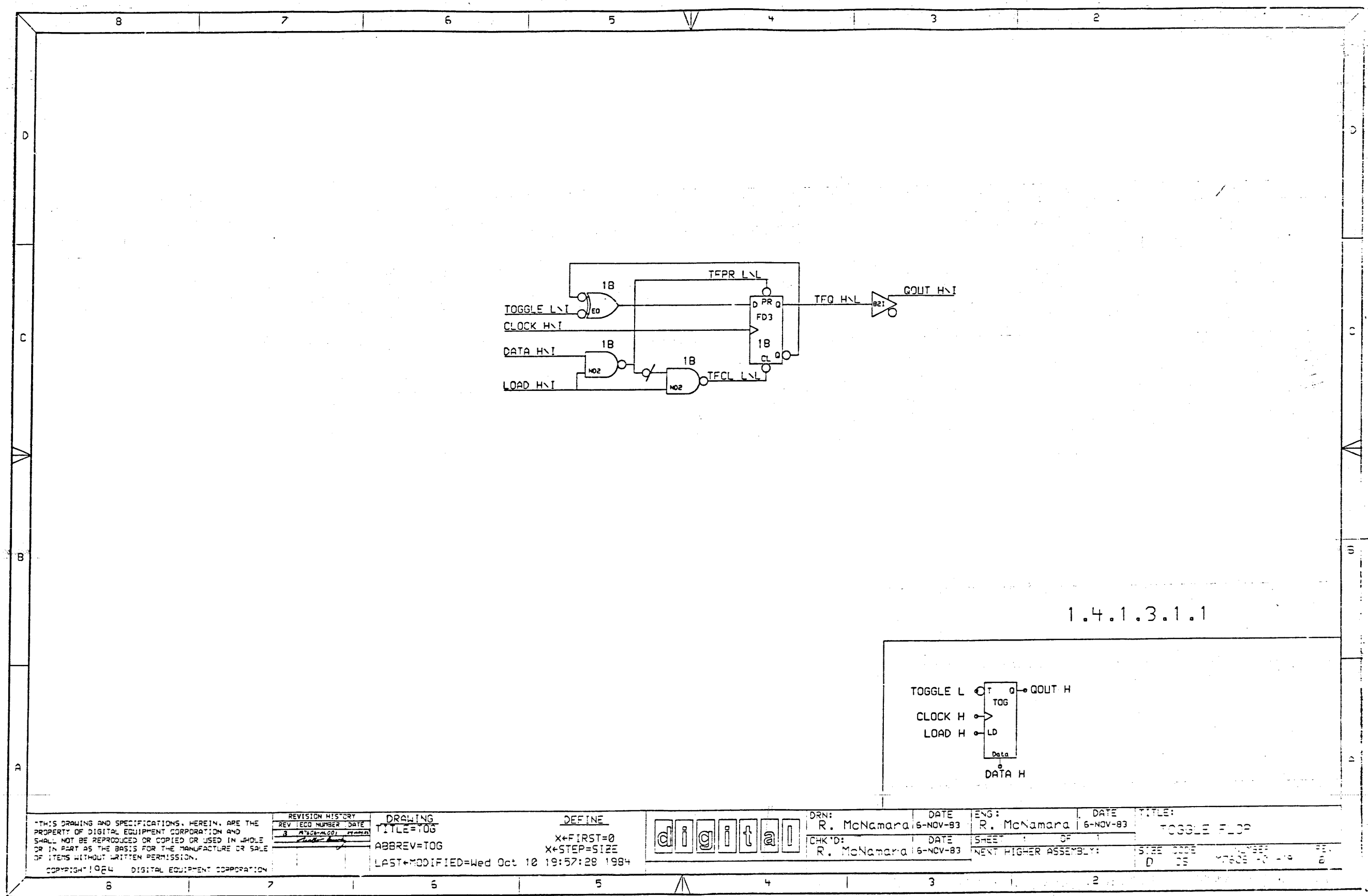
REVISION HISTORY			
REV	ECO NUMBER	DATE	DESCRIPTION
1		6-NOV-83	Initial Design

DRAWING
TITLE=BLK MD CTR
ABBREV=BMCTR
CIRCUIT+TYPE=BLKMDCTR
LAST+MODIFIED=Wed Oct 10 19:28:01 1984

DEFINE
X*FIRST=0
X*STEP=SIZE

digital

DRN: R. McNamara	DATE: 6-NOV-83	ENG: R. McNamara	DATE: 6-NOV-83	TITLE: BLK MD CTR LOGIC
CHK'D: R. McNamara	DATE: 6-NOV-83	SHEET: 1 OF 1	NEXT HIGHER ASSEMBLY:	SIZE: CODE NUMBER: D CS 7525-03-18



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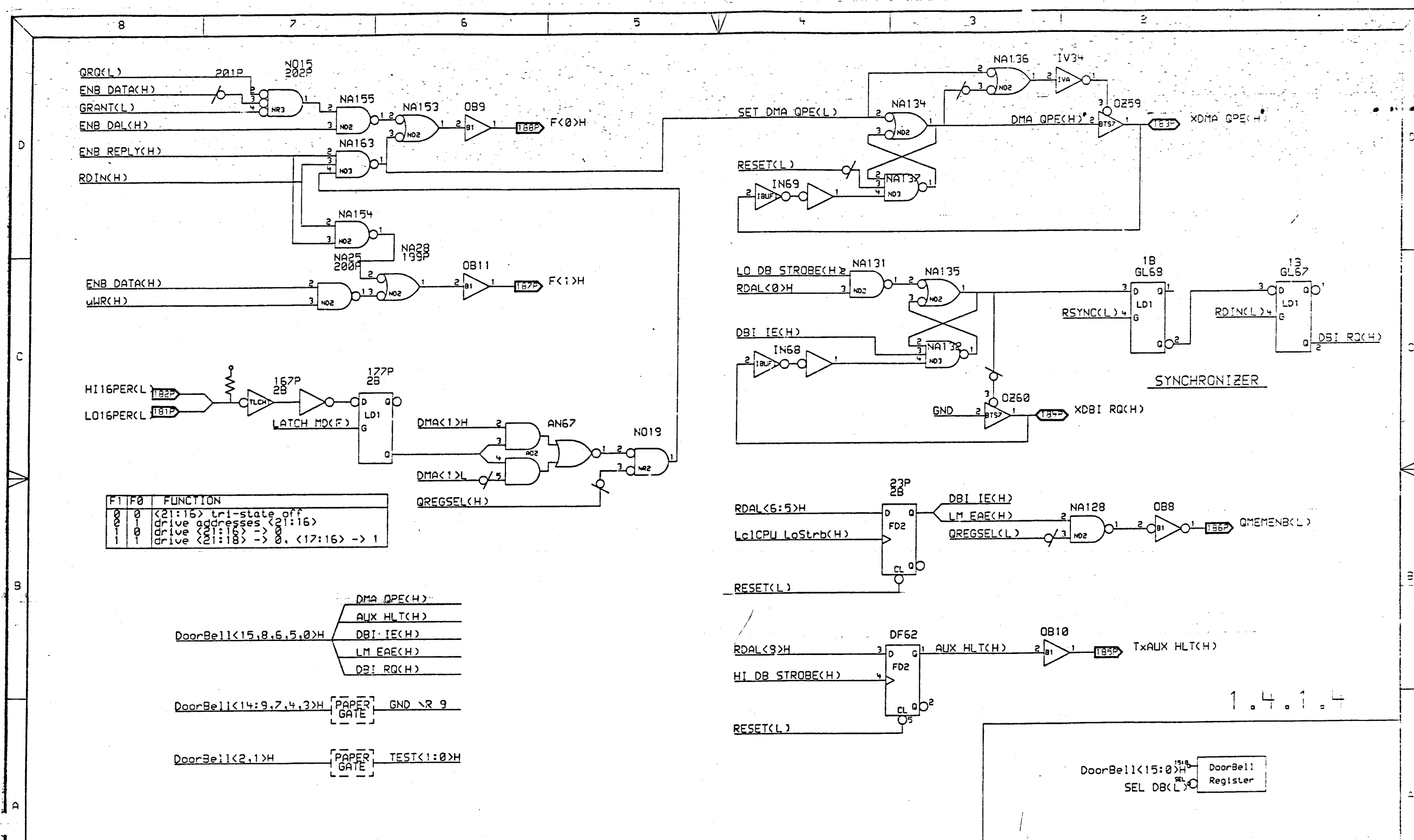
REVISION HISTORY		
REV	ECO NUMBER	DATE
1	1	1984

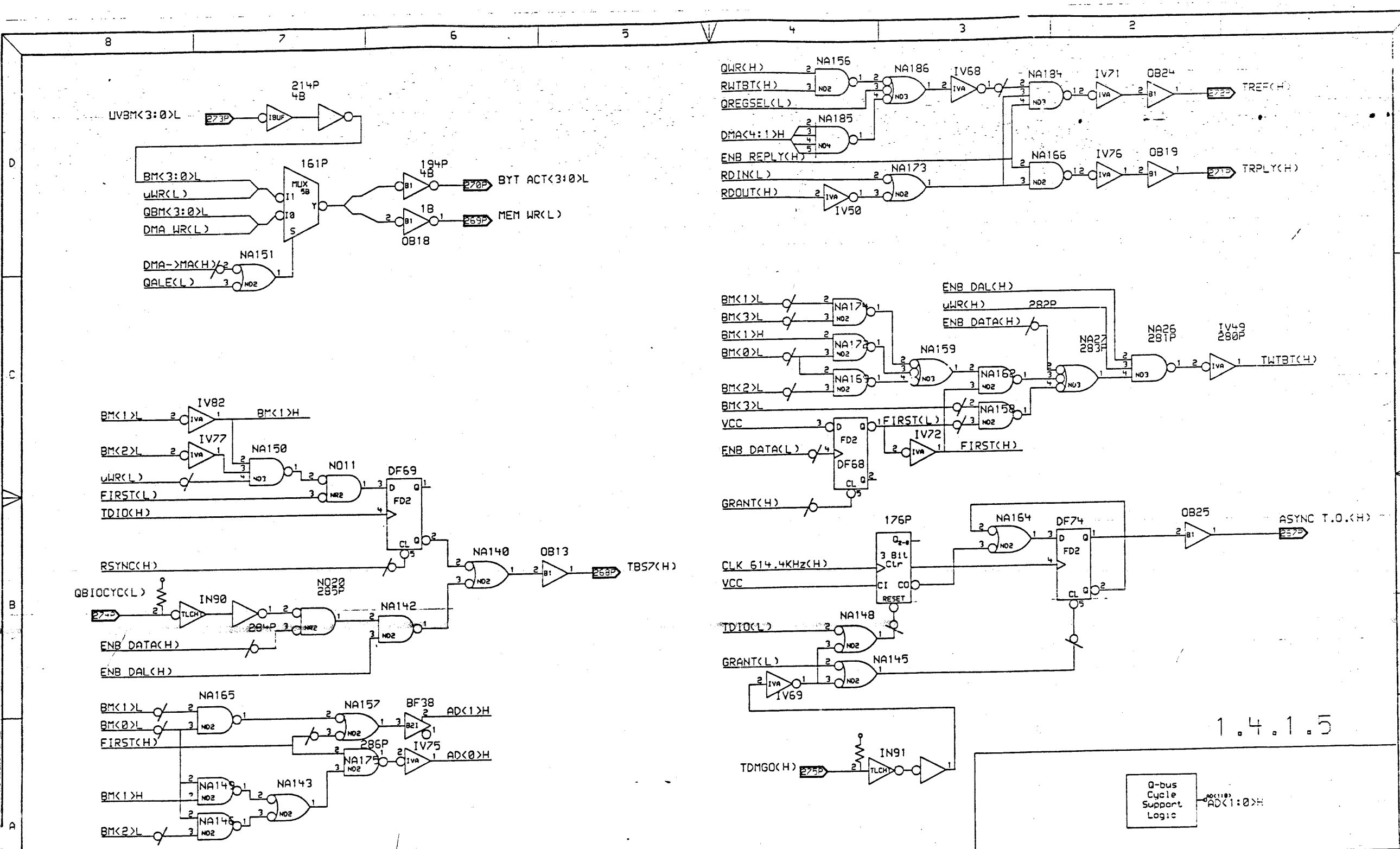
DRAWING
TITLE=TOG
ABBREV=TOG
LAST MODIFIED=Wed Oct 10 19:57:28 1984

DEFINE
X*FIRST=0
X*STEP=SIZE

digital

DRN: R. McNamara	DATE 6-NOV-83	ENG: R. McNamara	DATE 6-NOV-83	TITLE: TOGGLE FLOP
CHK'D: R. McNamara	DATE 6-NOV-83	SHEET 1 OF 1	NEXT HIGHER ASSEMBLY:	SIZE CODE D 05





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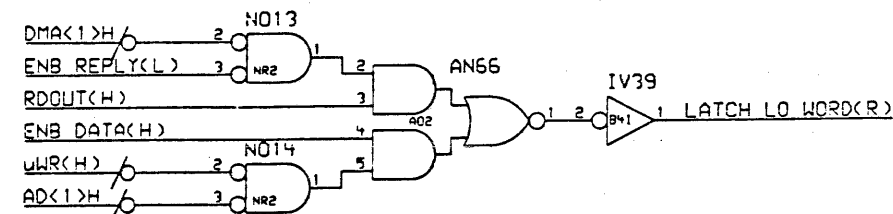
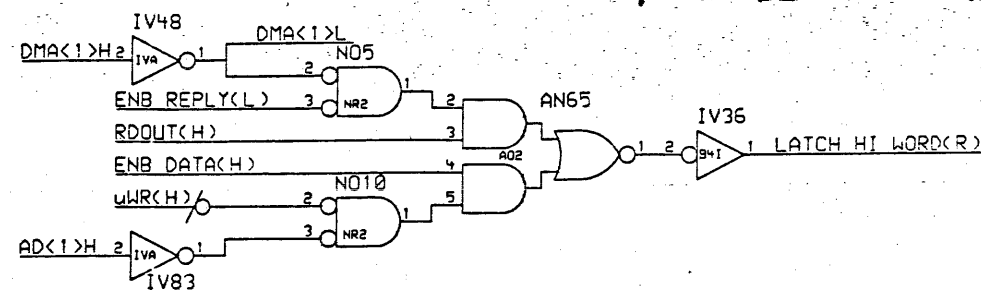
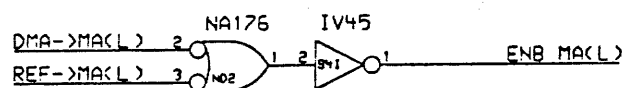
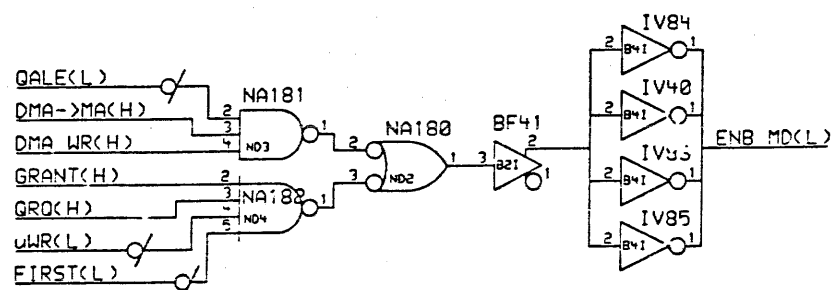
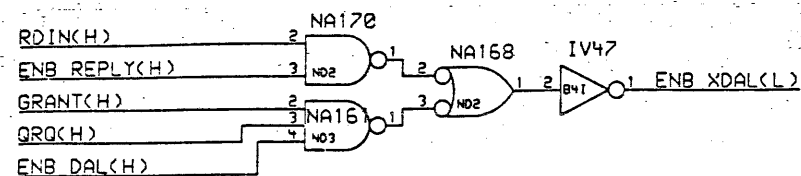
COPYRIGHT 1984 DIGITAL EQUIPMENT CORPORATION

REVISION HISTORY		
REV	ECO NUMBER	DATE
1	11746M20	21-OCT-83

DRAWING
LAST MODIFIED=Wed Oct 10 19:53:46 1984

digital

DRN:	R. McNamara	DATE	21-OCT-83	ENG:	R. McNamara	DATE	21-OCT-83	TITLE:	Q-bus Support Logic
CHK'D:	R. McNamara	DATE	21-OCT-83	SHEET	1	OF		SIZE	CODE
NEXT HIGHER ASSEMBLY:				NUMBER					
				005 17508-10-21					



1.4.1.6

Data	LATCH LO WORD(R)
Flow	LATCH HI WORD(R)
Control	LATCH MD(H)
Logic	ENB MD(L)
	ENB MAC(L)
	ENB XDAL(L)

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REV	ECO NUMBER	DATE
B	1000	10-10-84

DRAWING

LAST MODIFIED=Wed Oct 10 19:45:31 1984

digital

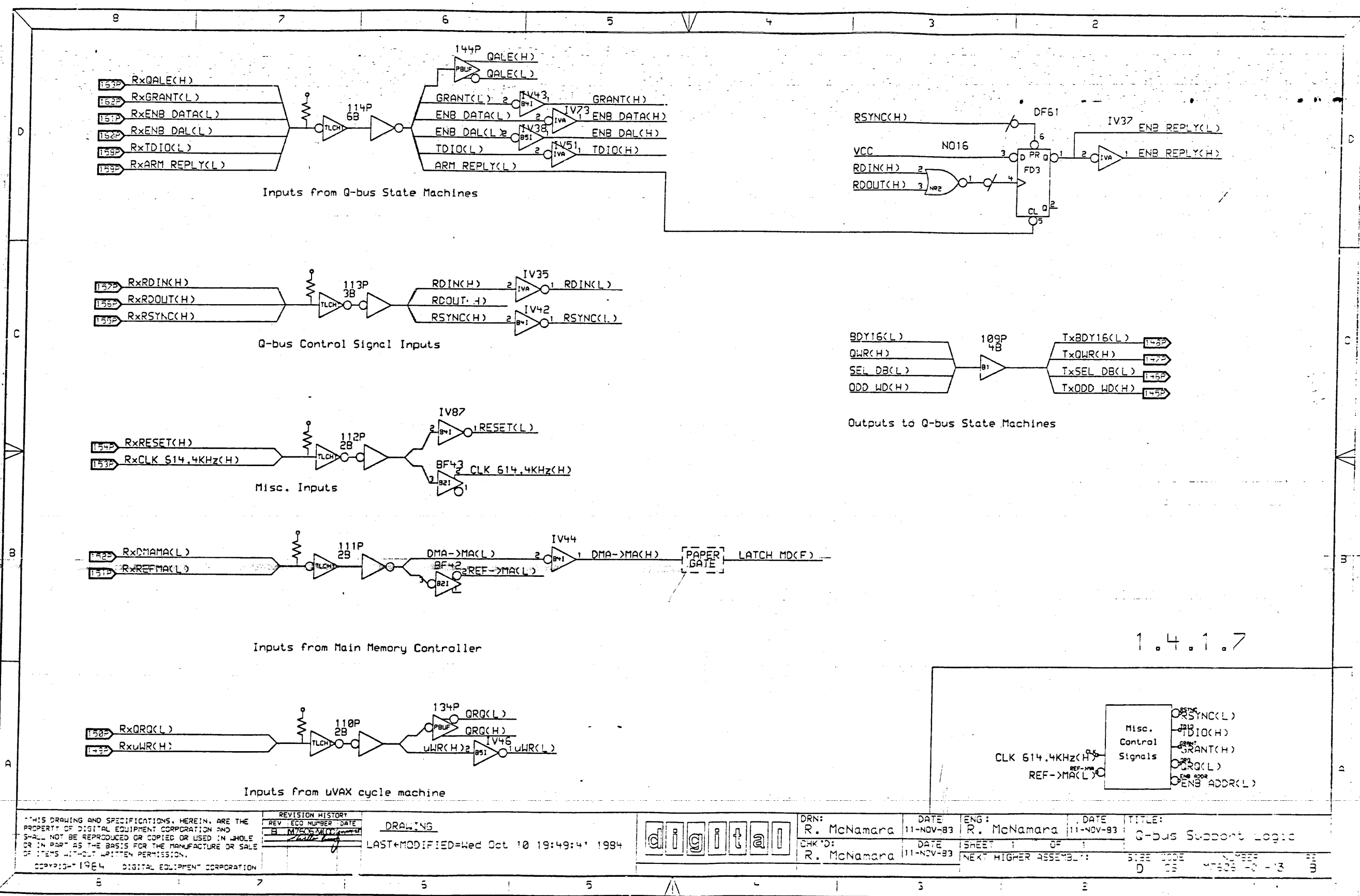
DRN: R. McNamara
CHK'D: R. McNamara

DATE 09-NOV-83
DATE 09-NOV-83

ENG: R. McNamara
SHEET 05

DATE 09-NOV-83
NEXT HIGHER ASSEMBLY:

FILE: Q-bus Support Logic
ISSUE CODE NUMBER
D 05 17308-00-22



1.4.1.7

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REVISION HISTORY		
REV	ECO NUMBER	DATE
1	1	11-NOV-83

DRAWING
LAST MODIFIED: Wed Oct 10 19:49:41 1984

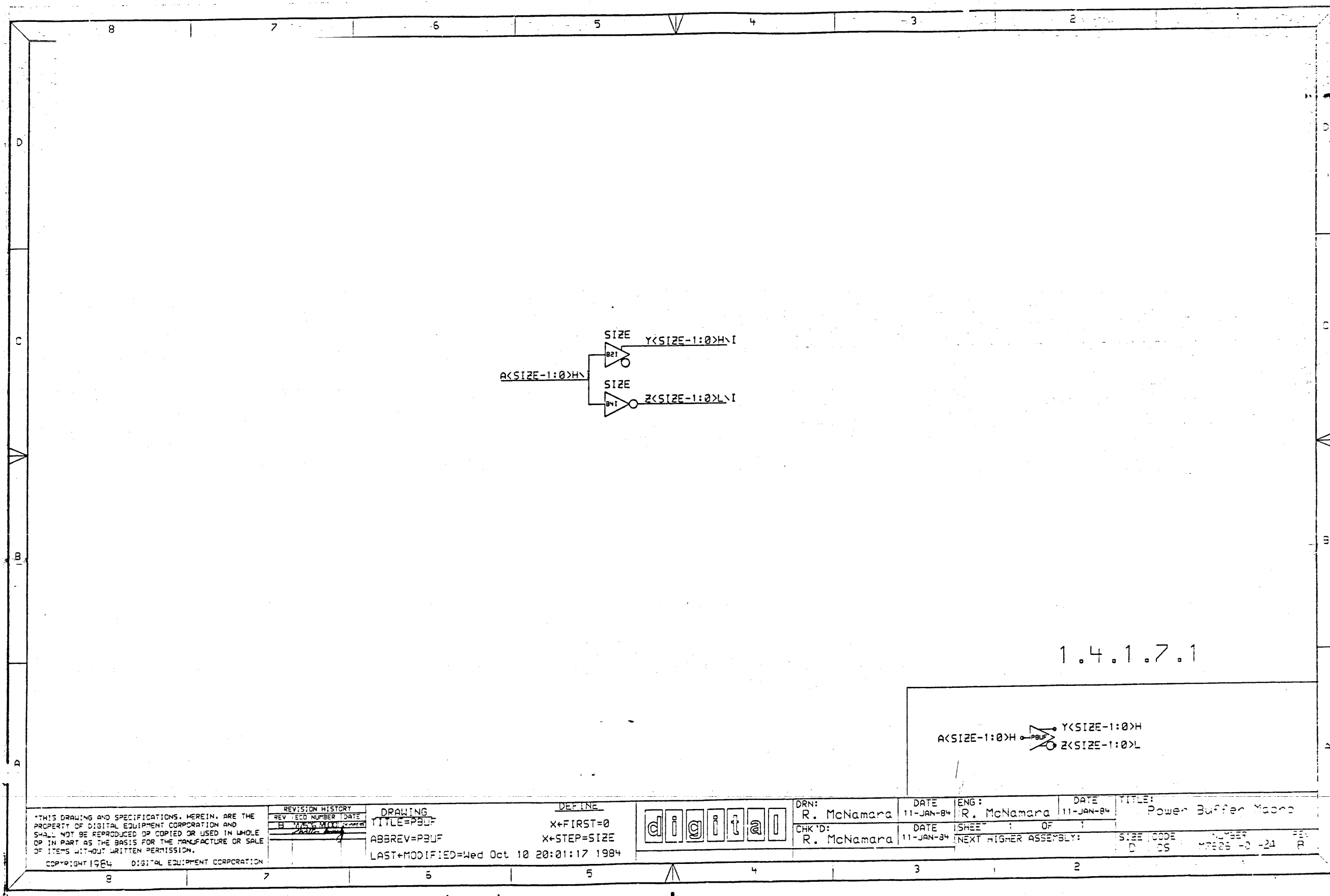
digital

DRN: R. McNamara
CHK'D: R. McNamara

DATE 11-NOV-83
DATE 11-NOV-83

ENG: R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

TITLE: Q-bus Support Logic
SIZE: 11-NOV-83
D 25 47628-10-13



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REV	ECO NUMBER	DATE
1	1	11-JAN-84

DRAWING
TITLE=PSUF
ABBREV=PSUF
LAST MODIFIED=Wed Oct 10 20:01:17 1984

DEFINE
X+FIRST=0
X+STEP=SIZE

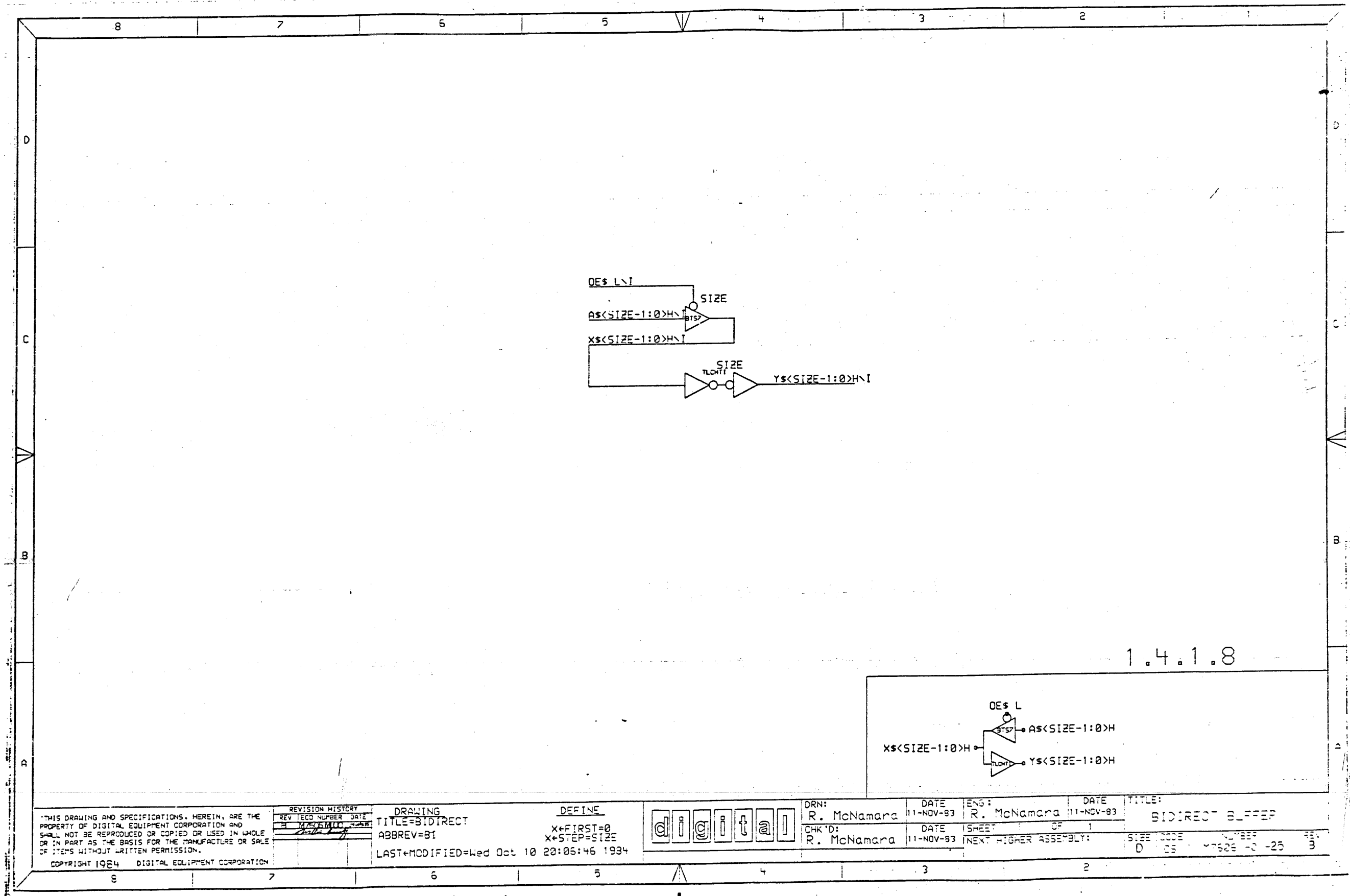
digital

DRN:
R. McNamara
CHK'D:
R. McNamara

DATE
11-JAN-84
DATE
11-JAN-84

ENG:
R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

TITLE:
Power Buffer Macro
SIZE CODE
D 05
NUMBER
57825-01-24
REV
R



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REVISION HISTORY		
REV	ISSUED NUMBER	DATE
1	10-10-84	10-10-84

DRAWING
TITLE=BIDIRECT
ABBREV=B1
LAST MODIFIED=Wed Oct 10 20:05:46 1984

DEFINE

X+FIRST=0
X+STEP=SIZE

digital

DRN:
R. McNamara
CHK'D:
R. McNamara

DATE
11-NOV-83

ENG:
R. McNamara
DATE
11-NOV-83

TITLE:

BIDIRECT BLFPER

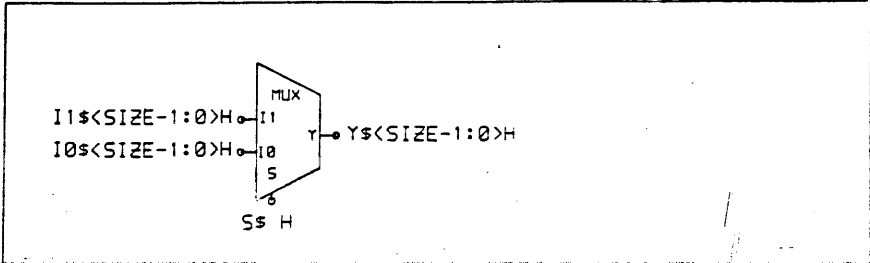
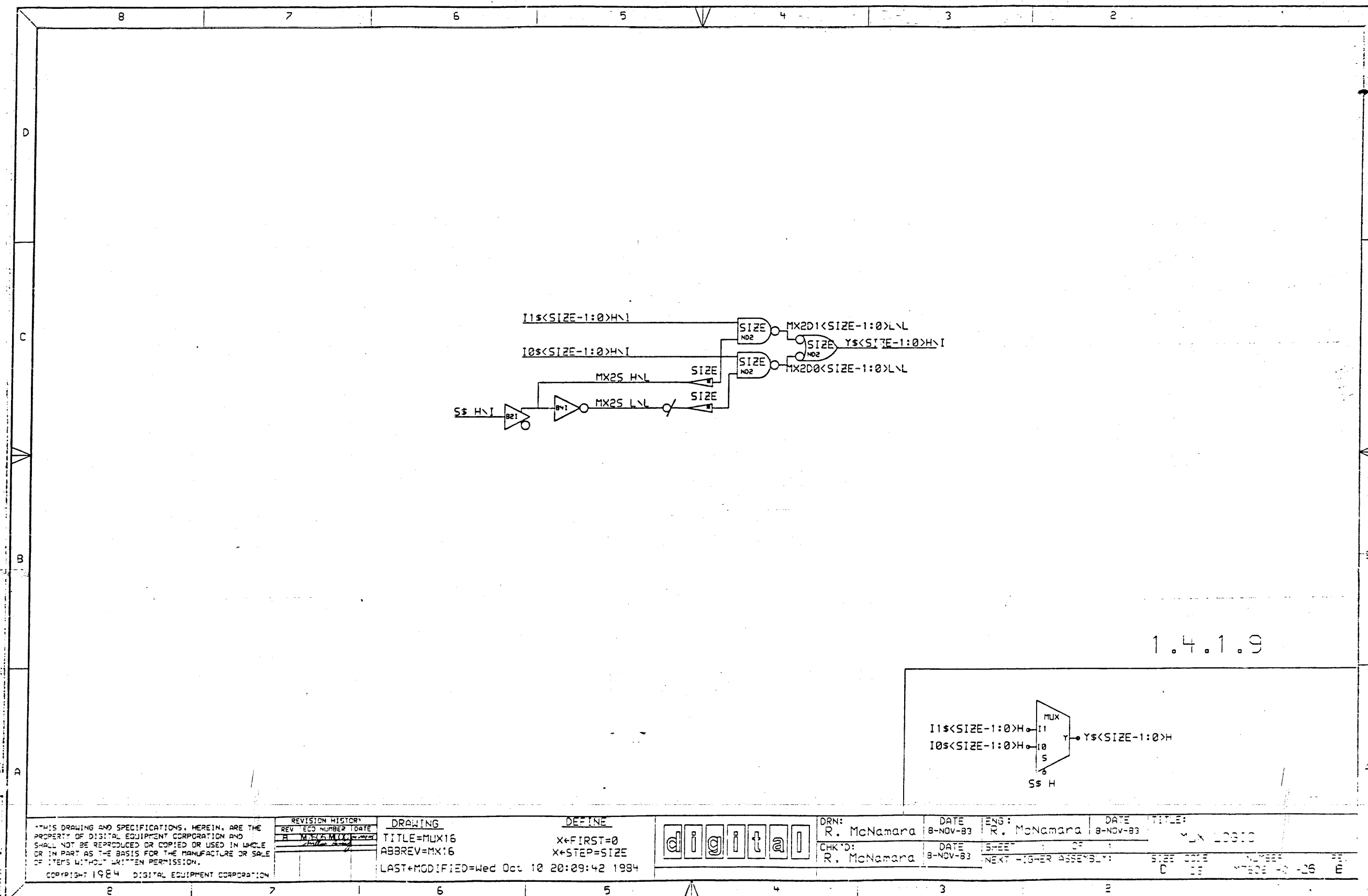
SHEET
1

NEXT HIGHER ASSEMBLY:

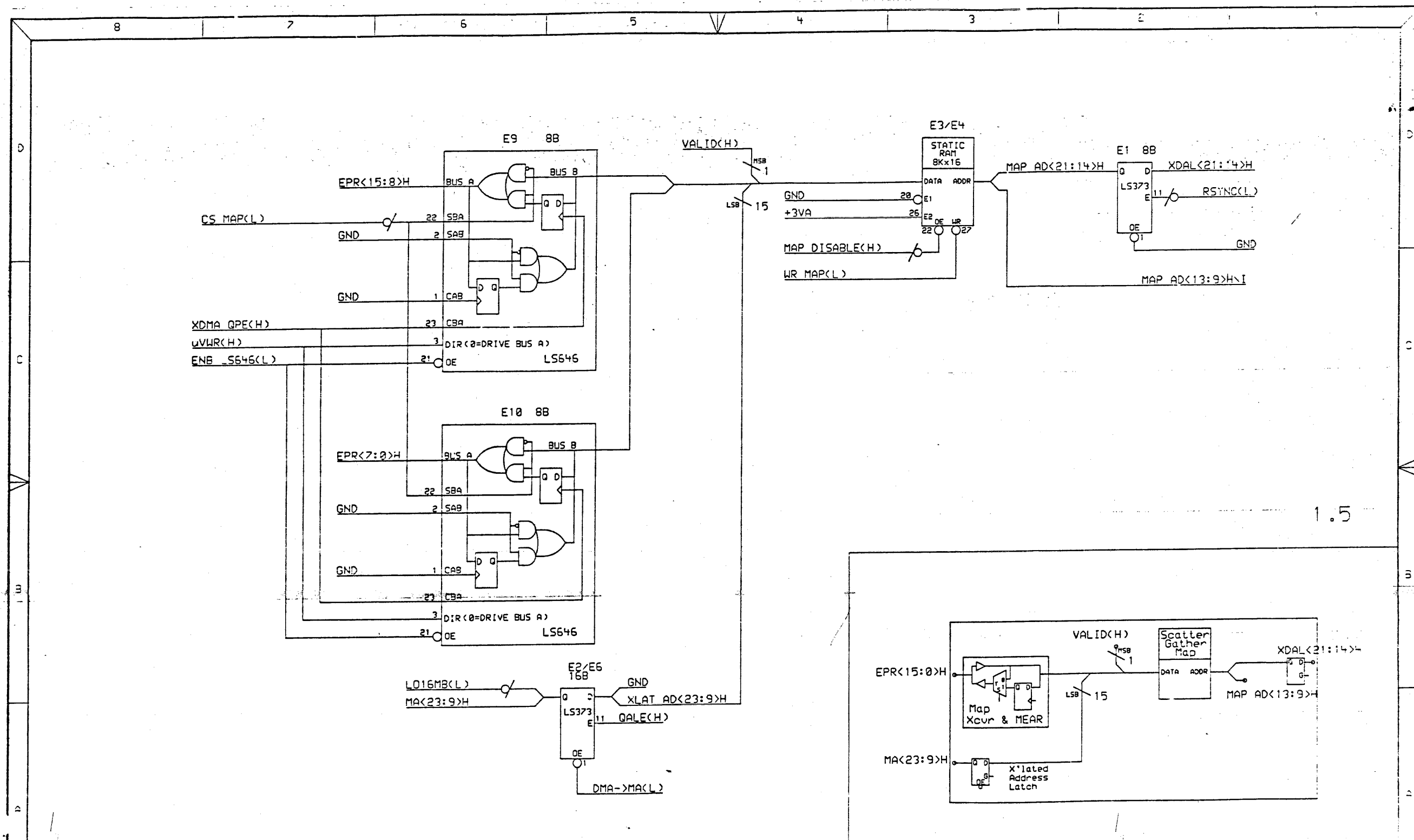
SIZE CODE
D 05

NUMBER
17526-02-25

REV
3



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	REV	ECO NUMBER	DATE				R. McNamara	8-NOV-83	R. McNamara	8-NOV-83	MUX LOGIC
	1	1	8-NOV-83								
			TITLE=MUX16		X+FIRST=0		CHK'D:	DATE	SHEET	OF	NEXT HIGHER ASSEMBLY:
			ABBREV=MX16		X+STEP=SIZE						
			LAST*MODIFIED=Wed Oct 10 20:09:42 1984				R. McNamara	8-NOV-83	1	1	SIZE CODE
											NUMBER
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REV	ECO NUMBER	DATE
1	1	10/7/84

DRAWING
TITLE=MAP GROUP
ABBREV=MAP
CIRCUIT+TYPE=TRANS+MAP
LAST+MODIFIED=Sun Oct 7 18:33:49 1984

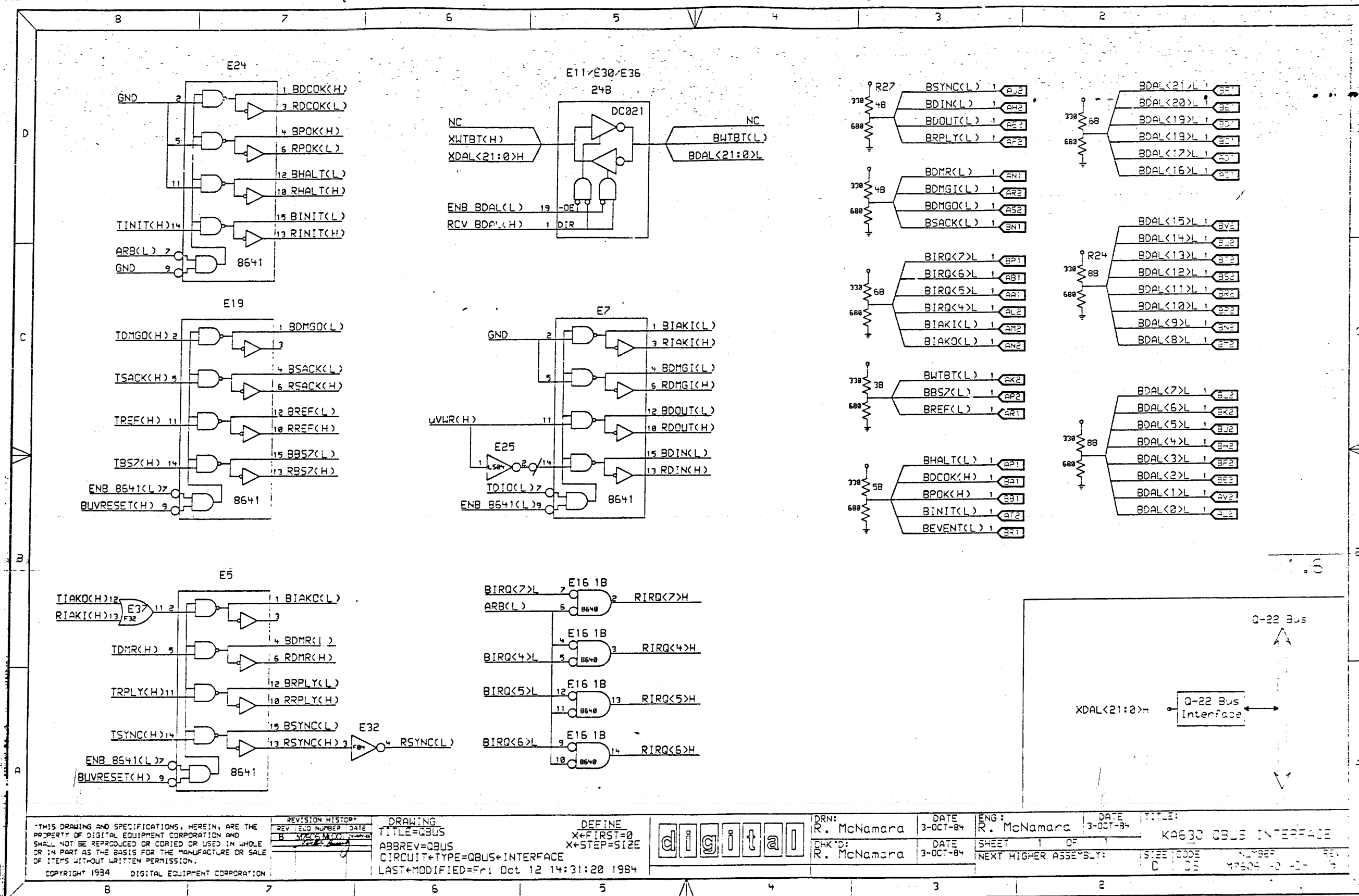
DEFINE
X+FIRST=0
X+STEP=SIZE

DES: R. McNamara
CHK'D: R. McNamara

DATE: 3-OCT-84
DATE: 3-OCT-84

ENG: R. McNamara
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

TITLE: Translation Map Group
SIZE/ CODE: D CS
NUMBER: M7605-22-27



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REVISION HISTORY	DATE
1	10/12/84

DRAWING
TITLE=QBUS
ABBREV=QBUS
CIRCUIT+TYPE=QBUS+INTERFACE
LAST+MODIFIED=Fri Oct 12 14:31:20 1984

DEFINE
X+FIRST=0
X+STEP=SIZE

digital

DRN:
R. McNamara
DATE
3-OCT-84

CHK'D:
R. McNamara
DATE
3-OCT-84

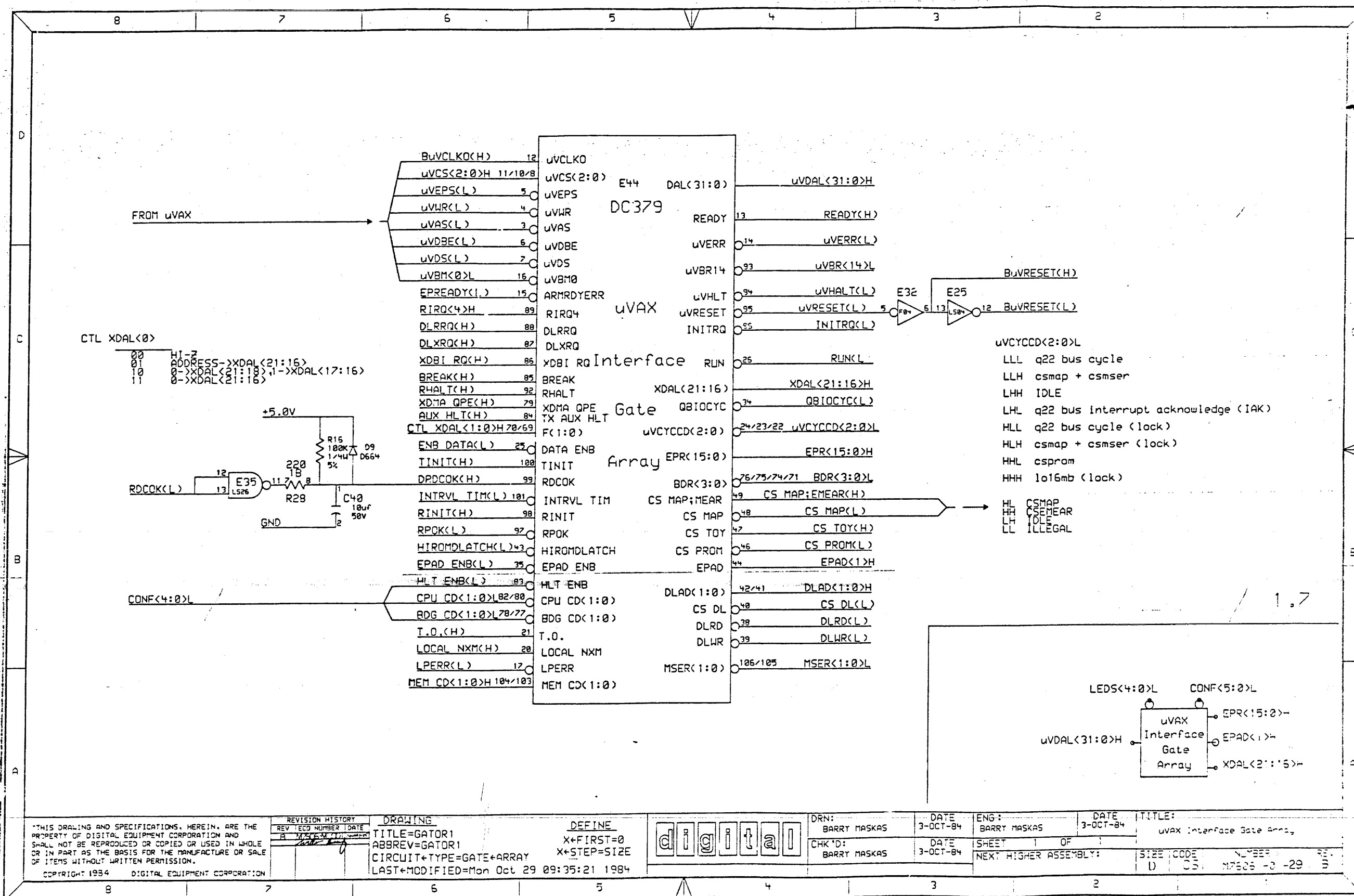
ENG:
R. McNamara
DATE
3-OCT-84

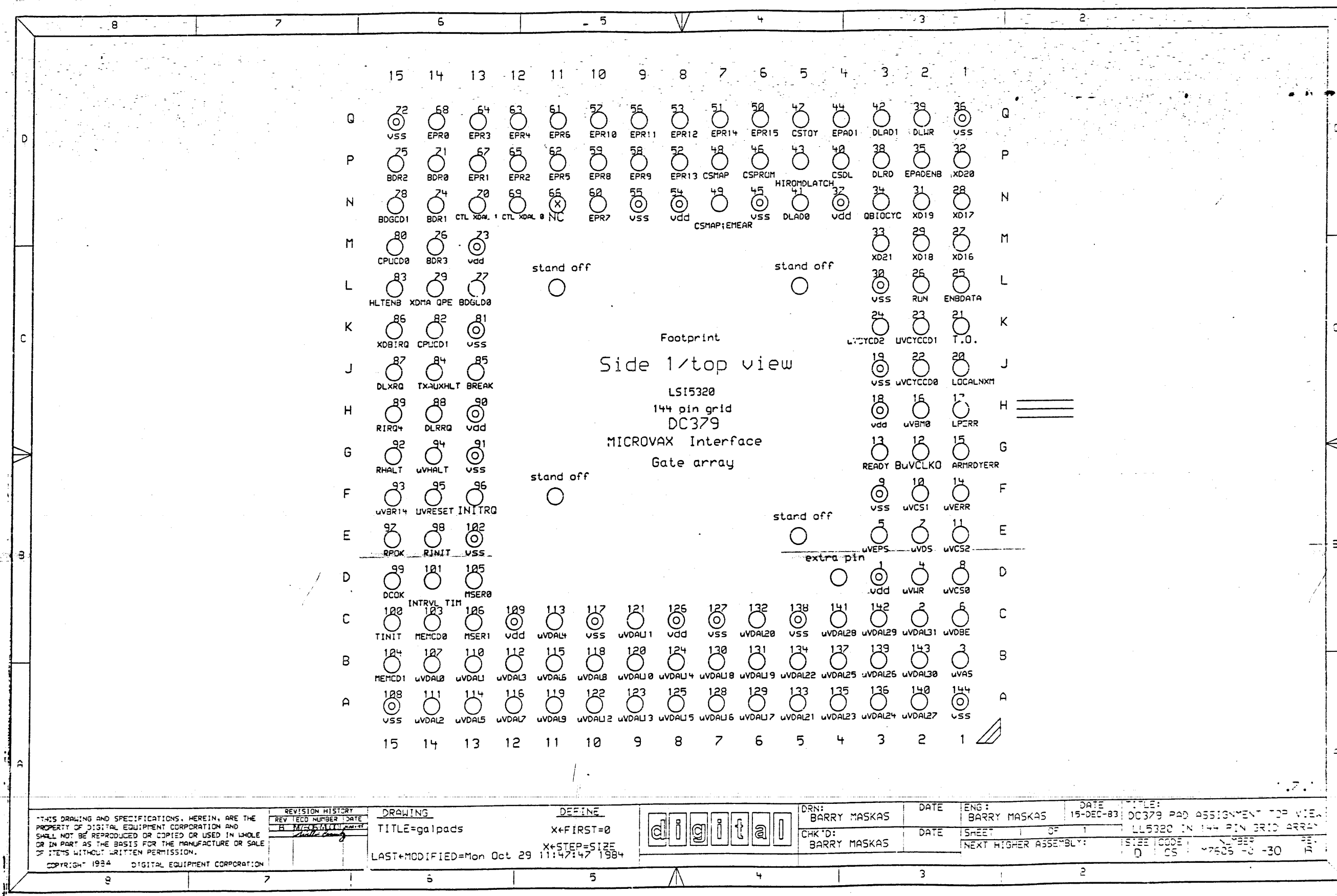
SHEET 1 OF 1

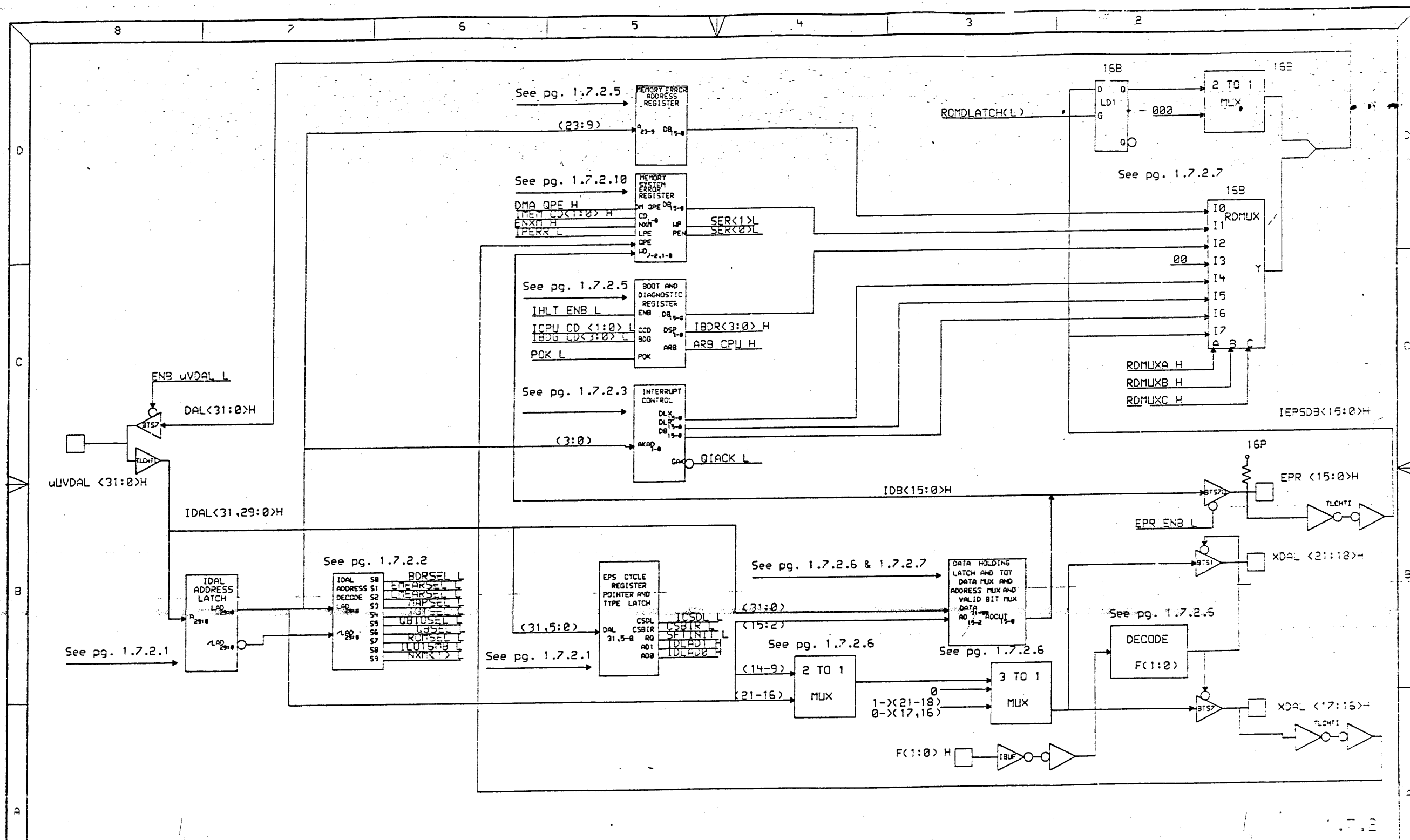
NEXT HIGHER ASSEMBLY:

TITLE:
KA630 QBUS INTERFACE

SIZE CODE
D 05 17505-00-01







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REV	ECO NUMBER	DATE
1	1	10/7/83

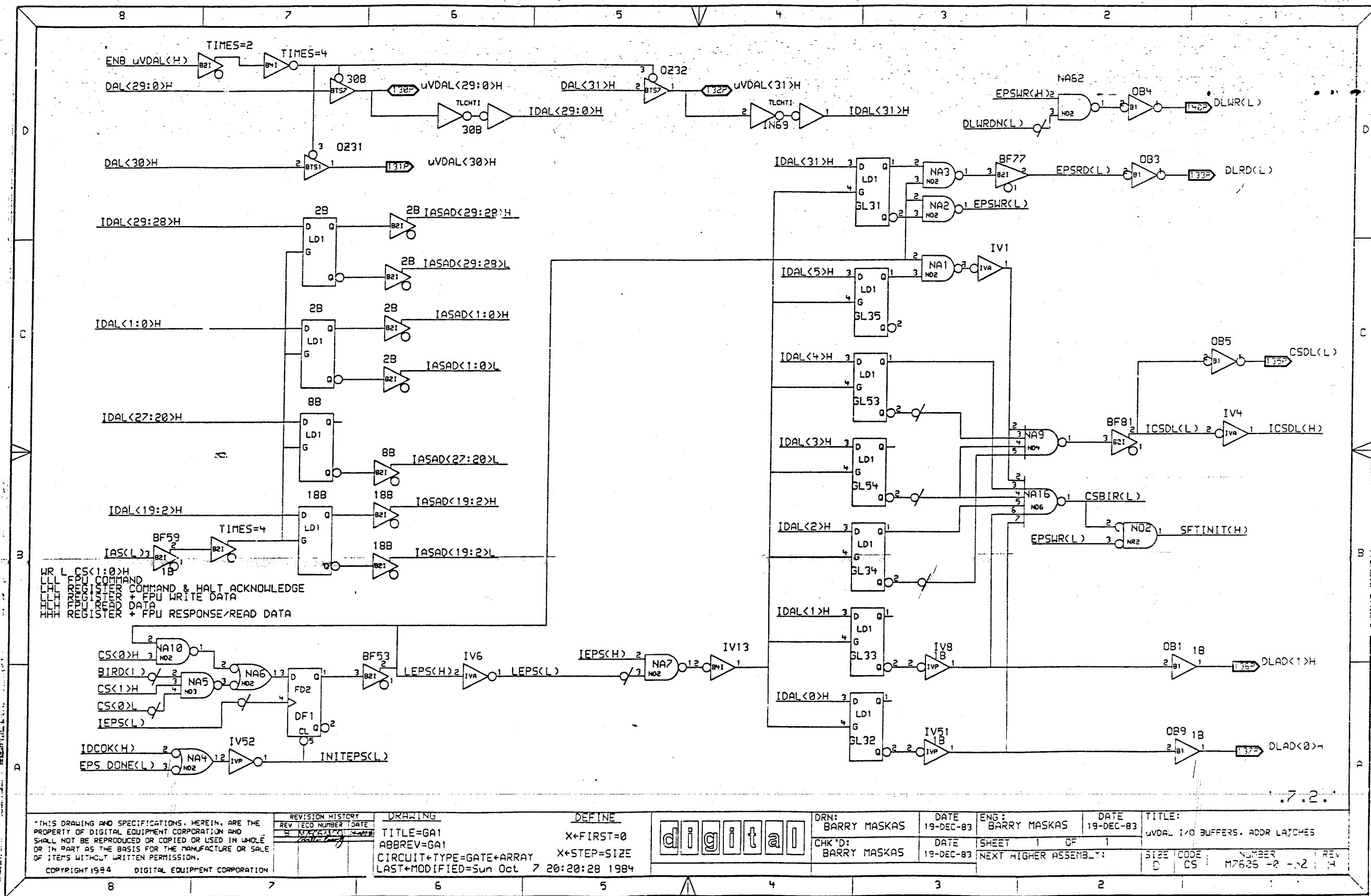
DRAWING
 TITLE=GAIBLOCK
 ABBREV=GAIBLOCK
 CIRCUIT+TYPE=GATE+ARRAY
 LAST+MODIFIED=Sun Oct 7 20:17:53 1984

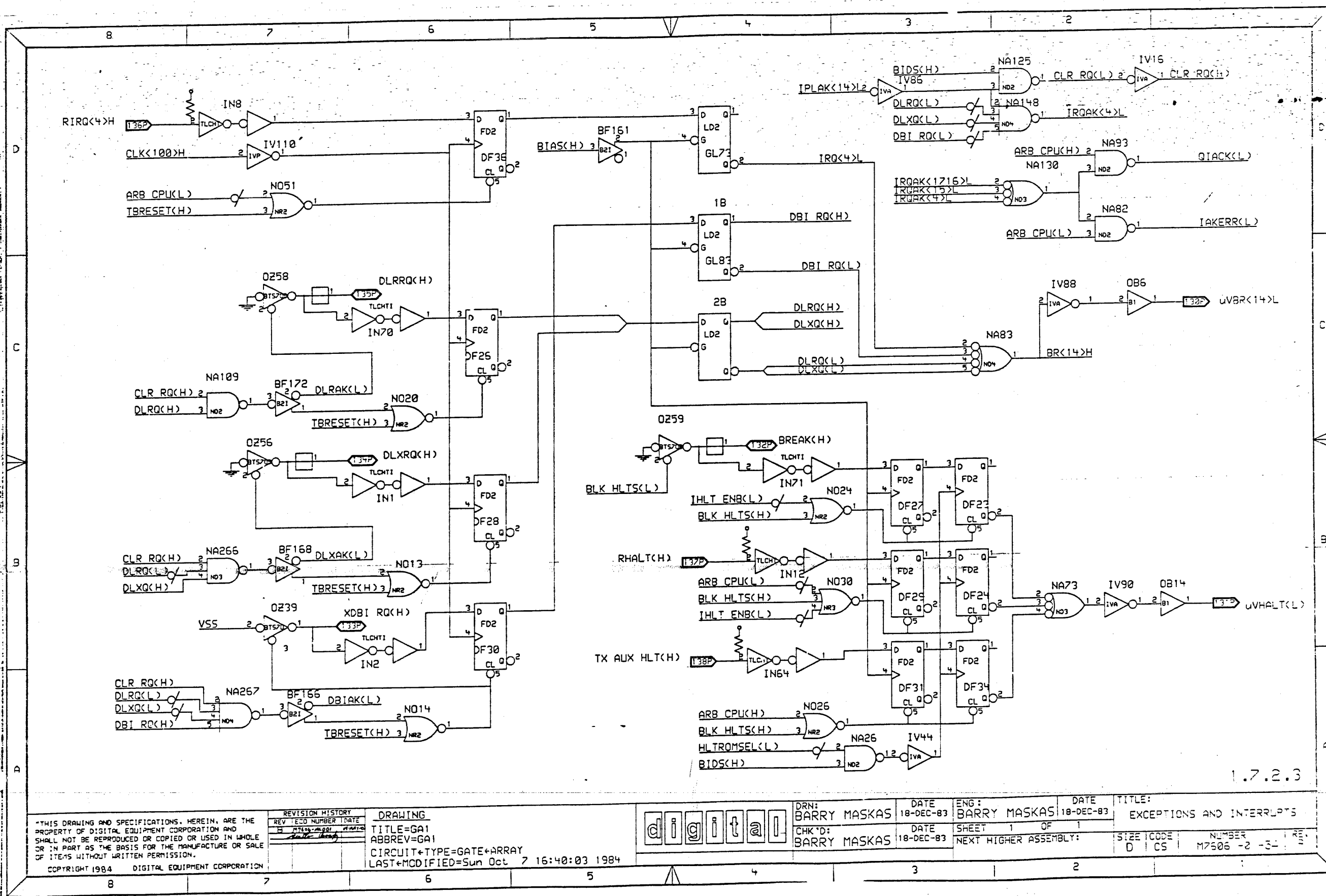
digital

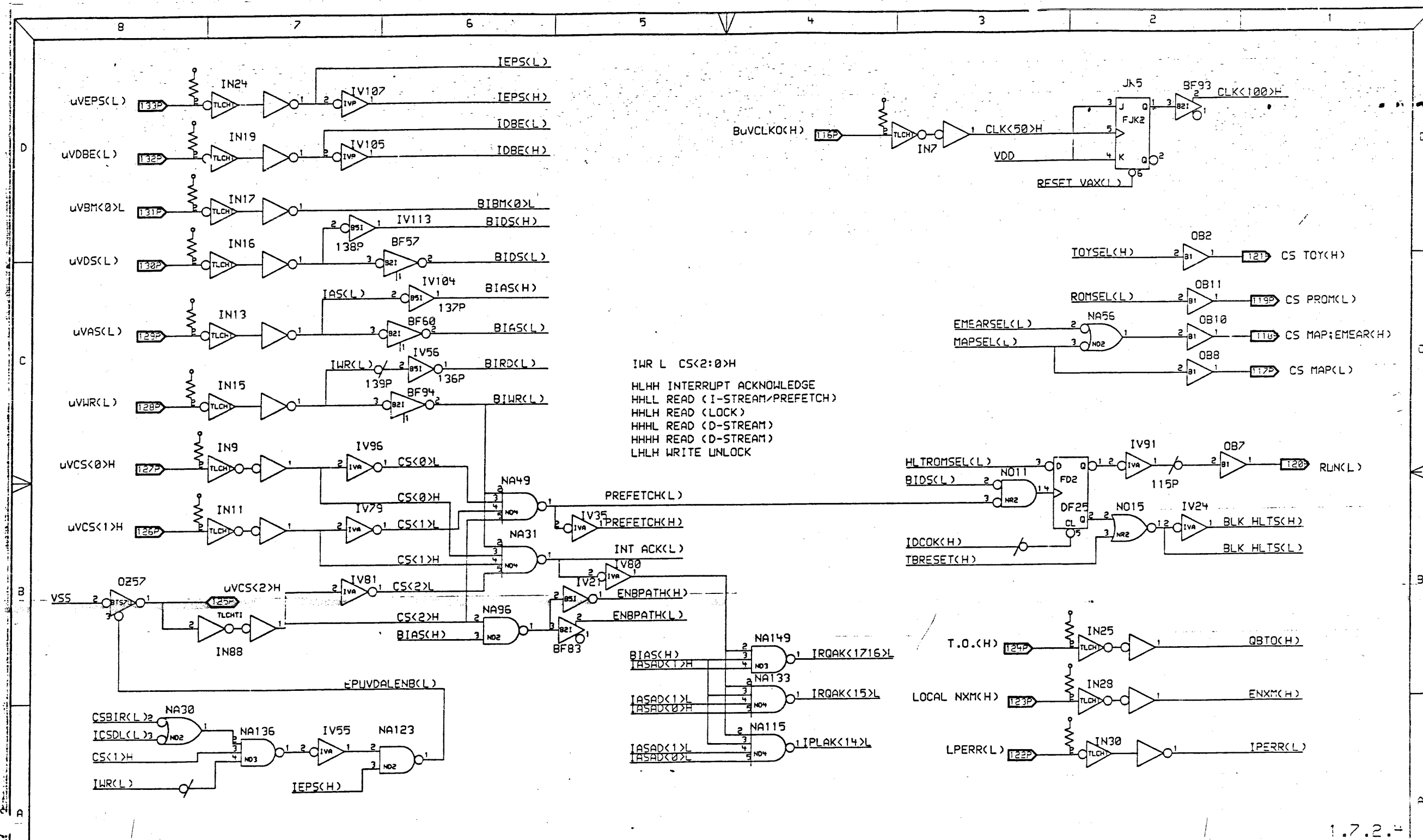
DRN: BARRY MASKAS
 CHK'D: BARRY MASKAS
 DATE: 15-DEC-83

ENG: BARRY MASKAS
 SHEET 1 OF 1
 NEXT HIGHER ASSEMBLY:

DATE: 15-DEC-83
 TITLE: Uvax Interface Gate Array
 Data Path
 SIZE: 10000
 M75000-10-1







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REVISION HISTORY		
REV	ECG NUMBER	DATE
1	17-001	11-11-83

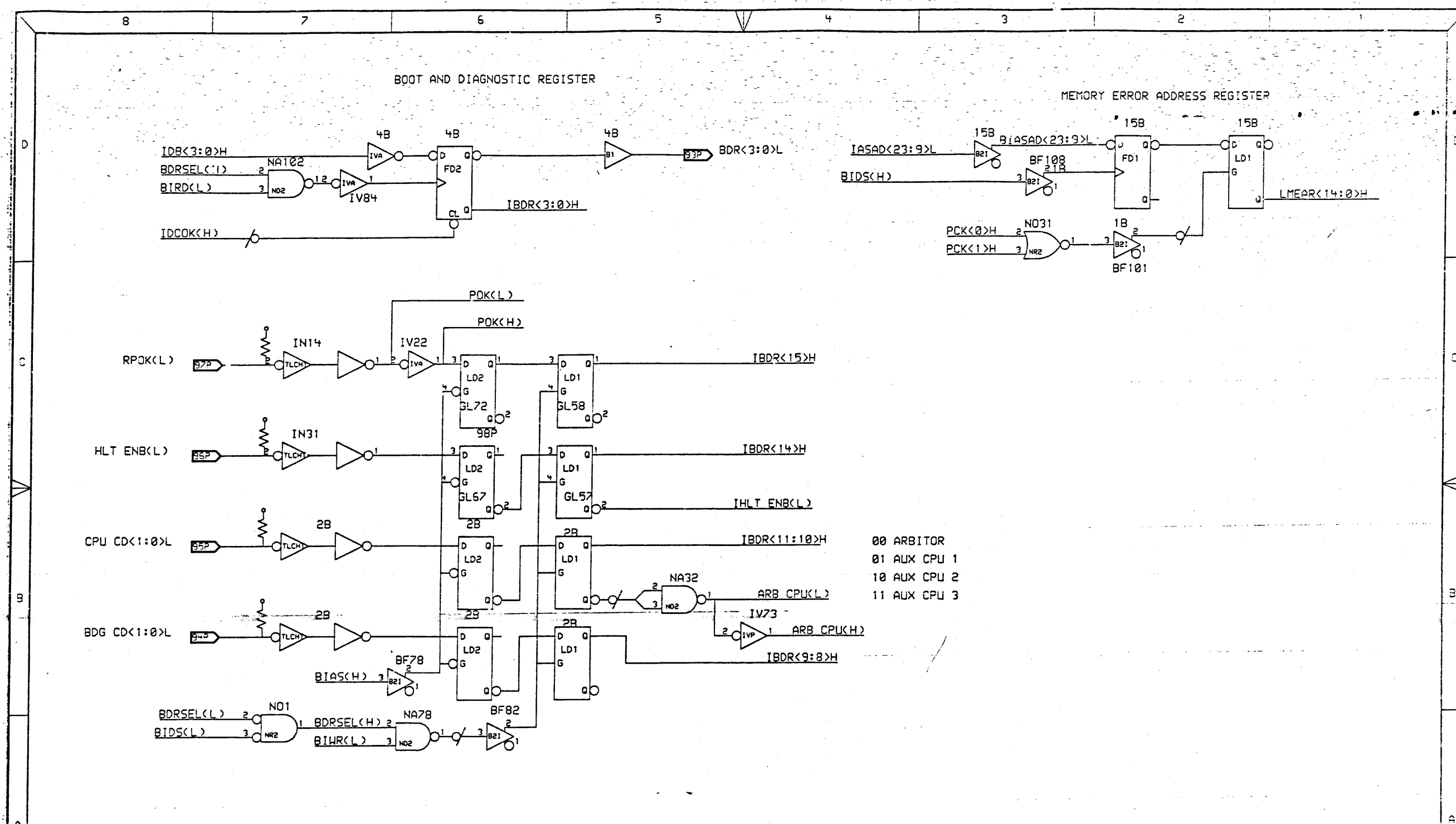
DRAWING
TITLE=GA1
ABBREV=GA1
CIRCUIT+TYPE=GATE+ARRAY
LAST+MODIFIED=Sat Oct 20 18:44:09 1984

digital

DRN: BARRY MASKAS
CHK'D: BARRY MASKAS
DATE: 19-DEC-83
DATE: 19-DEC-83

ENG: BARRY MASKAS
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

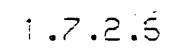
TITLE: VAX INPUTS AND I/O PINS
SIZE D CODE CS NUMBER M7635 -0 -35 REV 3



- 00 ARBITOR
- 01 AUX CPU 1
- 10 AUX CPU 2
- 11 AUX CPU 3

1.7.2.5

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					CHK'D: BARRY MASKAS DATE 18-DEC-83		SHEET 1 OF 1		NEXT HIGHER ASSEMBLY:	
							SIZE CODE 0 C5		NUMBER M7606 -0 -35	
									REV 1	

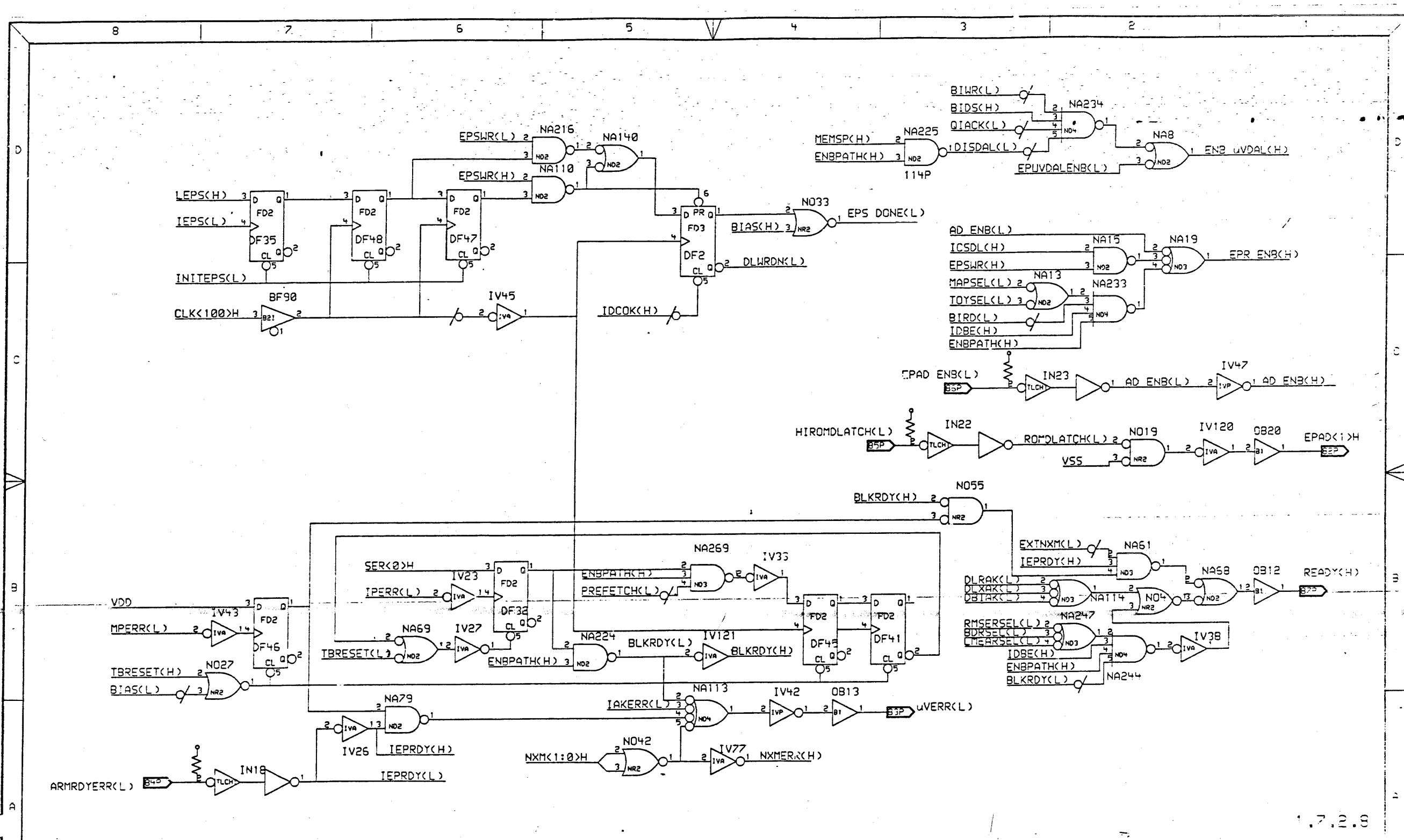


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TITLE=GA1
ABBREV=GA1
CIRCUIT+TYPE=GATE+ARRAY
LAST+MODIFIED=NOT WRITTEN

TITLE:			
EPR BUS, XDAL BUS			
SIZE	CODE	NUMBER	REV
	86		2

SIZE	CODE	NUMBER	REV
D	CS	M7626 -0 -37	3



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REVISION HISTORY		
REV	ECN	NUMBER DATE
1	1	11/18/83
2	1	12/15/83

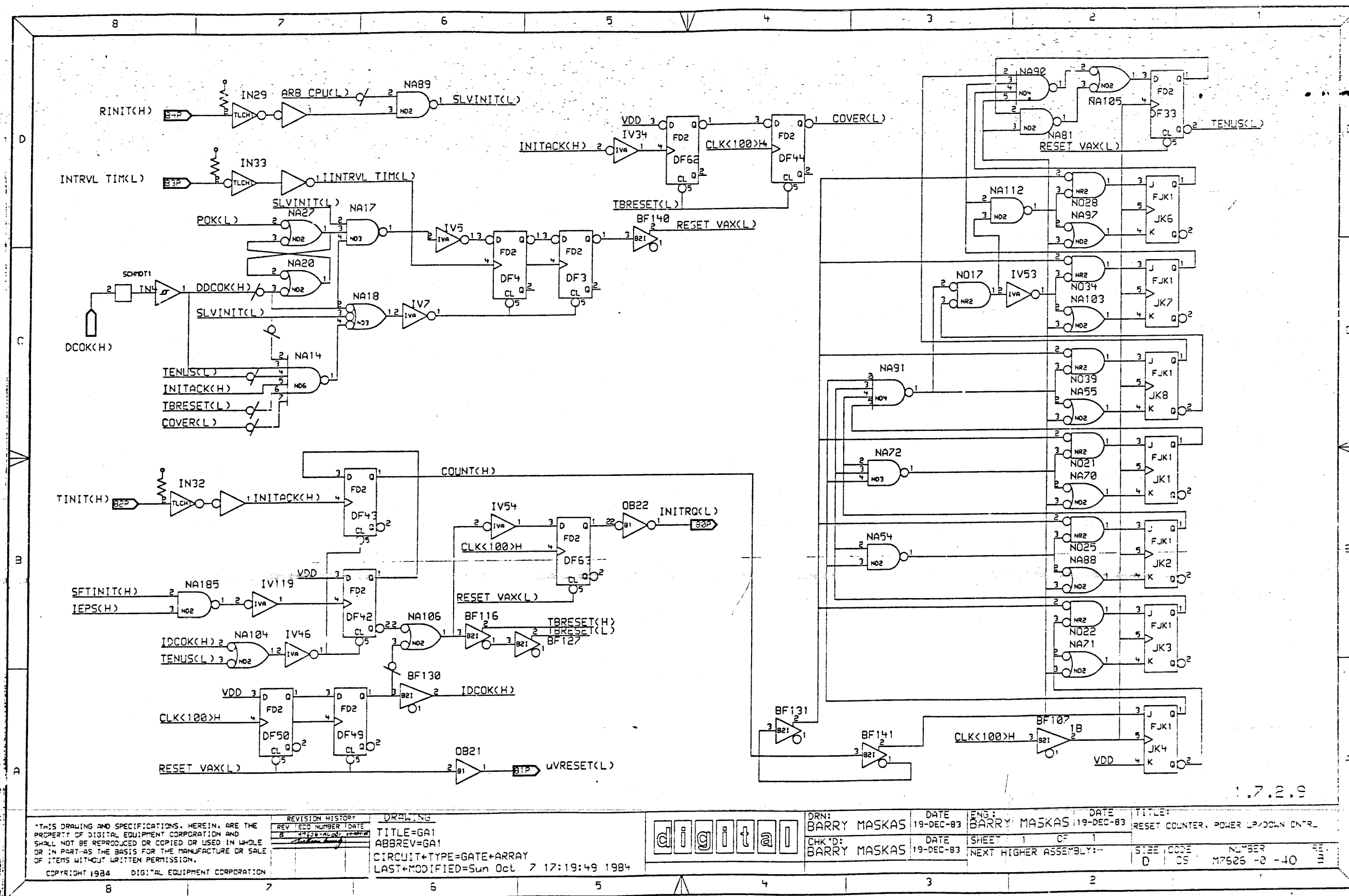
DRAWING
TITLE=GA:
ABBREV=GA:
CIRCUIT+TYPE=GATE+ARRAY
LAST+MODIFIED=Sun Oct 7 17:15:18 1984

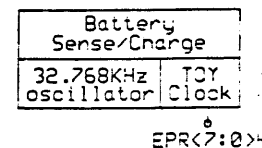
digital

DRN: BARRY MASKAS
CHK'D: BARRY MASKAS
DATE 19-DEC-83
DATE 19-DEC-83

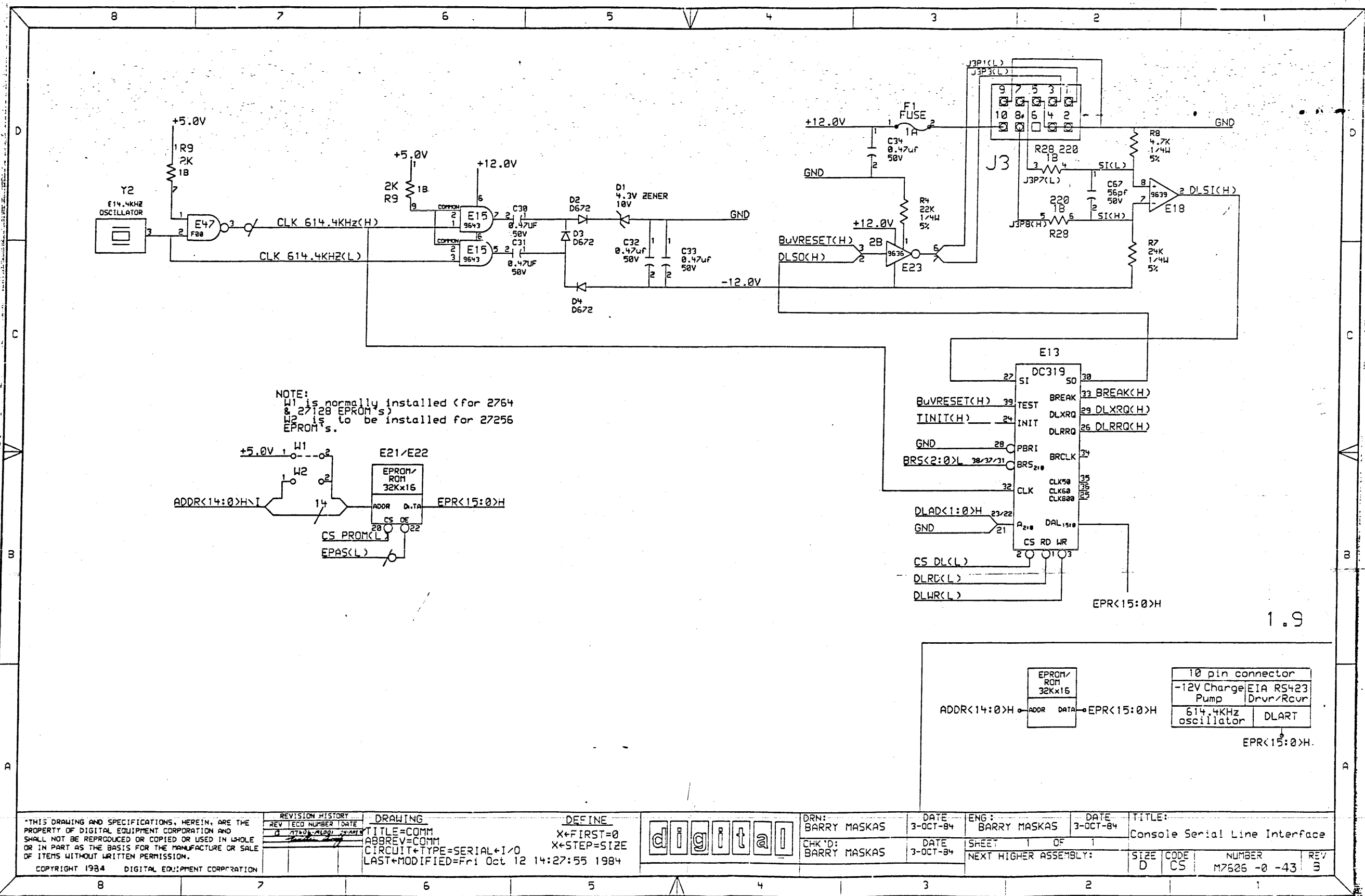
ENG: BARRY MASKAS
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

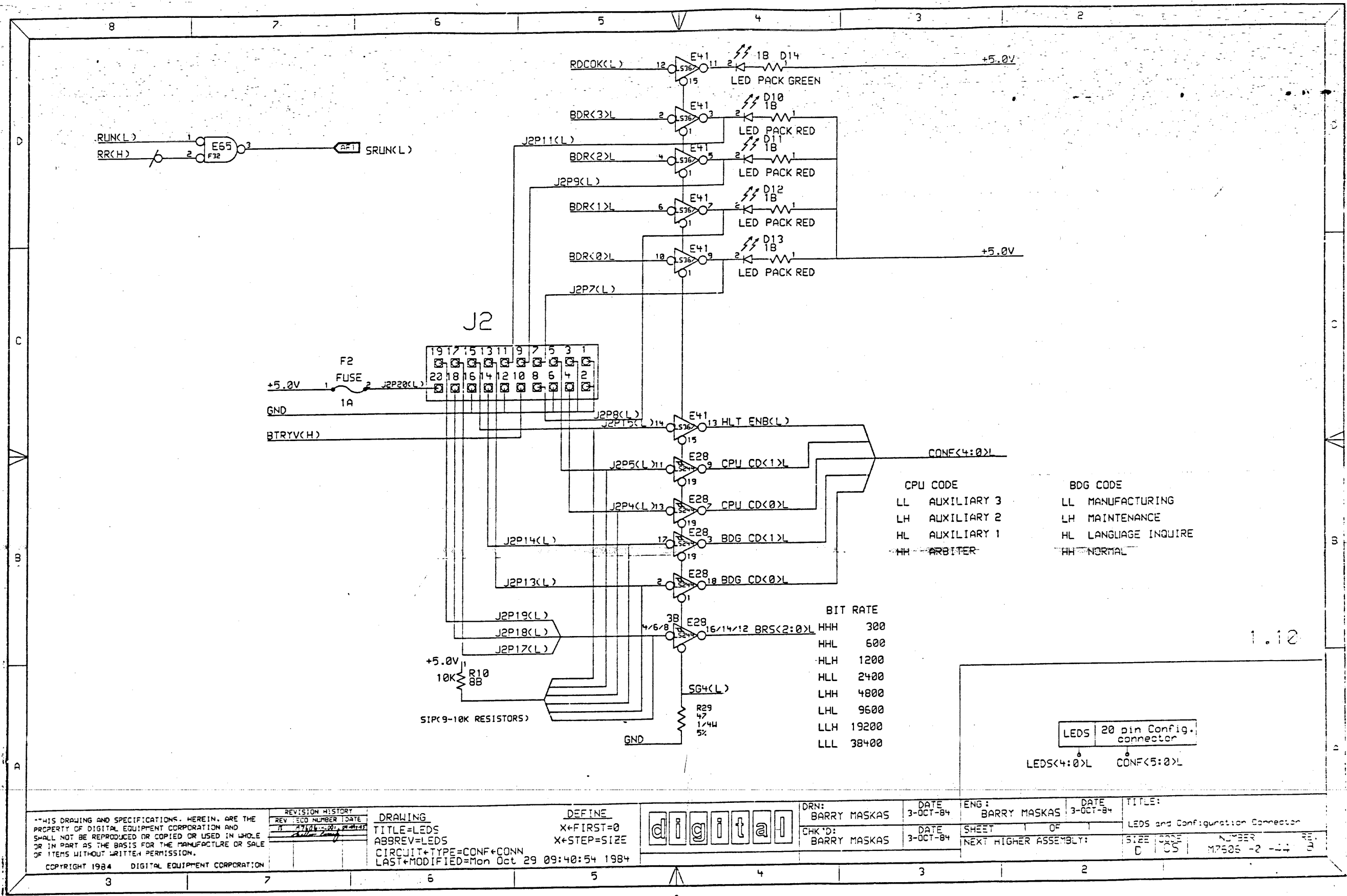
DATE 19-DEC-83
TITLE: MISC. CONTROL STROBES
SIZE CODE D CS
NUMBER 47626 -2 -09

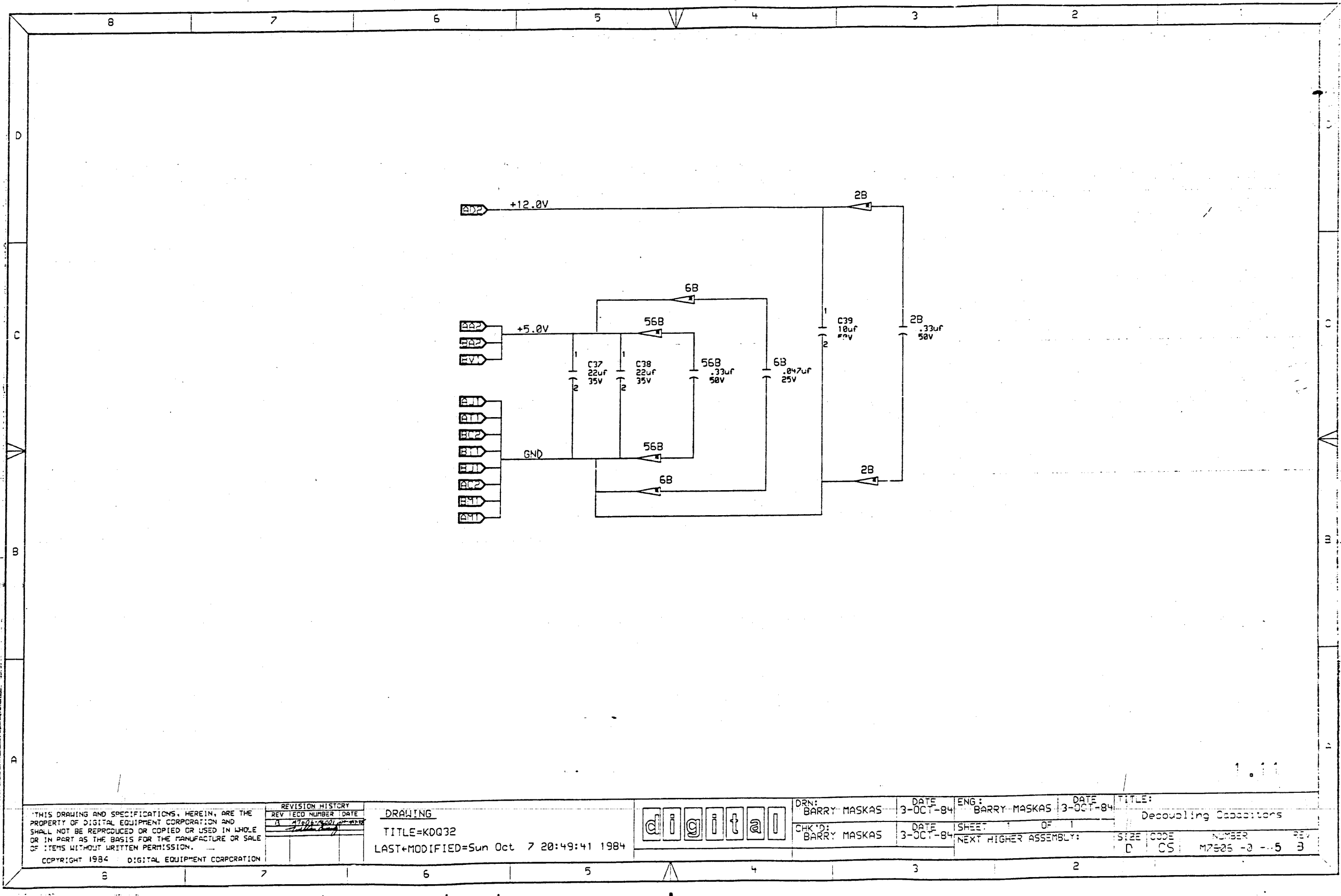




SIZE	CODE	QTY	UNIT	PRICE	TOTAL
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REVISION HISTORY		
REV	ECO NUMBER	DATE
01	17808-1-001	05-02-84

DRAWING
TITLE=KDQ32
LAST*MODIFIED=Sun Oct 7 20:49:41 1984



DRN: BARRY MASKAS
CHK'D: BARRY MASKAS

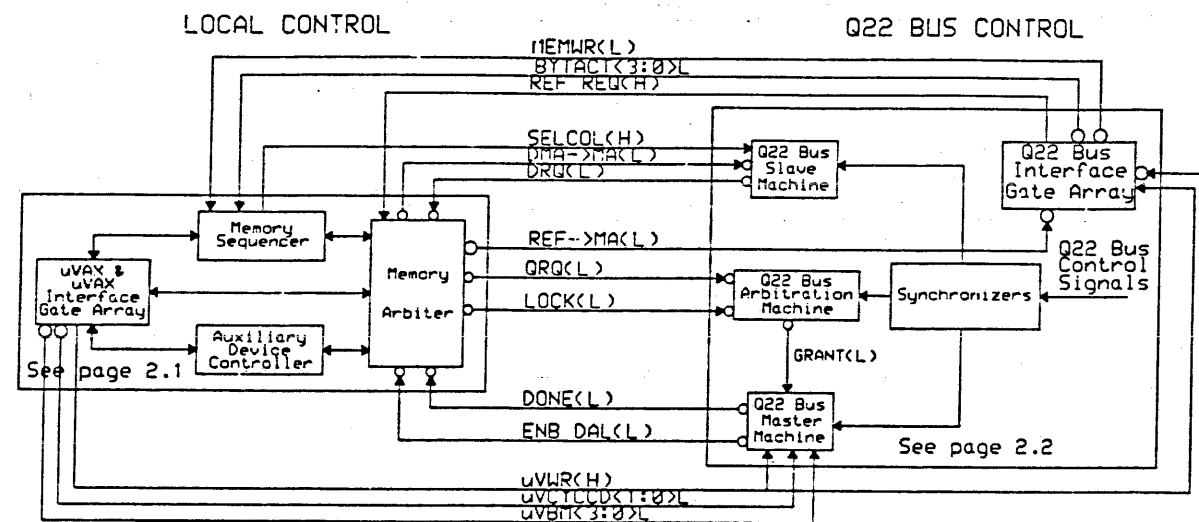
DATE 3-OCT-84
DATE 3-OCT-84

ENG: BARRY MASKAS
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY:

DATE 3-OCT-84

TITLE: Decoupling Capacitors

SIZE	CODE	NUMBER	REV
D	CS	M7525 -3 -5	3



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REVISION HISTORY		
REV	ECO NUMBER	DATE
0	1	8-DEC-83

DRAWING
TITLE=CTLBLOCK
ABBREV=ct1
CIRCUIT+TYPE=CONTROL
LAST*MODIFIED=Mon Oct 29 09:44:18 1984

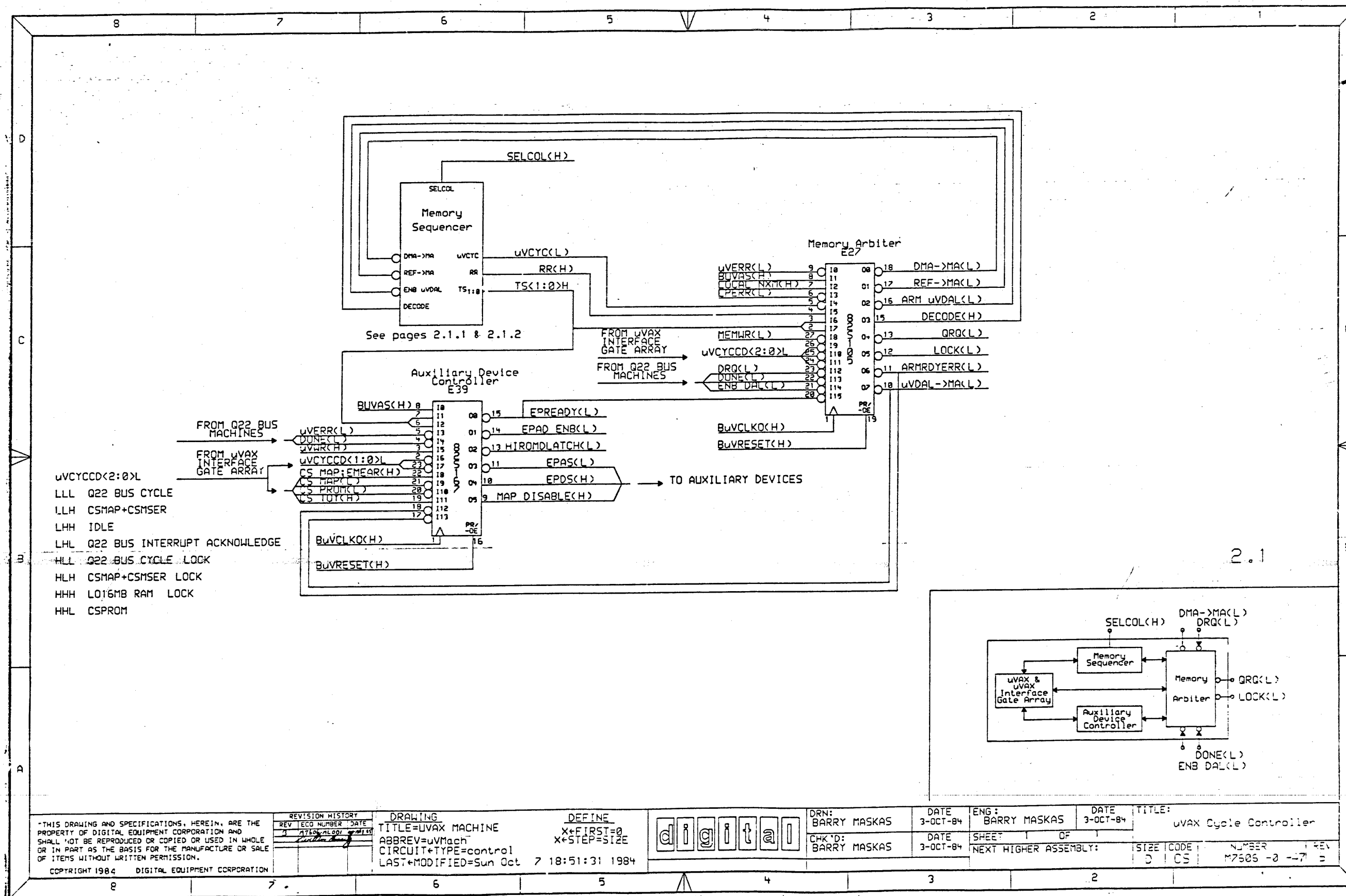
digital

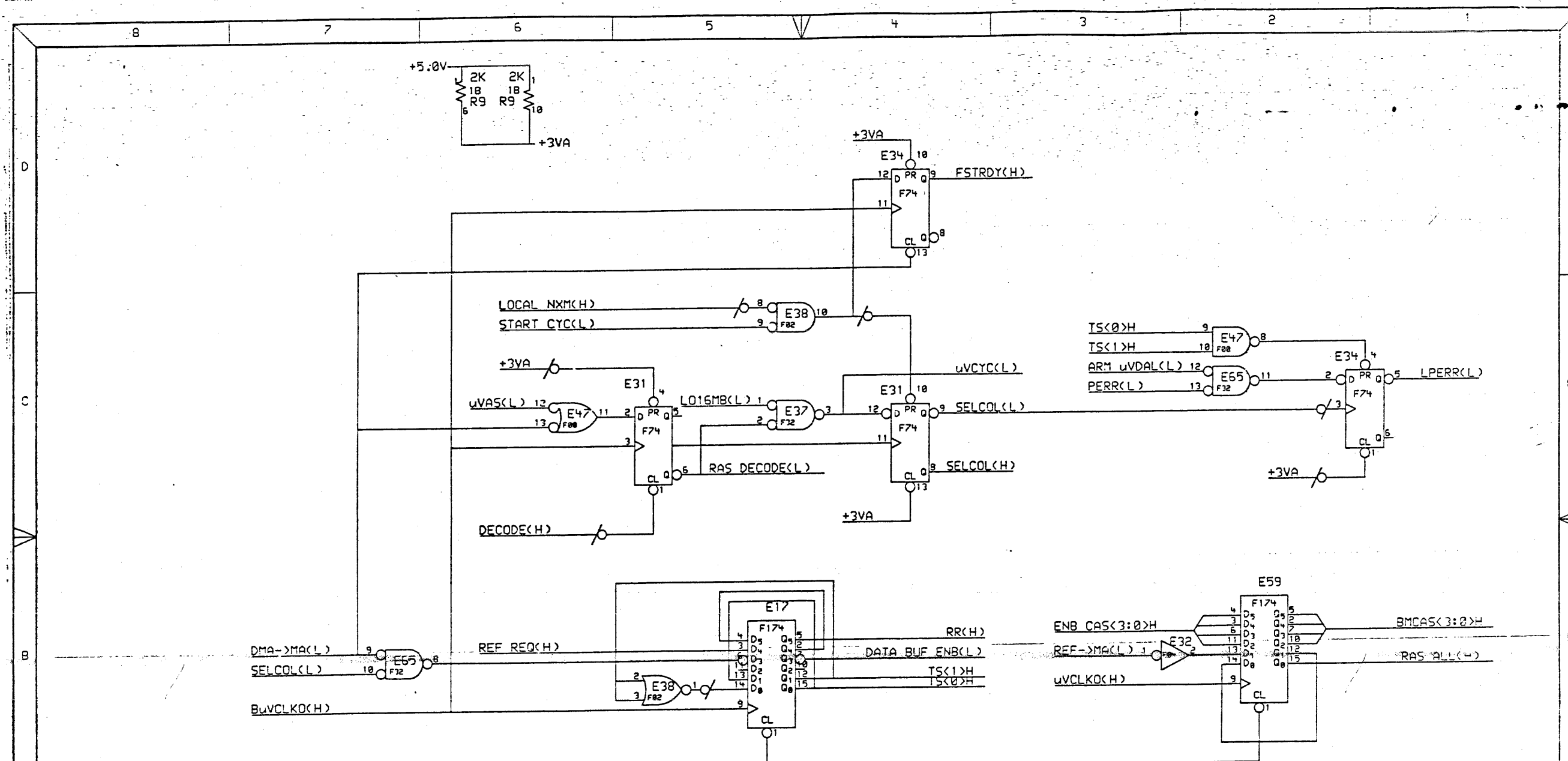
DRN:
BARRY MASKAS
CHK'D:
R. MCNAMARA

DATE
8-DEC-83
DATE
9-DEC-83

ENG:
BARRY MASKAS
SHEET 07
NEXT HIGHER ASSEMBLY:

TITLE:
KA630 State Machines
SIZE CODE NUMBER REV
1 CS M7505 -0 -46 3





TS<1:0> STATE TABLE (uVAX Microcycle Counter)

s	ns	
T4	00 01	first uVAX CLK0 after BuVRESET
T1	01 11	
T2	11 10	
T3	10 00	

2.1.1

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REV	ECO	NUMBER	DATE
1		1	10-DEC-83

DRAWING

TITLE=MEM CTRLR

ABBREV=MEMCTRL

CIRCUIT+TYPE=MEM+CTRL

LAST+MODIFIED=Fri Oct 12 14:23:21 1984

digital

DRN: BARRY MASKAS

CHK'D: BARRY MASKAS

DATE 18-DEC-83

DATE 18-DEC-83

ENG: BARRY MASKAS

SHEET 1 OF 1

NEXT HIGHER ASSEMBLY:

DATE 18-DEC-83

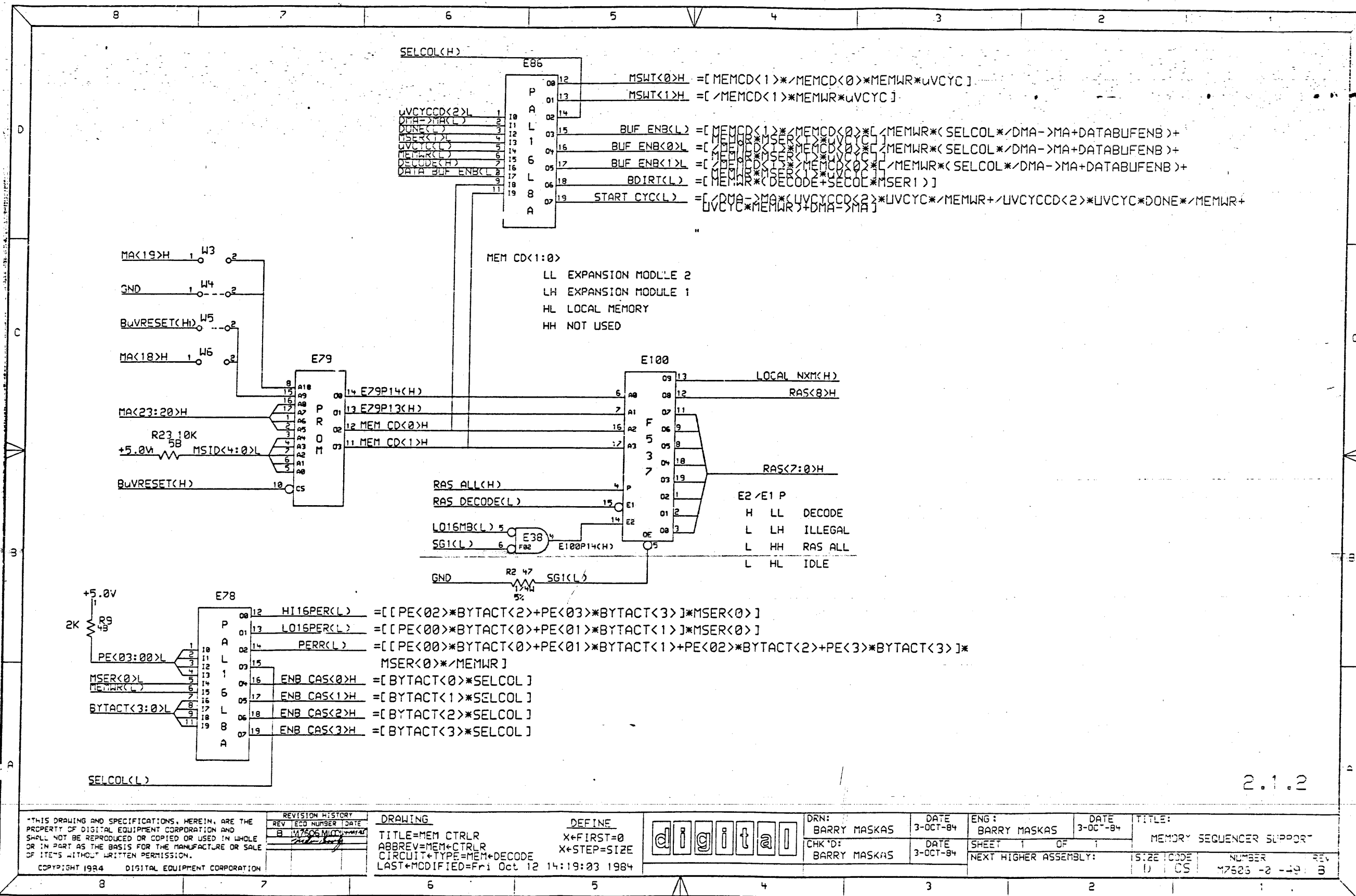
TITLE: MEMORY SEQUENCER

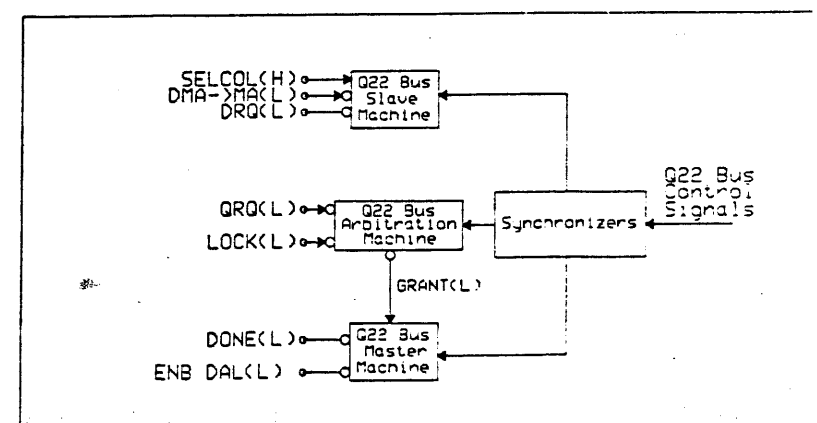
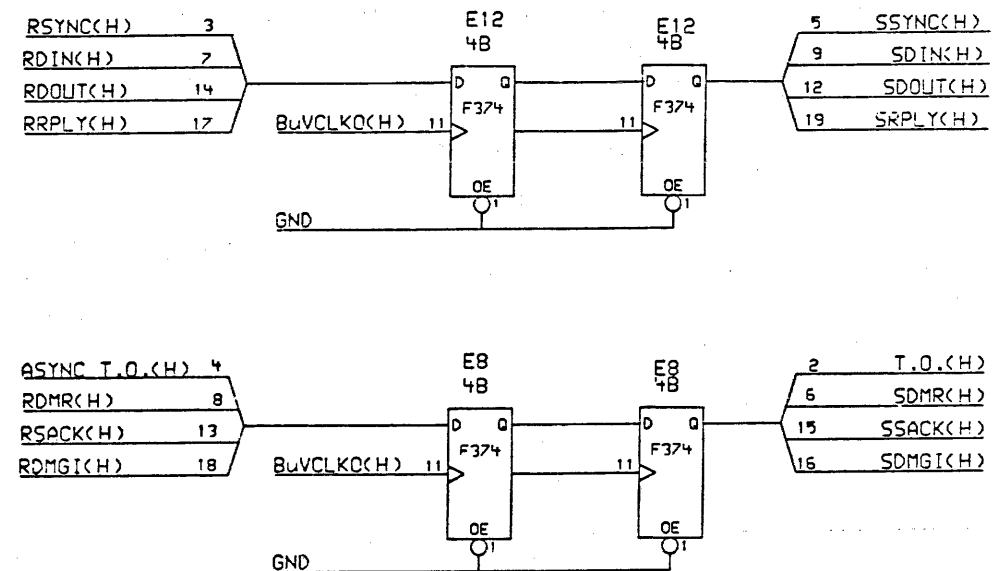
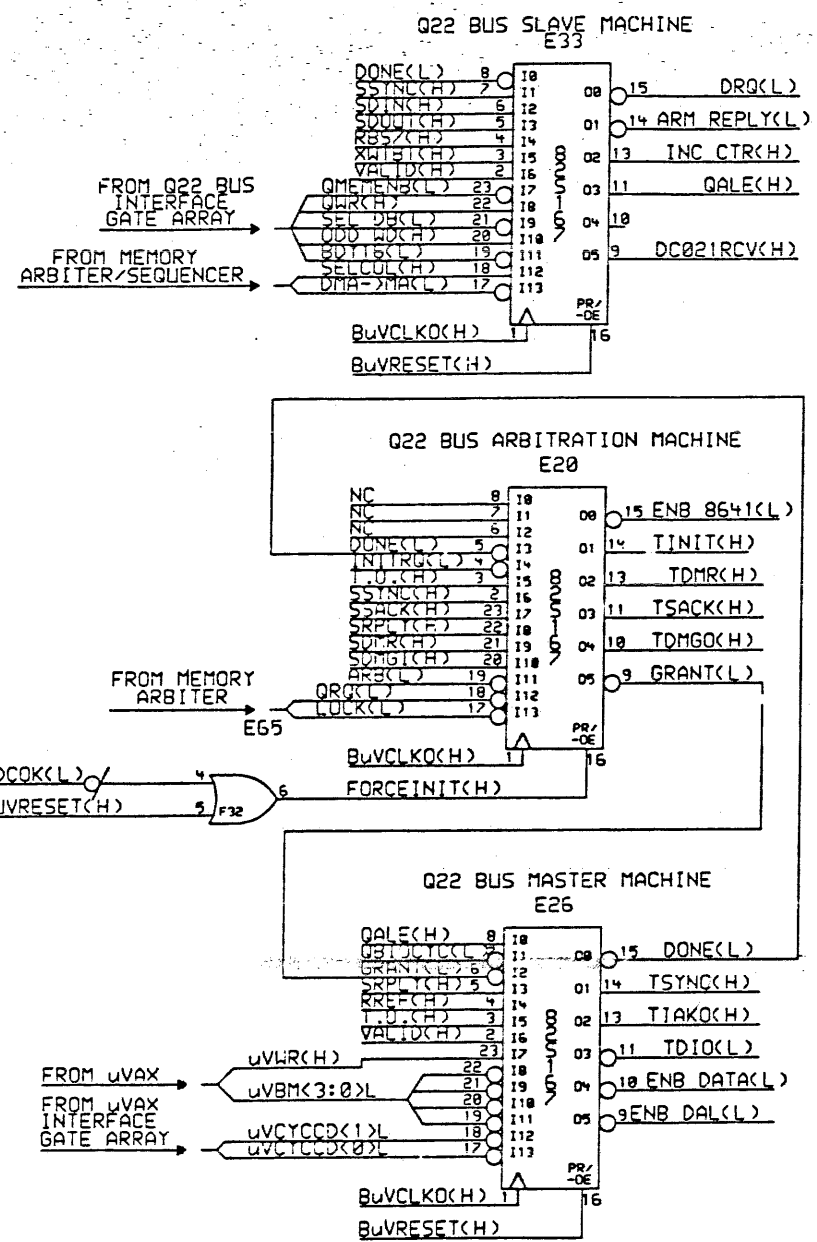
SIZE D

CODE CS

NUMBER M7635 -2 -43

REV B





NAME 23_053L1_00_E27 ;MEMORY ARBITER MACHINE
 OPTION PRESET
 INPUT -EPRDY, -ENBADR, -DONE, -DRQ, -CYCD0, -CYCD1, -CYCD2,
 INPUT -MEMWR, S0, S1, RR, -VCYC, -LPERR, LOCALNXM, BVAS, -VERR
 OUTPUT -DALMA, -RDYERR, -LOCK, -GRQ, DECODE, -ARMDAL, -REFMA, -DMAMA

LOCAL FLAG ;LOCK FLAG PREVENTS VAX FROM CHANGING MEMORY UNTIL Q22 BUS
 ;MASTERSHIP, Q22 BUS AND REFRESH CYCLES ARE ALLOWED.
 MACHINE 23_053L1_00_E27

STATE POWERUP ;LET REFRESH OR Q22 SLAVE THROUGH UNTIL Q22 BUS OWNED.
 IF [-DONE * S1 * -S0 * RR] THEN FINISHUP [-DMAMA, -DALMA, -DECODE,&
 -ARMDAL, -FLAG]
 IF [-DONE * DRQ] THEN FINISHUP [-DMAMA, -DALMA, -DECODE,&
 -ARMDAL, -FLAG]
 IF [DONE * -ENBADR * CYCD0] THEN VXT12 [RDYERR, ARMDAL] ;LOCAL MISS.
 IF [DONE * ENBADR * CYCD0] THEN Q22MT3 [-DALMA] ;LOCAL MISS AND GLOBAL HIT
 IF [DONE * CYCD1 * -CYCD0] THEN VXT12 [RDYERR] ;FINISH UP THE MAP CYCLE.

STATE SYNCUP, NUMBER -LLLLH
 GOTO FINISHUP [-DMAMA, -DALMA, -DECODE, -ARMDAL]

STATE FINISHUP, NUMBER -HLLH ;Q22 SLAVE CYCLES HAVE TOP PRIORITY, REFRESH
 IF [-LPERR * DRQ] THEN Q22SLAVE [DMAMA]
 IF [-LPERR * RR * -DRQ] THEN REFRESH [REFMA] ;LPERR IS FOR OPTIMIZATION.
 IF [FLAG * -RR * -DRQ] THEN VXT10 ;IF FLAGWAIT FOR DONE.
 IF [-S1 * -S0 * -EPRDY * -FLAG * -RR * -DRQ] THEN VXSLAVE [DALMA]

STATE REFRESH, NUMBER -HHHLH
 GOTO RT2

STATE RT2, NUMBER -LLLLL
 GOTO RT3

STATE RT3 ;WHEN REFRESH, RAS(8:0)H AND LOCALNXM(H) ASSERT.
 GOTO RT4

STATE RT4
 GOTO RT5 [-RDYERR]

STATE RT5
 GOTO RT6 [-REFMA]

STATE RT6
 GOTO SYNCUP [-DECODE, -RDYERR]

STATE Q22SLAVE, NUMBER -HLLH ;LOCAL MEMORY IS SLAVE TO Q22 BUS MASTER.
 GOTO RT2 [DECODE]

STATE VXSLAVE, NUMBER -HHLL ;LOCAL MEMORY IS SLAVE TO VAX.
 GOTO VXT3 [DECODE]

STATE VXT3, NUMBER -LHLL
 IF [S1 * -S0 * VCYC * BVAS * CYCD2 * -DONE] THEN READY [ARMDAL, RDYERR]
 IF [S1 * -S0 * VCYC * BVAS * -CYCD2 * -CYCD1 * -CYCD0 * DONE] THEN READY &
 [ARMDAL, RDYERR]
 IF [S1 * -S0 * VCYC * CYCD1 * CYCD0 * DONE * EPRDY] THEN READY [ARMDAL,&
 RDYERR]
 IF [S1 * -S0 * -BVAS * -VCYC] THEN FINISHUP [-DMAMA, -DALMA,&
 -DECODE, -ARMDAL] ;IF NOTHING TO DO GO LOOK FOR REFRESH OR Q22 SLAVE.
 IF [S1 * -S0 * VCYC * BVAS * -CYCD2 * -DONE] THEN RT4 [GRQ, LOCK, FLAG]
 IF [S1 * -S0 * -VCYC * BVAS] THEN VXT41 [-DECODE] ;WHAT CYCLE TO DO?

STATE VXT41 ;FIND OUT WHAT KIND OF CYCLE TO RUN?
 IF [-VCYC * CYCD2 * -CYCD1 * CYCD0] THEN Q22CYC [GRQ, -DALMA, ARMDAL]
 IF [-VCYC * -CYCD2 * -CYCD1 * CYCD0] THEN PROMCYC [RDYERR]
 IF [-VCYC * -CYCD1 * -CYCD0] THEN RT4
 IF [-VCYC * CYCD2 * CYCD1 * -CYCD0] THEN Q22CYC
 IF [-VCYC * -CYCD2 * CYCD1 * -CYCD0] THEN Q22CYC [LOCK]
 IF [-VCYC * CYCD2 * CYCD1 * CYCD0] THEN Q22CYC [ARMDAL]
 IF [-VCYC * -CYCD2 * CYCD1 * CYCD0] THEN Q22CYC [LOCK, ARMDAL]

STATE READY ;CYCLE IS RUNNING AND EPR MACHINE ASSERTS EPRDY
 IF [-S1 * S0] THEN VXT6 ;TO STROBE MEMCD(1:0) OR MSR WRITE DATA.

STATE VXT6 ;IF VAX ERROR PIN IS ASSERTED, EXTRA uCYCLE OCCURS.
 IF [VERR] THEN VXT9 [-LOCK, -GRQ] ;FREE Q22 BUS ON ERRORS OR MEMORY WRITES.
 IF [-VERR * MEMWR] THEN VXT7 [-GRQ, -DECODE, -RDYERR, -FLAG]
 IF [-VERR * MEMWR] THEN VXT7 [-LOCK, -DECODE, -RDYERR, -FLAG, -GRQ]

STATE VXT7, NUMBER -LLLH
 GOTO VXT8 [-DMAMA, -DECODE, -DALMA, -ARMDAL]

STATE VXT8, NUMBER -HLLH ;IF LOCAL PARITY ERROR THEN PROTECT MEMORY AND WAIT
 IF [LPERR] THEN VXTHOLD [-LOCK]
 IF [-LPERR * -RR * -DRQ] THEN VXSLAVE [DALMA] ;ALLOW Q22 IAKS.
 IF [-LPERR * RR * -DRQ] THEN REFRESH [REFMA] ;ALLOW REFRESH.
 IF [-LPERR * DRQ] THEN Q22SLAVE [DMAMA] ;ALLOW Q22 SLAVE.

STATE VXTHOLD ;FORCE VAX MACHINE CHECK, THEN ALLOW MEMORY CYCLES
 IF [BVAS * S1 * -S0 * -VERR] THEN VXT9

STATE VXT9 ;STALL ONE MICROCYCLE IF VAX SAW AN ERROR.
 IF [S1 * S0 * -LOCALNXM] THEN VXT7 [-DECODE, -RDYERR]
 IF [S1 * S0 * LOCALNXM] THEN RT3 [-DECODE, -GRQ, -RDYERR, -ARMDAL]

STATE VXT10 ;STALL UNTIL Q22 BUS MASTERSHIP THEN RUN READ LOCK CYCLE.
 IF [S1 * -S0 * DONE * BVAS * -CYCD2 * -CYCD1 * -CYCD0] THEN FINISHUP
 [-FLAG]
 IF [S1 * -S0 * -DONE * BVAS * -CYCD2 * -CYCD1 * -CYCD0] THEN FINISHUP
 IF [S1 * -S0 * -DONE * -BVAS] THEN VXT8 [-FLAG, -DMAMA, -GRQ, -DALMA,&
 -ARMDAL]

STATE PROMCYC ;EPR MACHINE RUNS A PROM CYCLE WHEN RDYERR IS ASSERTED.
 IF [EPRDY] THEN RT4 ;EPR MACHINE ASSERTS EPRDY WHEN PROM CYCLE IS DONE.

STATE VXT12 ;EPR MACHINE IS DOING THE SYNCING.
 IF [EPRDY] THEN READY [RDYERR] ;EPR MACHINE IS DONE AND VAX
 ;IS FINISHING.

STATE Q22CYC ;Q22 BUS DATA CYCLE OR IAK.
 IF [S1 * S0] THEN POWERUP [GRQ]

STATE Q22MT3 ;GET IN STEP AND GO RUN THE MISS-HIT CYCLE, TELL EPR MACHINE.
 IF [S1 * -S0] THEN Q22MT4 [DMAMA, RDYERR]

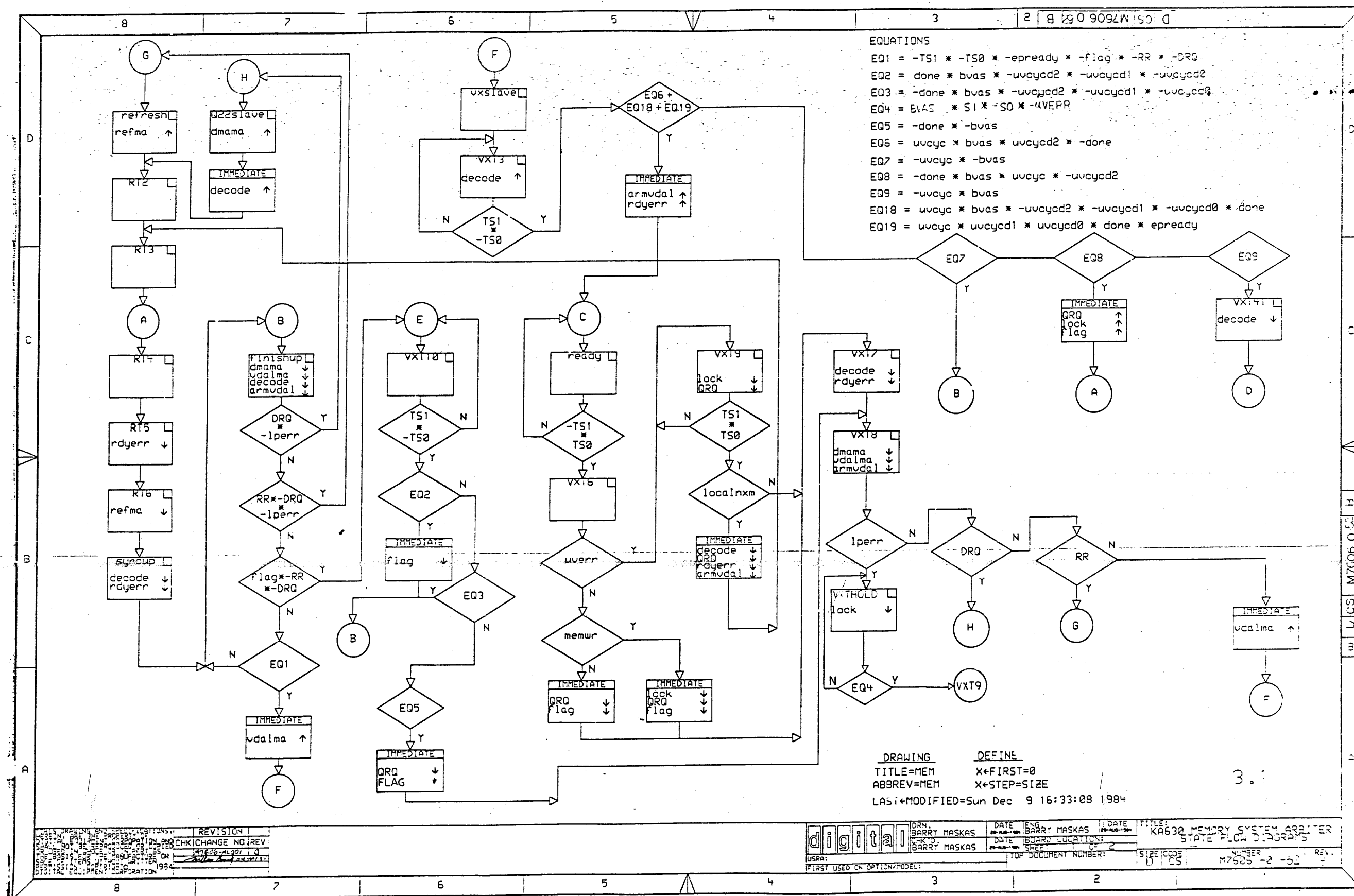
STATE Q22MT4, NUMBER -HLLH
 GOTO VXSLAVE

END

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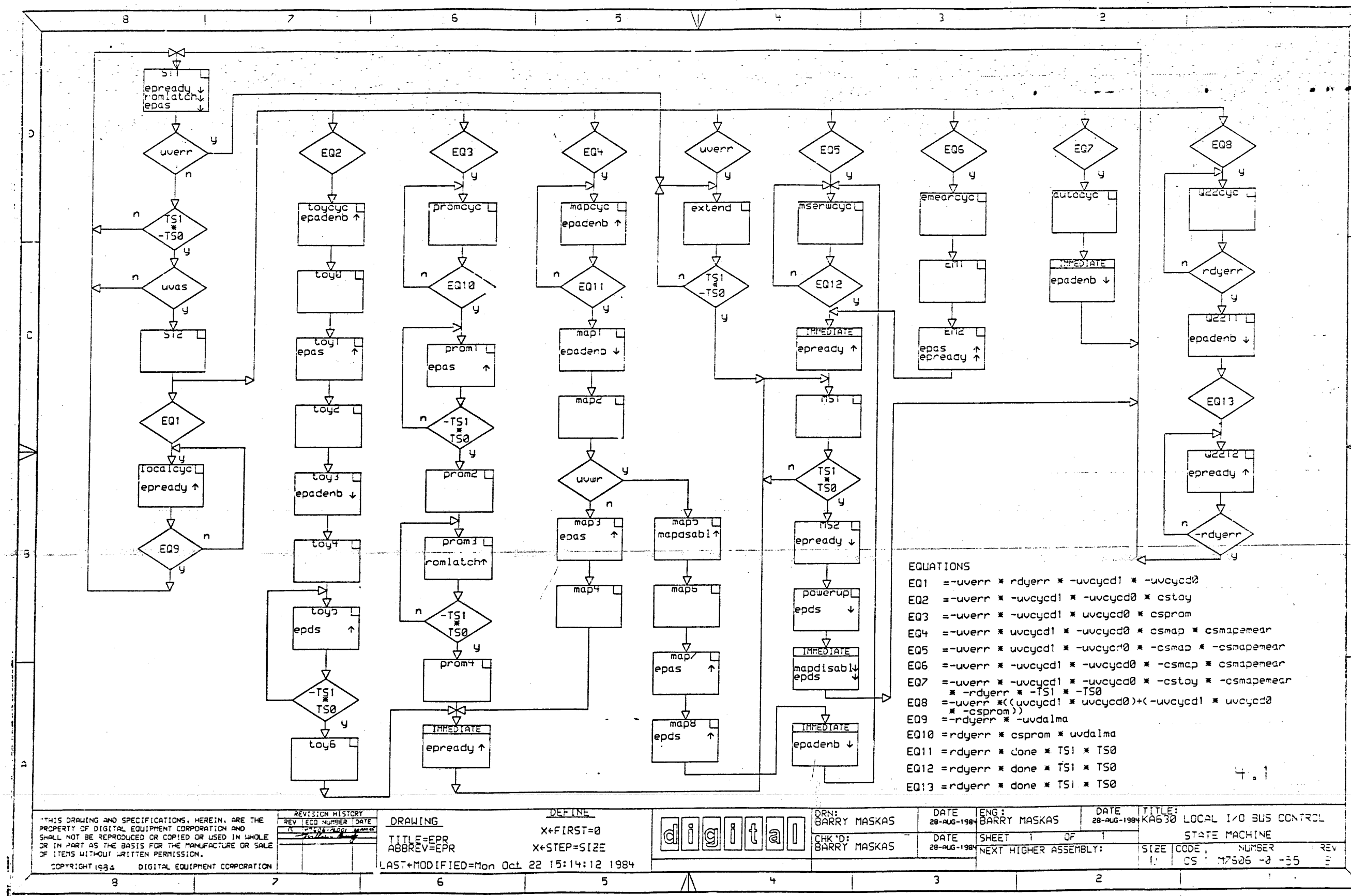
REVISIONS
 CHK CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE
	09-AUG-95	R. MCNAMARA	09-AUG-95	MEMORY ARBITER LISTING
CHK'D	DATE	BOARD LOCATION	SIZE	CODE
R. MCNAMARA	09-AUG-95	SHEET	D	CS
FIRST USED ON OPTION/MODEL:			NUMBER	REV.
			M7606-0-51	A



8	7	6	5	4	3	2	1
NAME 23-014L3-00 ;KA630 LOCAL I/O CONTROL MACHINE revision 2 OPTION PRESET INPUT NC1, -UVDALMA, -RDYERR, CSTOY, -CSPROM, -CSMAP, CSMAPEMEAR, INPUT -UVCYCD0, -UVCYCD1, NC0, UVWR, -DONE, -UVERR, TS0, TS1, BUVAS OUTPUT NC3, MAPDISABLE, EPDS, -EPAS, NC2, -HIROMDLATCH, -EPADENB, -EPREADY MACHINE 23-014L3-00 STATE POWERUP GOTO ST1 [-MAPDISABLE, -EPDS, -EPREADY, -EPAS, -HIROMDLATCH] STATE ST1, NUMBER LLHLL IF [TS1 * -TS0 * BUVAS * -UVERR] THEN ST2 IF [TS1 * -TS0 * UVERR] THEN EXTEND STATE ST2, NUMBER LLLHLL ;WHAT SHOULD RUN? IF Q22 BUS PUT ADDRESS ON EPR. IF [-UVERR * RDYERR * -UVCYCD1 * -UVCYCD0] THEN LOCALCYC [EPREADY] IF [-UVERR * -UVCYCD1 * -UVCYCD0 * CSTOY] THEN TOYCYC [EPADENB] IF [-UVERR * -UVCYCD1 * UVCYCD0 * CSPROM] THEN PROMCYC IF [-UVERR * UVCYCD1 * -UVCYCD0 * CSMAP * CSMAPEMEAR] THEN MAPCYC [EPADENB] IF [-UVERR * UVCYCD1 * -UVCYCD0 * -CSMAP * -CSMAPEMEAR] THEN MSERWCYC IF [-UVERR * -UVCYCD1 * -UVCYCD0 * -CSMAP * CSMAPEMEAR] THEN EMEARCYC IF [-UVERR * -UVCYCD1 * -UVCYCD0 * -CSTOY * -CSMAPEMEAR * -RDYERR * -TS1 * & -TS0] THEN AUTOCYC IF [-UVERR * (-UVCYCD1 * UVCYCD0) + (-UVCYCD1 * UVCYCD0 * -CSPROM)] THEN Q22CYC [EPADENB] IF [UVERR] THEN EXTEND STATE EXTEND, NUMBER LLHLL IF [TS1 * -TS0] THEN MS1 STATE LOCALCYC ;FAST READY FLEW BY BUT EPREADY ASSERTS TO STROBE ERRORS. IF [-RDYERR * -UVDALMA] THEN ST1 [-EPREADY, -HIROMDLATCH, -EPAS] STATE TOYCYC, NUMBER HLLHH ;TOY CLOCK CYCLE GOTO TOYT0 STATE TOYT0, NUMBER HLLHL ;ADDRESS SETUP TIME GOTO TOYT1 [EPAS] ;CHIP SELECT AND ADDRESS ARE STABLE SO GO TO IT. STATE TOYT1, NUMBER HLLHL GOTO TOYT2 ;ADDRESS HOLD TIME. STATE TOYT2, NUMBER HLLHL GOTO TOYT3 [-EPADENB] STATE TOYT3, NUMBER HLLHL GOTO TOYT4 ;PREVENT TRISTATE OVERLAP. STATE TOYT4, NUMBER HLLHL GOTO TOYT5 [EPDS] ;GET OR PUT THE DATA. STATE TOYT5, NUMBER LLHLLH ;DELAY FOR DATA ACCESS TIME. IF [-TS1 * TS0] THEN TOYT6 STATE TOYT6, NUMBER LLLLLL ;TELL UVAX THAT CYCLE CAN FINISH. GOTO MS1 [EPREADY] STATE PROMCYC ;PROM CYCLE IF [UVDALMA * CSPROM * RDYERR] THEN PROMT1 [EPAS] ;ADDRESS OK? STATE PROMT1 ;DELAY FOR ACCESS TIME. IF [-TS1 * TS0] THEN PROMT2 STATE PROMT2 ;DELAY FOR ACCESS TIME, STROBE HI 16 BITS INTO LATCH. GOTO PROMT3 [HIROMDLATCH] ;CHANGE ADDRESS TO LO 16 BITS. STATE PROMT3 ;DELAY FOR ACCESS TIME. IF [-TS1 * TS0] THEN PROMT4 STATE PROMT4, NUMBER LLLLLL ;TELL MEMORY MACHINE AND UVAX TO FINISH. GOTO MS1 [EPREADY] STATE MAPCYC ;MAP CYCLE IF [RDYERR * DONE * TS1 * TS0] THEN MAPT1 [-EPADENB] ;HAVE THE Q22 BUS? STATE MAPT1, NUMBER LLLHH GOTO MAPT2 ;PREVENT TRISTATE OVERLAP. STATE MAPT2, NUMBER LLLHLH ;READ OR WRITE CYCLE? IF [UVWR] THEN MAPT5 [MAPDISABLE] ;IF WRITE THEN 2 THE MAP OUTPUTS. IF [-UVWR] THEN MAPT3 [EPAS] ;IF READ THEN GET DATA ON EPR BUS. STATE MAPT3, NUMBER HLLHLH GOTO MAPT4 ;DELAY ONE TICK TO SYNC UP. STATE MAPT4, NUMBER HLLLLL ;TELL UVAX AND MEMORY MACHINE TO FINISH. GOTO MS1 [EPREADY] STATE MAPT5 GOTO MAPT6 ;PREVENT TRISTATE OVERLAP. STATE MAPT6 ;ENABLE THE WRITE DATA. GOTO MAPT7 [EPAS] STATE MAPT7 ;ASSERT THE WRITE STROBE. GOTO MAPT8 [EPDS] STATE MAPT8 ;SYNC UP WITH UVAX AND MEMORY MACHINE. GOTO MSERWCYC [-EPADENB] STATE MSERWCYC ;MSER WRITE CYCLE, GET THE Q22 BUS, STROBE THE DATA. IF [RDYERR * DONE * TS1 * TS0] THEN MS1 [EPREADY] STATE MS1, NUMBER LLLLLL ;STROBE THE MSER DATA WITH EPREADY. IF [TS1 * TS0] THEN MS2 [-EPREADY] STATE MS2 GOTO POWERUP [-EPDS] STATE EMEARCYC, NUMBER HHHHLH ;EXTERNAL MEAR CYCLE. GOTO EM1 STATE EM1, NUMBER HHHHLH ;ENABLE THE EPR DATA AND TELL UVAX TO FINISH. GOTO EM2 [EPAS, EPREADY] STATE EM2, NUMBER HHHHLH GOTO MS1 [EPREADY] STATE AUTOCYC ;NOTHING TO DO SO BACK FOR ANOTHER LOOK. GOTO ST1 [-EPREADY, -HIROMDLATCH, -EPAS, -EPADENB] STATE Q22CYC ;Q22 BUS IAK OR Q22 BUS READ OR WRITE. IF [RDYERR] THEN Q22T1 [-EPADENB] STATE Q22T1 IF [RDYERR * DONE * TS1 * TS0] THEN Q22T2 [EPREADY] STATE Q22T2 ;WAIT UNTIL MEMORY MACHINE IS DONE. IF [-RDYERR] THEN ST1 [-EPREADY, -HIROMDLATCH, -EPAS] END							
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REVISIONS		DATE		TITLE	
CHK	CHANGE NO.	REV	DATE	ENG.	LOCAL I/O CONTROL
			09-AUG-85	R. MCNAMARA	MACHINE LISTING
			DATE	BOARD LOCATION	
			09-AUG-85	SHEET 1 OF 1	
			DSK:8.12PC4.550	09-AUG-85 02:40	NEXT HIGHER ASSEMBLY:
			FIRST USED ON OPTION/MODEL:		
SIZE		CODE	NUMBER	REV.	
D	CS	M7606-0-54	A		



```

name 23_008L3_00_E20    ;Q22 bus arbitration control machine

input  nci15, -lock, -qrq, -arb, sdmgi, sdmr, srply, ssack,
input  ssync, nci6, t_o, -initrq, -done, nci2, nci1, nci0

output nco7, -grant, tdmgo, tsack, nci3, tdmr, tinit, -enb8641

machine q_bus_arb

state A
goto Q [-grant, -tdmgo, -tsack, -tdmr, tinit, -enb8641]

state B
if [-ssack * (arb + (-arb * sdmgi)) * (qrq + initrq) * (ssync + srply)] then D
if [-ssack * arb * qrq * -initrq * -ssync * -srply] then E [grant, -tdmr]
if [-ssack * arb * initrq * -ssync * -srply] then Q [tinit, -tdmr]
if [-ssack * ((arb * sdmr) + (-arb * sdmgi)) * -qrq * -initrq] then C [tdmgo]
if [-ssack * -arb * sdmgi * qrq * -initrq * -ssync * -srply] then E &
[grant, -tdmr, tsack]
if [-ssack * -arb * sdmgi * initrq * -ssync * -srply] then Q &
[tinit, -tdmr, tsack]
if [ssack * (qrq + initrq) + -arb * -sdmgi * (qrq + initrq)] then [tdmr]
if [ssack * -qrq * -initrq + -arb * -sdmgi * -qrq * -initrq + &
arb * -qrq * -initrq * -sdmr] then [-tdmr]

state C
if [arb * ssack] then B [-tdmgo, -tsack, -tinit, -grant, enb8641]
if [arb * -ssack * t_o] then F [-tdmgo]
if [-arb * -sdmgi] then B [-tdmgo, -tsack, -tinit, -grant, enb8641]

state D
if [-ssync * -srply * arb * qrq * -initrq] then E [grant, -tdmr]
if [-ssync * -srply * arb * -qrq * -initrq] then E [-grant, -tdmr]
if [-ssync * -srply * arb * initrq] then Q [tinit, -tdmr]
if [-ssync * -srply * -arb * qrq * -initrq] then E [grant, tsack, -tdmr]
if [-ssync * -srply * -arb * -qrq * -initrq] then E [-grant, tsack, -tdmr]
if [-ssync * -srply * -arb * initrq] then Q [tinit, tsack, -tdmr]

state E
if [-qrq * -lock] then B [-tdmgo, -tsack, -tinit, -grant, enb8641]
if [-qrq] then [-grant]
if [qrq] then [grant]

state F
goto H

state H
goto J

state J
goto K

state K
goto L

state L
goto M

state M
goto N

state N
goto P

state P
goto B [-tdmgo, -tsack, -tinit, -grant, enb8641]

state Q
if [-initrq] then B [-tdmgo, -tsack, -tinit, -grant, enb8641]

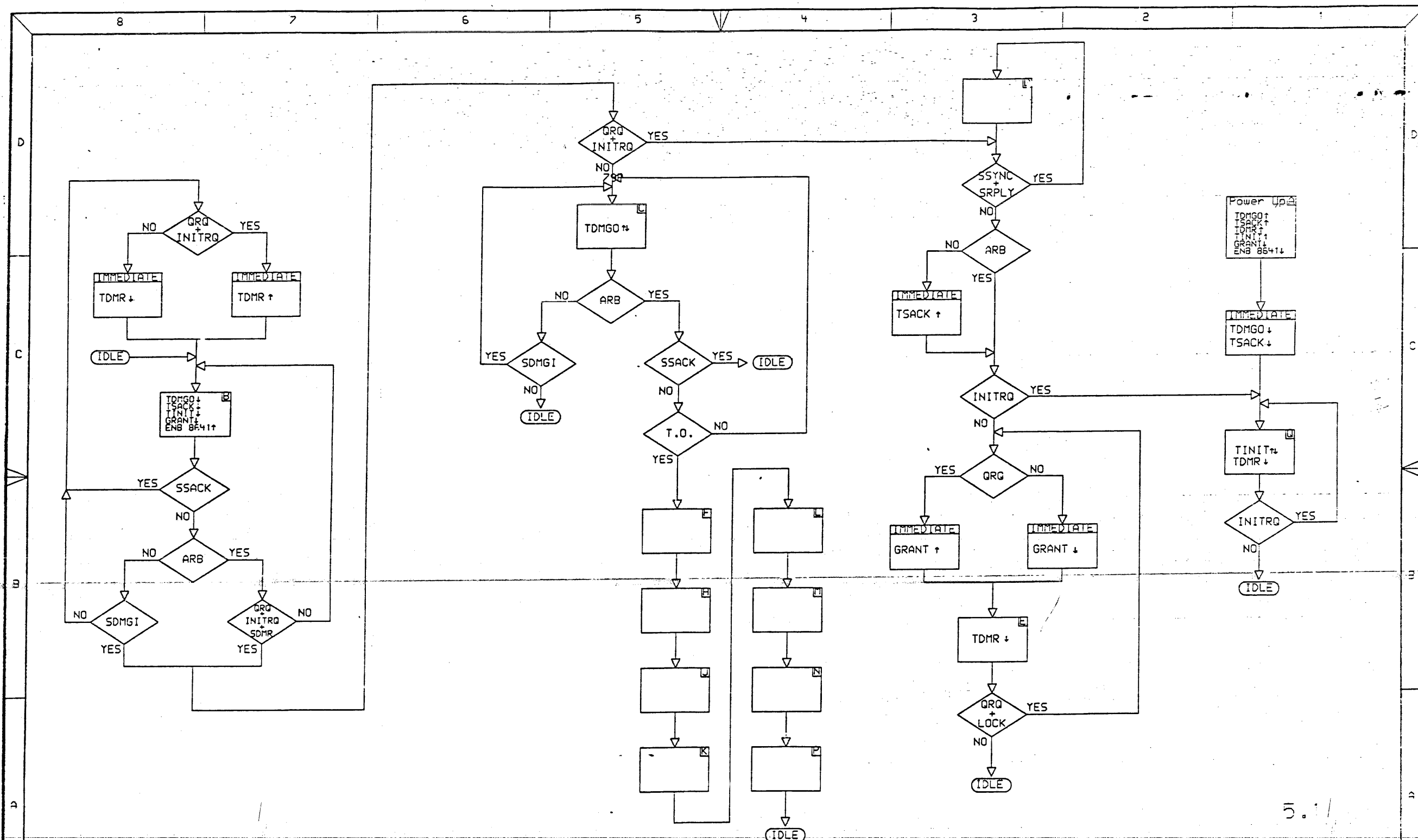
END

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE: Q22 BUS ARB. CTL MACHINE LISTING
	09-AUG-95	R. MCNAMARA	09-AUG-95	
CHK'D	DATE	BOARD LOCATION	OF	
R. MCNAMARA	09-AUG-95	SHEET	1	
DSK:5.12P(4,558)	109-AUG-95 07:39	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:			D CS	M7606-0-56
			REV.	A



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REVISION HISTORY		
REV	ECO NUMBER	DATE
1	178-114-001	01-07-91
<i>William J. King</i>		

DRAWING
TITLE=ARB
ABBREV=arb

LAST+MODIFIED=Mon Oct 22 15:04:38 1984

DEFINE

```
X ← FIRST = 0
X ← STEP = SIZE
```

digital

DRN:	R. McNamara
CHK'D:	R. McNamara

DATE
6-Jul-84

DATE
6-Jul-84

ENG:	R. McNamara
SHEET	1 OF 1
NEXT HIGHER ASSE	

DATE
6-Jul-84

TITLE:		
QBUS ARBITRATION CONTROLLED		
DETAILED CONTROL FLOW DIAGRAM		
SIZE	CODE	NUMBER

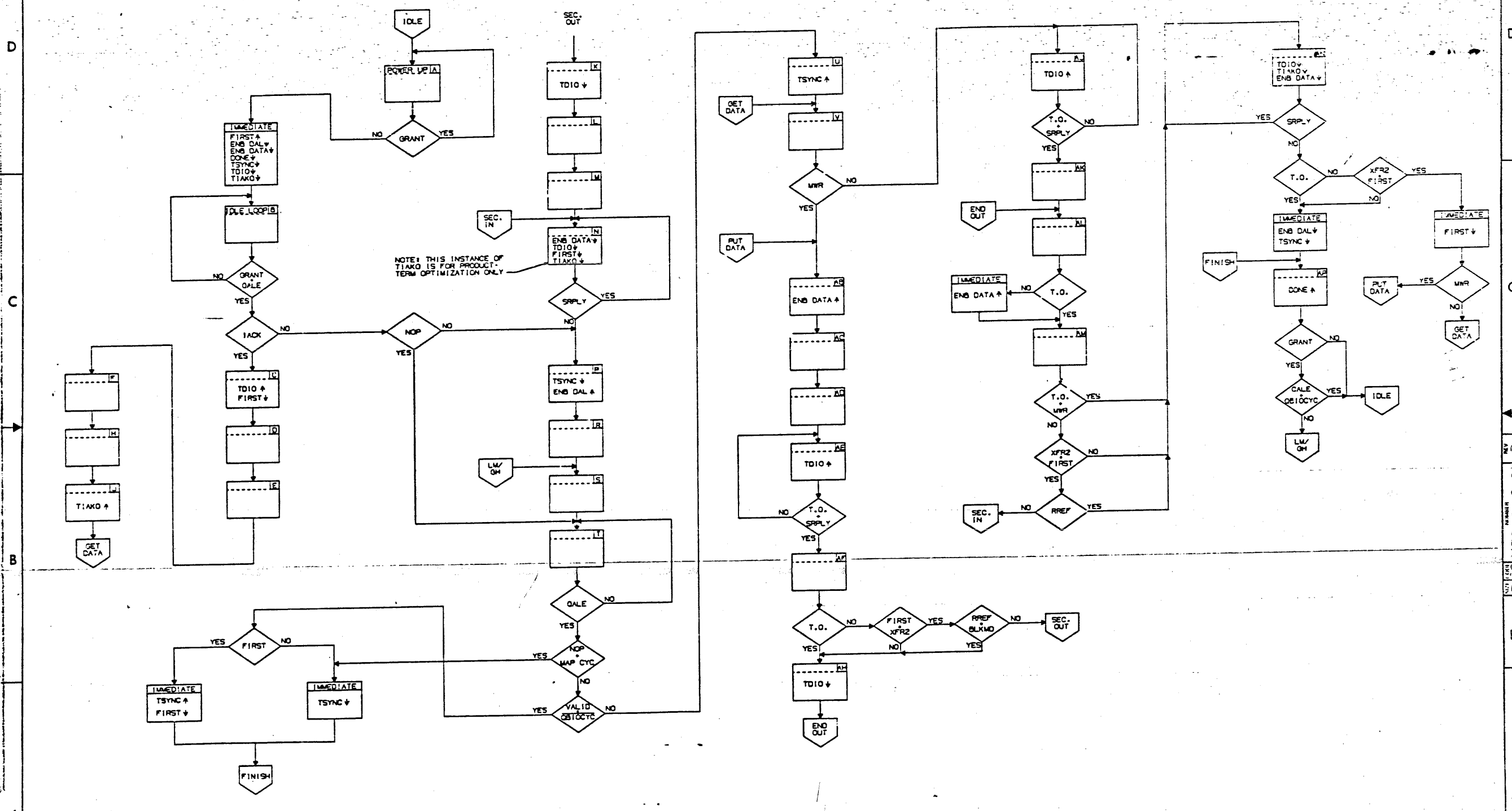
SIZE	CODE	NUMBER
D	CS	M7525 -2 -57

4000

15.

8	7	6	5	4	3	2	1
name 23_009L3_00_E26 ;Q22 bus master cycle control machine input nci15, -cd0, -cd1, -bm0, -bm1, -bm2, -bm3, uVWR input valid, nci6, t_o, rref, srply, -grant, -qbiocyc, qale output nco7, -EnbDal, -EnbData, -tdio, nco3, tiako, tsync, -done local first machine q_bus_master state A, number -hhhhh if [-grant] then B [first, -EnbDal, -EnbData, -done, -tdio, -tiako, -tsync] state B, number -hhhlh if [grant * qale * (-cd1)] then P [EnbDal] if [grant * qale * (-cd1 * -cd0)] then I if [grant * qale * (-cd1 * cd0)] then C [tdio, -first] state C, number -hlhlh goto D state D, number -llhlh goto E state E, number -hlhjh goto F state F, number -lllhj goto H state H, number -hlhlh goto J [tiako] state J, number -lhhll goto V state K, number -hlhlh goto L state L, number -hhlll goto M state M, number -lhlhl goto N [-EnbData, -tdio, -first] state N, number -lhlhl if [-srply] then P [EnbDal, -tsync] state P, number -hlhlh goto R state R, number -llhlh goto S state S, number -hlhhh goto T state T, number -llhhh if [qale * (-cd0 + (valid * qbiocyc))] & then AP [done] if [qale * cd0 * (-valid + qbiocyc)] & then U [tsync] if [qale * cd0 * valid * -qbiocyc * -first] then [-tsync] if [qale * cd0 * valid * -qbiocyc * first] then [tsync, -first] state U, number -hlhlh goto V state V, number -llhlh if [uVWR] then AB [EnbData] if [-uVWR] then AJ [tdio, -EnbDal] state AJ, number -hhhlh if [t_o + srply] then AK state AK, number -hlhlh goto AL state AL, number -llhlh goto AM if [-t_o] then [EnbData] state AM, number -hlhlh if [-t_o * -uVWR * (bm2*bm1) * first * -rref] then N & [-EnbData, -tdio, -tiako, -first] if [t_o + uVWR + -(bm2*bm1) + -first + rref] then AN & [-EnbData, -tdio, -tiako] state AN, number -llhlh if [-srply * -t_o * first * (bm2*bm1) * -uVWR] & then V [-first] if [-srply * -t_o * first * (bm2*bm1) * uVWR] & then AB [-first, EnbData] if [-srply * (t_o + -(bm2*bm1) + -first)] & then AP [-tsync, done, -EnbDal] state AP, number -hlhlh if [grant * -qbiocyc * -qale] then S if [-grant + qbiocyc + qale] then A state AB, number -hhhlh goto AC state AC, number -lhhhl goto AD state AD, number -hhhlh goto AE [tdio] state AE, number -hhhlh if [t_o + srply] then AF state AF, number -hlhlh if & [-t_o * first * (bm2*bm1) * -(uVWR + bm0 * bm3) * rref]] & then K [-tdio] if & [t_o + -first + -(bm2*bm1) + rref * (uVWR + bm0 * bm3)] & then AH [-tdio] state AH, number -llhlh goto AL END							
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8	7	6	5	4	3	2	1

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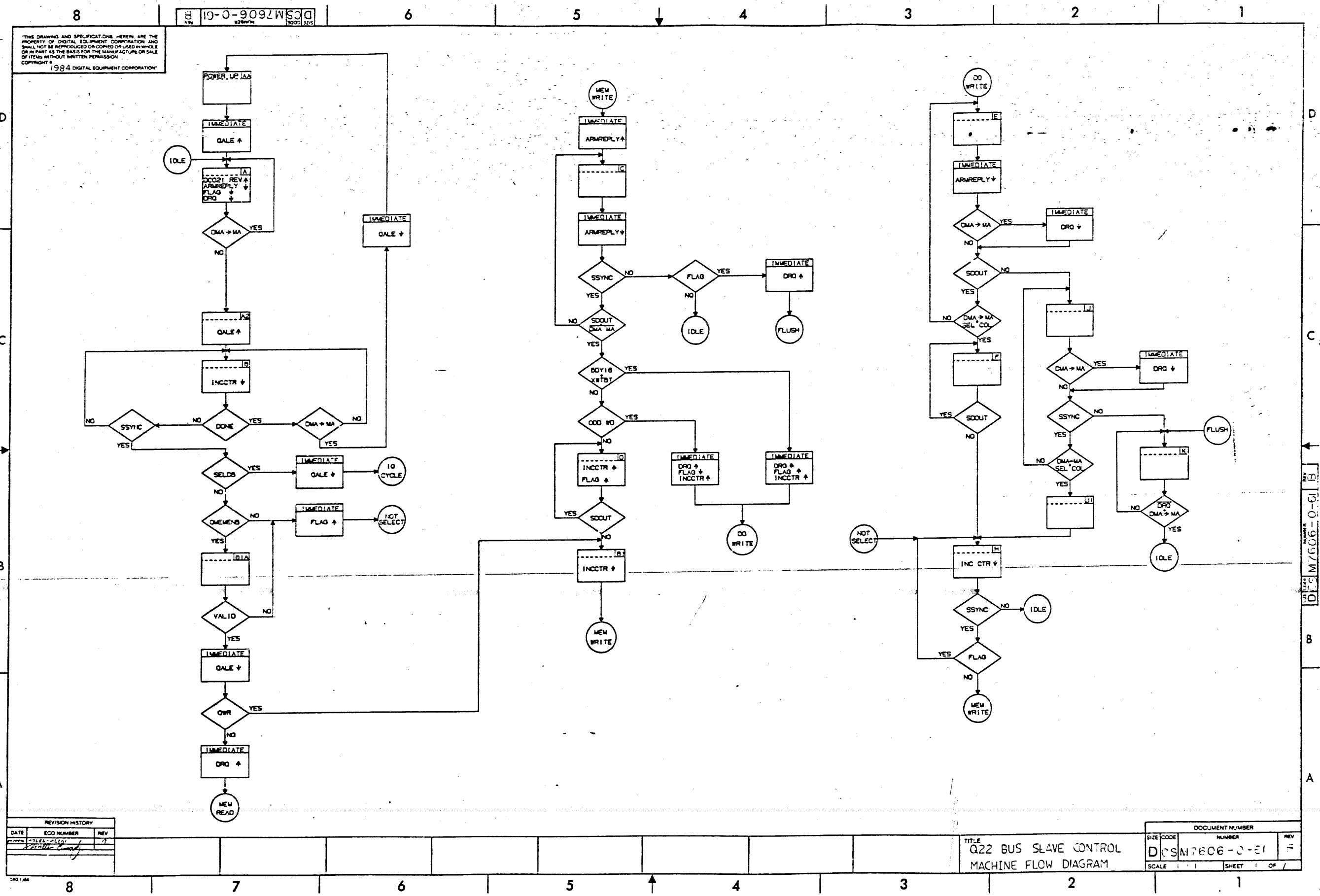


REVISION HISTORY		
DATE	ECO NUMBER	REV
10/1/84	1	1
10/1/84	2	2

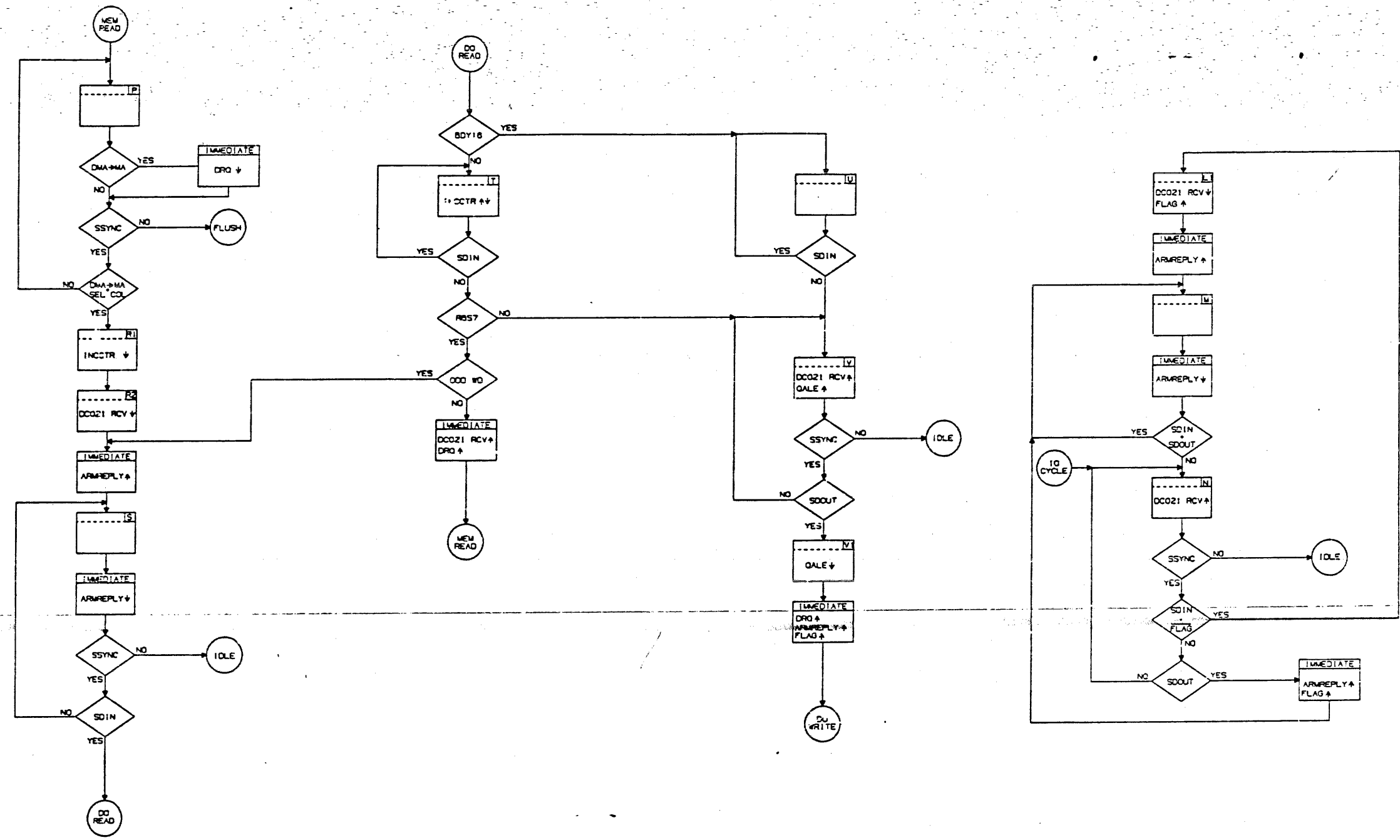
TITLE
Q22 BUS MASTER CONTROL
MACHINE FLOW DIAGRAM

DOCUMENT NUMBER		
SIZE	CODE	NUMBER
D	C	M7606-0-59
SHEET		OF
1		1

8	7	6	5	4	3	2	1
name 22_010L3_00_E33 ;Q22 bus slave cycle control machine input -drq, -dmaMA, selcol, -bdy16, oddwd, -sel_db, qwr, -QMemEnb input valid, nci6, xwtbt, rbs7, sdout, sdin, ssync, -done output nco7, DC021rcv, nco5, qale, nco3, incctr, -armreply, -drq local flag machine q_bus_slave state AA, number -hhhhh goto A [DC021rcv, -drq, qale, -armreply, -flag] state A, number -11111 if [-dmaMA] then A2 [qale] state A2, number -1h111 goto B [-incctr] state B1, number -1hh11 goto C [armreply] state C, number -11h11 always [-armreply] if [-ssync * flag] then K [drq] if [-ssync * -flag] then A & [DC021rcv, -armreply, -flag, -drq] if [ssync * sdout * -dmaMA * -xwtbt * -bdy16 * -oddwd] then D [incctr, flag] if [ssync * sdout * -dmaMA * (xwtbt + bdy16)] then E [drq, flag, incctr] if [ssync * sdout * -dmaMA * -xwtbt * -bdy16 * oddwd] then E & [drq, -flag, incctr] state D if [-sdout] then B1 [-incctr] state E, number -11h1h always [-armreply] if [dmaMA] then [-drq] if [-sdout] then J if [sdout * dmaMA * selcol] then F state F if [-sdout] then H [-incctr] state H, number -1hh1h if [-ssync] then A [DC021rcv, -armreply, -flag, -drq] if [ssync * -flag] then C [armreply] state J, number -h111h if [dmaMA] then [-drq] if [-ssync] then K if [ssync * dmaMA * selcol] then J1 state J1, number -hh11h goto H [-incctr] state K, number -1111h if [dmaMA * -drq] then A [DC021rcv, -armreply, -flag, -drq] state L1, number -1hhhh goto M [armreply] state M, number -11hhh always [-armreply] if [-sdin * -sdout] then N [DC021rcv] state N, number -111h1 if [-ssync] then A [DC021rcv, -armreply, -flag, -drq] if [ssync * sdin * -flag] then L1 [-DC021rcv, flag] if [ssync * -sdin * -flag] * sdout] then M [armreply, flag] state P, number -h1111 if [dmaMA] then [-drq] if [-ssync] then K if [ssync * dmaMA * selcol] then R state R, number -hh111 goto R1 [-incctr] state R1, number -hh1h1 goto R2 [-DC021rcv] state R2, number -1hhhl goto S [armreply] state S, number -11h1h always [-armreply] if [-ssync] then A [DC021rcv, -armreply, -flag, -drq] if [ssync * sdin * bdy16] then U if [ssync * sdin * -bdy16] then T [incctr] state T if [-sdin * -rbs7] then V & [-incctr, DC021rcv, qale] if [-sdin * rbs7 * oddwd] then S & [-incctr, armreply] if [-sdin * rbs7 * -oddwd] then P & [-incctr, DC021rcv, drq] state U if [-sdin] then V [DC021rcv, qale] state V, number -h11h1 if [-ssync] then A [DC021rcv, -armreply, -flag, -drq] if [ssync * sdout] then V1 [-qale] state V1, number -1hh1h goto E [armreply, flag, drq] state B, number -1h1h1 if [-done * ssync * -sel_db * QMemEnb] then B1A if [-done * ssync * -sel_db * -QMemEnb] then H [flag, -incctr] if [-done * ssync * sel_db] then N [-qale, DC021rcv] if [done * dmaMA] then AA [-qale] state B1A if [-valid] then H [flag, -incctr] if [valid * -qwr] then P [-qale, drq] if [valid * qwr] then B1 [-qale, -incctr] END							
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8	7	6	5	4	3	2	1



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REVISION HISTORY		
DATE	ECO NUMBER	REV

TITLE
322 BUS SLAVE CONTROL
MACHINE FLOW DIAGRAM

DOCUMENT NUMBER		
SIZE	CODE	NUMBER
D	CS	M7606-0-62
SCALE	SHEET	OF

8	7	6	5	4	3	2	1
DEC PART NUMBER: 23-E42F1-00 KA630 -A, -B 1MB RAS DECODE PROM (E79) DATE ORIGINATED: 10-September-1984							
HEX LOC DATA	HEX LOC DATA	HEX LOC DATA	HEX LOC DATA	HEX LOC DATA	HEX LOC DATA	HEX LOC DATA	HEX LOC DATA
000 9	03D 5	07A 9	0B7 9	0F4 9	131 9	16E 9	1AB 9
001 9	03E 8	07B 9	0B8 8	0F5 9	132 9	16F 9	1AC 9
002 9	03F 9	07C 7	0B9 9	0F6 9	133 9	170 9	1AD 9
003 9	040 9	07D 9	0BA 9	0F7 9	134 9	171 9	1AE 9
004 9	041 9	07E 9	0BB 9	0F8 9	135 9	172 9	1AF 9
005 9	042 9	07F 9	0BC 9	0F9 9	136 9	173 9	1B0 9
006 9	043 9	080 9	0BD 9	0FA 9	137 9	174 9	1B1 9
007 9	044 9	081 9	0BE 9	0FB 9	138 9	175 9	1B2 9
008 9	045 9	082 9	0BF 9	0FC 9	139 9	176 9	1B3 9
009 9	046 9	083 9	0C0 9	0FD 9	13A 9	177 9	1B4 9
00A 9	047 9	084 9	0C1 9	0FE 9	13B 9	178 9	1B5 9
00B 9	048 9	085 9	0C2 9	0FF 9	13C 9	179 9	1B6 9
00C 9	049 9	086 9	0C3 9	100 9	13D 9	17A 9	1B7 9
00D 9	04A 9	087 9	0C4 9	101 9	13E 9	17B 9	1B8 9
00E 9	04B 9	088 9	0C5 9	102 9	13F 9	17C 9	1B9 9
00F 9	04C 9	089 9	0C6 9	103 9	140 9	17D 9	1BA 9
010 9	04D 9	08A 9	0C7 9	104 9	141 9	17E 9	1BB 9
011 9	04E 9	08B 9	0C8 9	105 9	142 9	17F 9	1BC 9
012 9	04F 9	08C 9	0C9 9	106 9	143 9	180 9	1BD 9
013 9	050 9	08D 9	0CA 9	107 9	144 9	181 9	1BE 9
014 9	051 9	08E 9	0CB 9	108 9	145 9	182 9	1BF 9
015 9	052 9	08F 9	0CC 9	109 9	146 9	183 9	1C0 9
016 9	053 9	090 9	0CD 9	10A 9	147 9	184 9	1C1 9
017 9	054 9	091 9	0CE 9	10B 9	148 9	185 9	1C2 9
018 9	055 9	092 9	0CF 9	10C 9	149 9	186 9	1C3 9
019 9	056 9	093 9	0D0 9	10D 9	14A 9	187 9	1C4 9
01A 9	057 9	094 9	0D1 9	10E 9	14B 9	188 9	1C5 9
01B 9	058 9	095 9	0D2 9	10F 9	14C 9	189 9	1C6 9
01C 9	059 9	096 9	0D3 9	110 9	14D 9	18A 9	1C7 9
01D 9	05A 9	097 9	0D4 9	111 9	14E 9	18B 9	1C8 9
01E 9	05B 9	098 9	0D5 9	112 9	14F 9	18C 9	1C9 9
01F 9	05C 9	099 9	0D6 9	113 9	150 9	18D 9	1CA 9
020 9	05D 9	09A 9	0D7 9	114 9	151 9	18E 9	1CB 9
021 9	05E 9	09B 9	0D8 9	115 9	152 9	18F 9	1CC 9
022 9	05F 9	09C 9	0D9 9	116 9	153 9	190 9	1CD 9
023 9	060 9	09D 9	0DA 9	117 9	154 9	191 9	1CE 9
024 9	061 9	09E 9	0DB 9	118 9	155 9	192 9	1CF 9
025 9	062 9	09F 9	0DC 9	119 9	156 9	193 9	1D0 9
026 9	063 9	0A0 9	0DD 9	11A 9	157 9	194 9	1D1 9
027 9	064 9	0A1 9	0DE 9	11B 9	158 9	195 9	1D2 9
028 9	065 9	0A2 9	0DF 9	11C 9	159 9	196 9	1D3 9
029 9	066 9	0A3 9	0E0 9	11D 9	15A 9	197 9	1D4 9
02A 9	067 9	0A4 9	0E1 9	11E 9	15B 9	198 9	1D5 9
02B 9	068 9	0A5 9	0E2 9	11F 9	15C 9	199 9	1D6 9
02C 9	069 9	0A6 9	0E3 9	120 9	15D 9	19A 9	1D7 9
02D 9	06A 9	0A7 9	0E4 9	121 9	15E 9	19B 9	1D8 9
02E 9	06B 9	0A8 9	0E5 9	122 9	15F 9	19C 9	1D9 9
02F 9	06C 9	0A9 9	0E6 9	123 9	160 9	19D 9	1DA 9
030 9	06D 9	0AA 9	0E7 9	124 9	161 9	19E 9	1DB 9
031 9	06E 9	0AB 9	0E8 9	125 9	162 9	19F 9	1DC 9
032 9	06F 9	0AC 9	0E9 9	126 9	163 9	1A0 9	1DD 9
033 9	070 9	0AD 9	0EA 9	127 9	164 9	1A1 9	1DE 9
034 9	071 9	0AE 9	0EB 9	128 9	165 9	1A2 9	1DF 9
035 9	072 9	0AF 9	0EC 9	129 9	166 9	1A3 9	1E0 9
036 9	073 9	0B0 9	0ED 9	12A 9	167 9	1A4 9	1E1 9
037 9	074 9	0B1 9	0EE 9	12B 9	168 9	1A5 9	1E2 9
038 9	075 9	0B2 9	0EF 9	12C 9	169 9	1A6 9	1E3 9
039 9	076 9	0B3 9	0F0 9	12D 9	16A 9	1A7 9	1E4 9
03A 9	077 9	0B4 9	0F1 9	12E 9	16B 9	1A8 9	1E5 9
03B 9	078 9	0B5 9	0F2 9	12F 9	16C 9	1A9 9	1E6 9
03C 9	079 9	0B6 9	0F3 9	130 9	16D 9	1AA 9	1E7 9

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REVISIONS
CHK CHANGE NO. REV

digital

DATE 09-AUG-85
CHK'D R. MCNAMARA
DATE 09-AUG-85
SHEET 1 OF 1
FIRST USED ON OPTION/MODEL:

DATE 09-AUG-85
ENG. R. MCNAMARA
DATE 09-AUG-85
BOARD LOCATION:
NEXT HIGHER ASSEMBLY:

TITLE: 1KX4 RAS DECODE
PROM (E79) LISTING

SIZE CODE NUMBER REV.
D CS M7606-0-63 A

8	7	6	5	4	3	2	1
T: PAL16L8A P: 23-169J5-00 E40 N: BARRY MASKAS D: 25 OCT 1984 S: /RPOK /BTRYOK CSMAPEMEAR /EPAS EPDS UVWR /DONE GALE /GRANT GND SRPLY /CLRPSH /ENBLS646 /WRMAP /ENBBDAL RDIN DC021RCV /TDIO /RCVBDALH VCC B: IF [VCC] CLRPSH = RPOK + BTRYOK IF [VCC] ENBLS646 = CSMAPEMEAR * EPAS IF [VCC] WRMAP = CSMAPEMEAR * UVWR * EPDS IF [VCC] ENBBDAL = /GRANT * /RDIN * /DONE + GRANT * /DONE * GALE + /DC021RCV * /GRANT * /DONE IF [VCC] RCVBDALH = /TDIO * /DC021RCV + /TDIO * GRANT + /SRPLY * /DC021RCV + /SRPLY * GRANT + UVWR * /DC021RCV + UVWR * GRANT E: KA630-A, -B, -C, -D (M7606) MISC. CONTROL STROBES T: PAL16L8A P: 23-170J5-00 E78 N: BARRY MASKAS D: 25 OCT 1984 S: /PE3 /PE2 /PE1 /PE0 /MSER0 /MEMWR /BYTACT3 /BYTACT2 /BYTACT1 GND /BYTACT0 /HI16PER /LO16PER /PERR /SELCOL /ENBCAS0H /ENBCAS1H /ENBCAS2H /ENBCAS3H VCC B: IF [VCC] HI16PER = MSER0 * PE2 * BYTACT2 + MSER0 * PE3 * BYTACT3 IF [VCC] LO16PER = MSER0 * PE0 * BYTACT0 + MSER0 * PE1 * BYTACT1 IF [VCC] PERR = MSER0 * /MEMWR * PE0 * BYTACT0 + MSER0 * /MEMWR * PE1 * BYTACT1 + MSER0 * /MEMWR * PE2 * BYTACT2 + MSER0 * /MEMWR * PE3 * BYTACT3 IF [VCC] ENBCAS0H = /SELCOL + /BYTACT0 IF [VCC] ENBCAS1H = /SELCOL + /BYTACT1 IF [VCC] ENBCAS2H = /SELCOL + /BYTACT2 IF [VCC] ENBCAS3H = /SELCOL + /BYTACT3 E: KA630-A, -B, -C, -D (M7606) MEMORY SYSTEM CAS CONTROL STROBES AND PARITY ERROR DETECTION STROBES T: PAL16L8A P: 23-171J5-00 E86 N: BARRY MASKAS D: 25 OCT 1984 S: /UVCYCCD2 /DMAMA /DONE /MSER1 /UVCYC /MEMWR DECODE /DATABUFENB MEMCD0 GND MEMCD1 /MSWT0H /MSWT1H SELCOL /BUFENB /BUFEN0 /BUFENB1 /BDIRT /STARTCYC VCC B: IF [VCC] MSWT0H = /MEMCD1 + MEMCD0 + /MEMWR + /UVCYC IF [VCC] MSWT1H = MEMCD1 + /MEMWR + /UVCYC IF [VCC] BUFENB = MEMCD1 * /MEMCD0 * /MEMWR * SELCOL * /DMAMA + MEMCD1 * /MEMCD0 * /MEMWR * DATABUFENB + MEMCD1 * /MEMCD0 * MEMWR * UVCYC * MSER1 IF [VCC] BUFEN0 = /MEMCD1 * MEMCD0 * /MEMWR * SELCOL * /DMAMA + /MEMCD1 * MEMCD0 * /MEMWR * DATABUFENB + /MEMCD1 * MEMCD0 * MEMWR * UVCYC * MSER1 IF [VCC] BUFENB1 = /MEMCD1 * /MEMCD0 * /MEMWR * SELCOL * /DMAMA + /MEMCD1 * /MEMCD0 * /MEMWR * DATABUFENB + /MEMCD1 * /MEMCD0 * MEMWR * UVCYC * MSER1 IF [VCC] BDIRT = MEMWR * DECODE + MEMWR * SELCOL * MSER1 IF [VCC] STARTCYC = /DMAMA * UVCYCCD2 * UVCYC * /MEMWR + /DMAMA * DECODE * /UVCYCCD2 * UVCYC * /MEMWR * DONE + /DMAMA * UVCYC * MEMWR + DMAMA * DECODE + DMAMA * SELCOL E: KA630-A, -B, -C, -D (M7606) MEMORY SUBSYSTEM BUFFER CONTROL STROBES AND MEMORY CYCLE ENABLE STROBE							
8	7	6	5	4	3	2	1

D
C
B
A
D

REV. A
NUMBER M7606-0-64
SIZE CODE CS
D
B

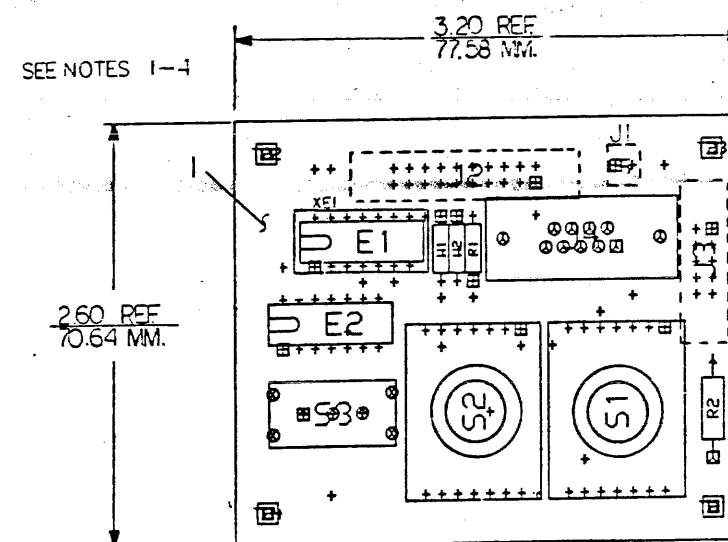
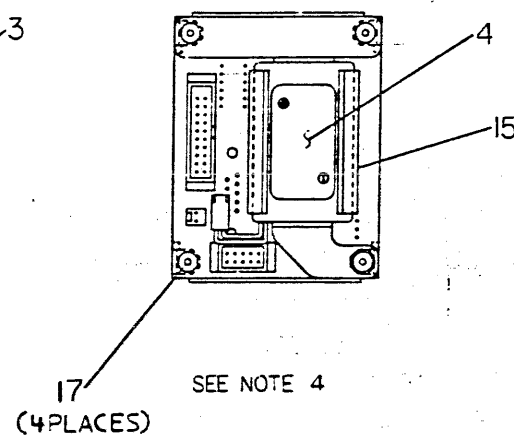
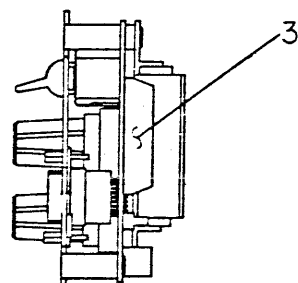
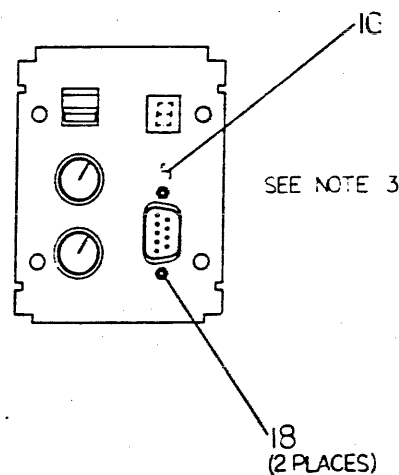
REVISIONS		
CHK	CHANGE NO.	REV.

digital	DATE 09-AUG-85	ENG. R. MCNAMARA	DATE 09-AUG-85	TITLE: PALASM LISTINGS FOR PAL16L8A DEVICES		
	CHK'D R. MCNAMARA	DATE 09-AUG-85	BOARD LOCATION: SHEET 1 OF 1			
FIRST USED ON OPTION/MODEL: DSK: 3.12P(4,550) 09-AUG-85 07:39 NEXT HIGHER ASSEMBLY:				SIZE CODE D CS	NUMBER M7606-0-64	REV. A

[illegible]

[illegible]

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NOTES:

1. W1, W2 ARE NOT INSTALLED.
 2. J1, J2 AND J3 ARE MOUNTED ON SIZE 2.
 3. MOUNT J4 TO ITEM 16 WITH ITEM 13 BEFORE SOLDERING INTO ITEM 1.
 4. TEST PANEL IN TESTER AFTER COMPLETING NOTE 3 AND BEFORE ASSEMBLY OF ITEMS 1, 15, 16 WITH ITEM 17.
- | STEP | E | Y AXIS | STEP | TIMES |
|--------|----------|--------|-------|-------|
| REPEAT | → X AXIS | STEP | TIMES | |

CH	CHANGE NO	REV
1	5416244-1	1
2	5416244-2	1
3	5416244-3	1
4	5416244-4	1
5	5416244-5	1
6	5416244-6	1
7	5416244-7	1
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ETCH REV.	A1
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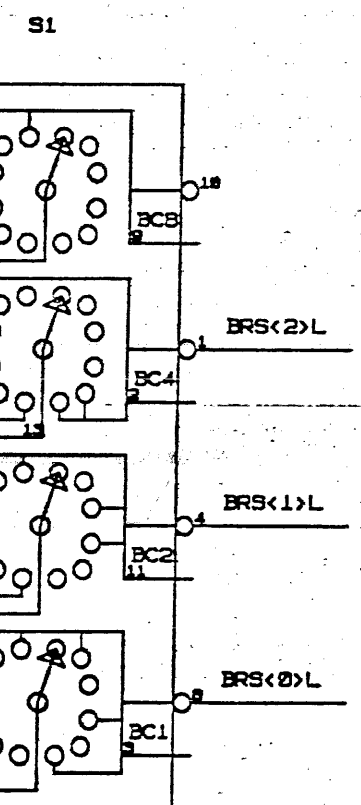
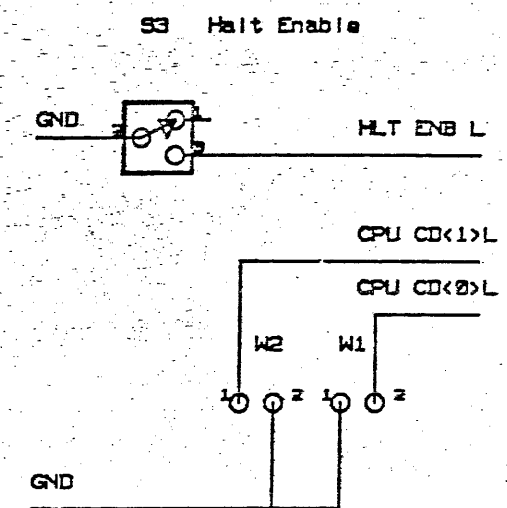
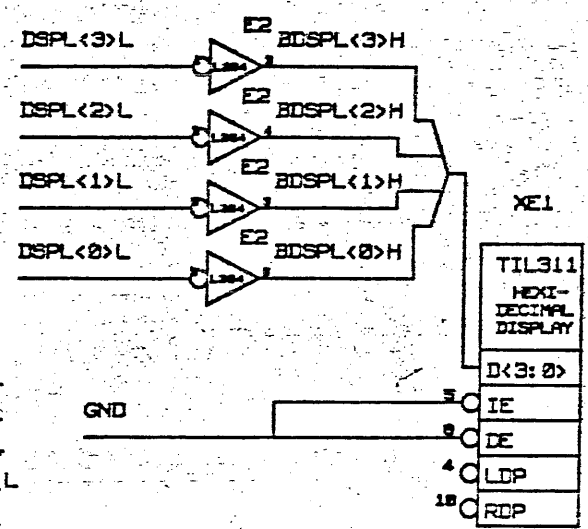
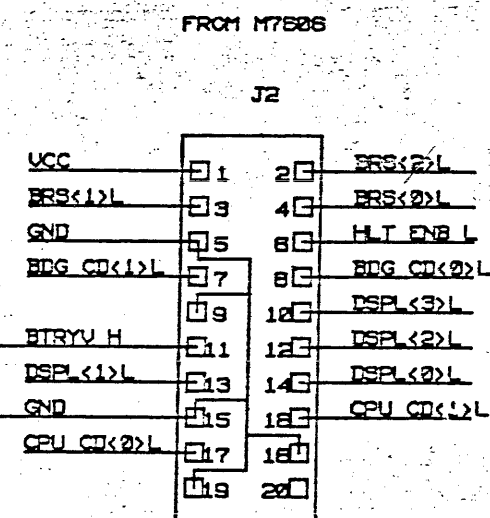
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DRN. DAVID DROZD		
CHK'D. J. J. J.		
MECH. ENG. J. J. J.		
PROJ. ENG. J. J. J.		
PROD. J. J. J.		
SCALE 2/1	SIZE CODE D UA	NUMBER 5416244-0-0
SHT. OF		
NEXT HIGHER ASSY. B-DD-5416244-0-0		

1 MS# 275400

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
						01	A1	
1	1	D-MD-5016743-0-0	5016743-01		CIRCUIT DRILL AND ETCH	1		
2	2		1216565-02		SKT,IC 14PIN DIP TIN ELEV	1		XE1
3	3		1217727-00		PCB,HEADER 20POS(1X20).100CC STR	1		J2
4	4		1219245-01		BATTERY,3CELL 3.75V .18MA NICAD	1		
5	5		1219251-00		PCB HEADER 02PIN(1X02).100CC STR	1		J1
6	6		1219573-05		CONN,D SUB 9PIN ASSY STR	1		J4
7	7		1219952-08		PCB HEADER 09PIN(2X05).100CC STR	1		J3
8	8		1223262-01		SW,ROT 1P08POS	1		S1
9	9		1223263-02		SW,ROT 2P03POS	1		S2
10	10		1223646-01		SW,RKR SPDT ON-OFF-ON	1		S3
11	11		1300229-00		100.0 .25 W 5.0 % CF	1		R1
12	12		1300354-00		750.0 .50 W 5.0 % CF	1		R2
13	13		1912803-00		LS04 INVERTER GATE,HEX	1		E2
14	14		1916921-00		HEXADECIMAL DISPLAY W/DECODER	1		E1
15	15		7430801-01		HOLDER,BATTERY	1		
16	16		7431737-01		PLATE,CONNECTOR	1		
17	17		9008181-01		SCREW,TAP PAN PHIL 6-	4		
18	18		9008451-01		SCREW LOCK,STANDOFF ONLY .060TH	2		

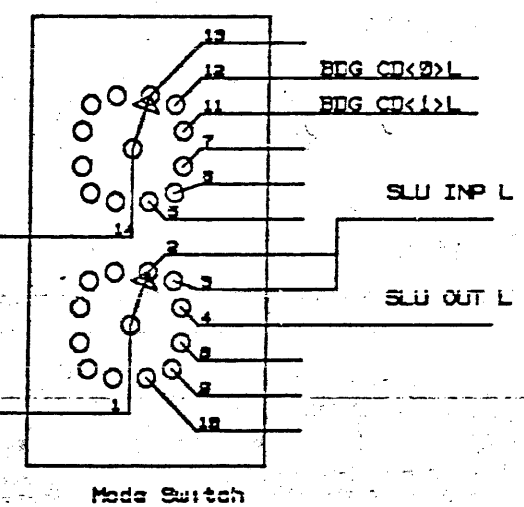
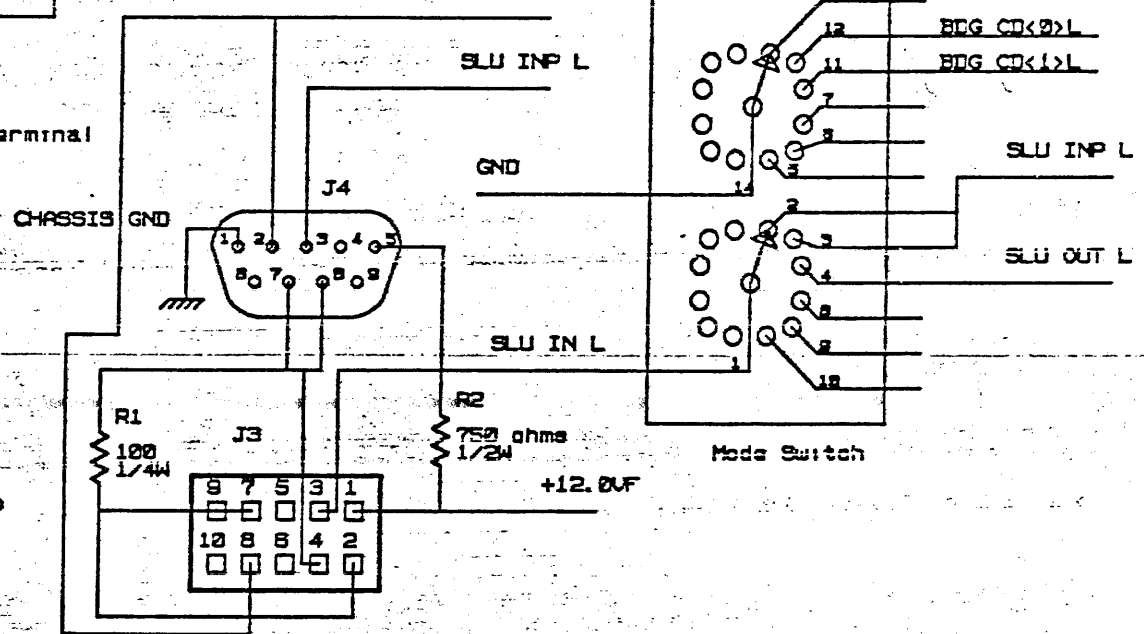
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ENG	ECO NUMBER	REV	SECTION A OF A			CHK'D: D. DROZD			DATE: 01-JUN-84			TITLE PARTS LIST		
	INITIAL	A	SECTION VARIATION INDEX									FUNCTION SEL/SLU MODULE		
			[A] 01											
			[B]											
			[C]			DES.ENG: M. DEMARE			DATE: 12-OCT-84			DOCUMENT NUMBER		
			[D]									SIZE CODE NUMBER REV		
			[E]											
			[F]			RESP.ENG.: M. DEMARE			DATE: 12-OCT-84			K PL 5416744-0-DBP A		
			[H]											
			[J]											
			[K]			MFG.ENG.: S. WASH			DATE: 12 NOV 84			RELEASE DATE: 27-FEB-85		
			[L]											
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			[N]			D-UA-5416744-0-0			D-UA-5416744-0-0P			ML769A.PLS		
												EDIT #		
												34		
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FROM M7808	TO FS&SLU	FROM M7808	TO FS&SLU
J2 PIN	J2 PIN	J2 PIN	J2 PIN
1	20 GND	11	18 DSPL<3>L
2	19 GND	12	9 GND
3	18 GND	13	8 BDG CD<0>L
4	17 CPU CD<0>L	14	7 BDG CD<1>L
5	16 CPU CD<1>L	15	6 HLT ENB L
6	15 GND	16	5 GND
7	14 DSPL<0>L	17	4 BRS<0>L
8	13 DSPL<1>L	18	3 BRS<1>L
9	12 DSPL<2>L	19	2 BRS<2>L
10	11 BTRYV H	20	1 +5.0V = VCC



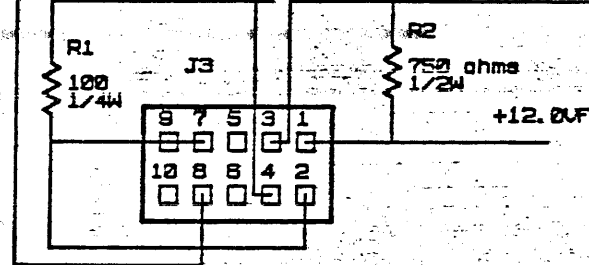
- BRS<2:0>L
- 0 HH 300 BAUD
 - 1 HL 600
 - 2 LH 1200
 - 3 LL 2400
 - 4 LH 4800
 - 5 LH 9600
 - 6 LH 19200
 - 7 LL 38400

to Console terminal



- CPU CD<1:2>L
- | W2 | W1 | FUNCTION |
|----|----|-------------|
| R | R | NORMAL |
| R | I | AUXILIARY 1 |
| I | R | AUXILIARY 2 |
| I | I | AUXILIARY 3 |
- I=INSERTED
R=REMOVED
- BDG CD<1:2>L
- 0 HH NORMAL
 - 1 HL LANGUAGE INQUIRY
 - 2 LH SLU LOOPBACK TEST

FROM M7808	TO FS&SLU
J3 PIN	J3 PIN
1	10 BURESET<L>
2	9 GND
3	8 SLU OUT L
4	7 GND
5	6 KEY
3	5 KEY
7	4 SLU IN H
8	3 SLU IN L
9	2 GND
10	1 +12.0V



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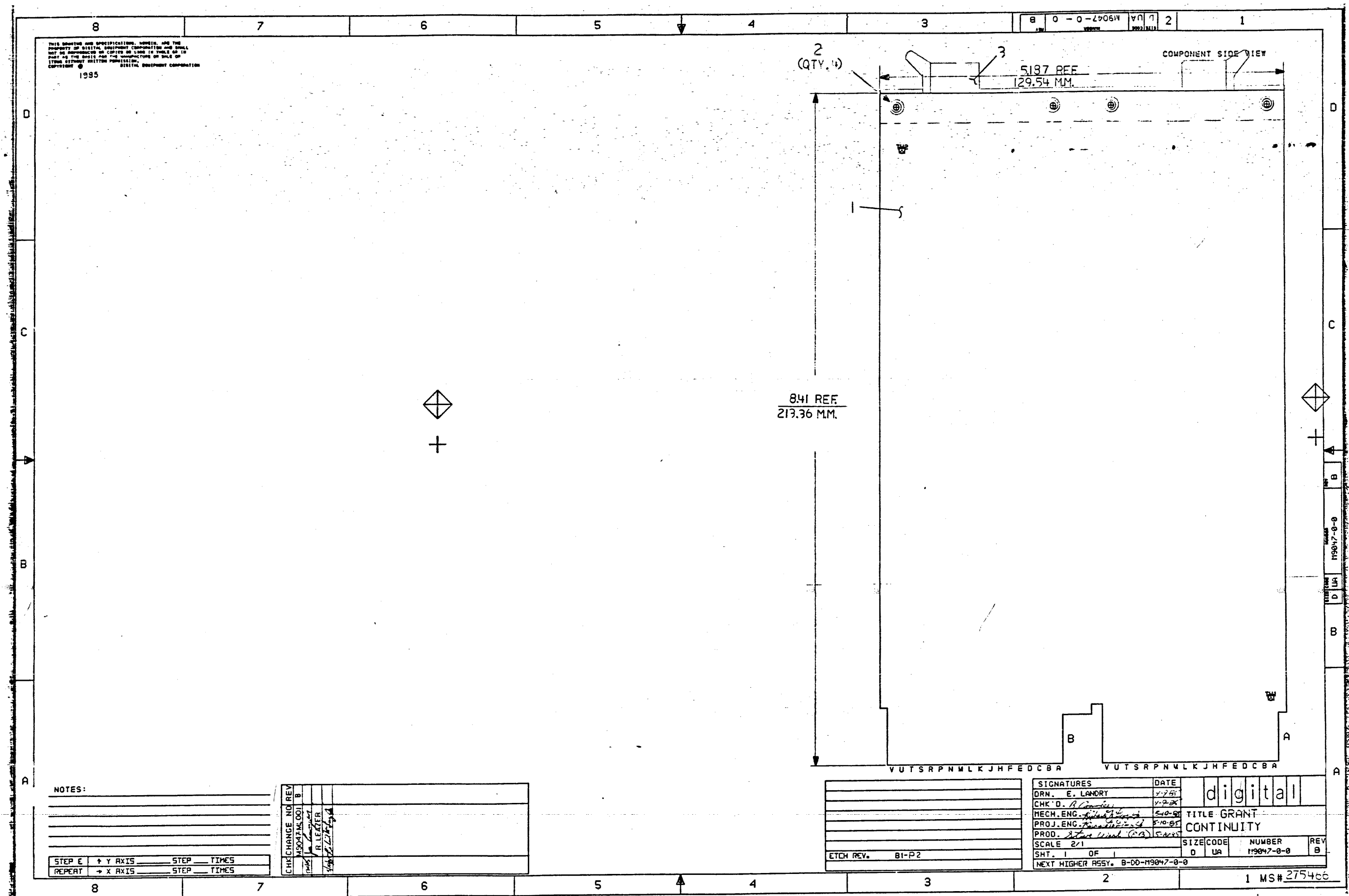
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REV EDS NUMBER DATE

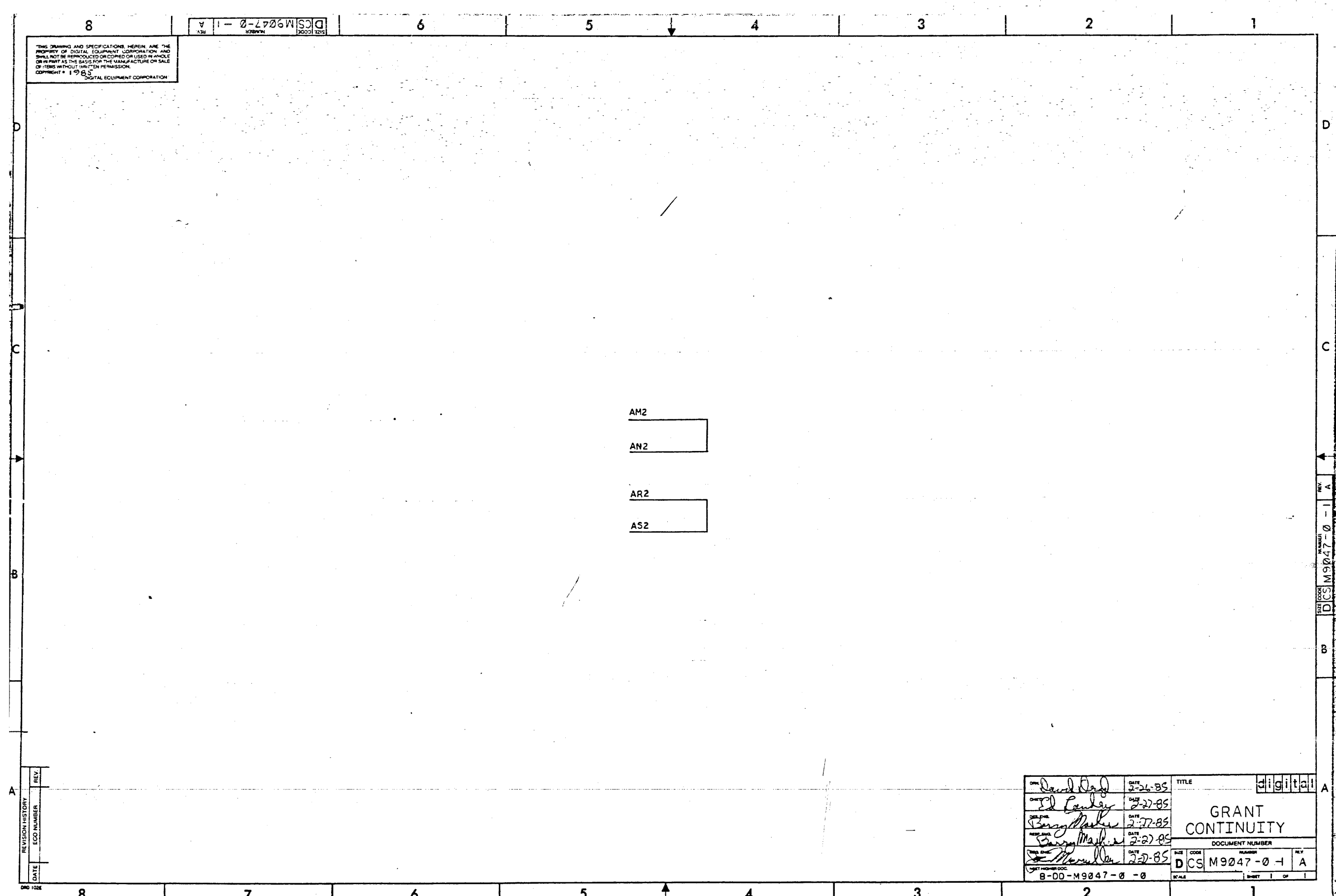
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digit

CRN: Malcolm de Mars
DATE 20-SEP-84
ENG: Malcolm de Mars
DATE 20-SEP-84
SHEET 1 OF 1
NEXT HIGHER ASSEMBLY: B-DD-5416744-0-0

TITLE: Function Selection & SLU Module (F33SLU) for M7808(KA630)			
SIZE	CODE	NUMBER	REV
K	CS	5416744-0-1	A





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SIZE CODE NUMBER
DCS M9047-0-1 A

REV.	ECO NUMBER	DATE

OWN	David Dwyer	DATE	2-26-85	TITLE	digital
CHG	Ed Conley	DATE	2-27-85	GRANT CONTINUITY	DOCUMENT NUMBER
DESIGN	Berry Parker	DATE	2-27-85		
REP AND	Berry Parker	DATE	2-27-85		
TRNG ENG	W. M. Muller	DATE	2-27-85	SIZE CODE NUMBER	REV
B-DD-M9047-0-0				DCS M9047-0-1	A
SCALE				SHEET	OF

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	
						01	02
					VARIATION REVISION LEVEL:	A1	A1
1	1	A-PS-1700313-0-0	1700313-01	A	CABLE ASSY,06 COND,MOLD,SHLD	2	1
2	2	A-PS-4700042-0-0	4700042-01		SCREWDRIVER,FLAT BLADE 0.12 X 2.		1
3	3	A-PS-3622092-0-0	3622092-01	A	LABEL,LCP5 CONFIGURATION		1
4	4	A-PS-9907349-0-0	9907349-01	A	CARTON,DIECUT,C,200PSI,W/GRAPHIC		1
5	5	B-DD-M9047-0	M9047-00		QBUS GRANT CONTINUITY,1ST USED I		1
6	6	A-PS-1700301-0-0	1700301-00	A	CABLE ASSY,07 COND,MOLD,SHLD	2	1
7	7	A-PS-3624251-0-0	3624251-01	A	LABEL,FRONT CONTROL PANEL BA123	1	1

REVISION HISTORY			BASIC PART NO: 7022382		DRN: D. HEALY	DATE: 05-DEC-84	D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: D. HEALY	DATE: 08-MAR-85	TITLE PARTS LIST BA123-A ACCESSORY KIT			
---	INITIAL	A	SECTION VARIATION INDEX [A]01,02		DES.ENG: J. KWONG	DATE: 08-MAR-85	DOCUMENT NUMBER			
			[B]		RESP.ENG.: J. KWONG	DATE: 08-MAR-85	SIZE	CODE	NUMBER	REV
			[C]		MFG.ENG.: J. EDWARDS	DATE: 08-MAR-85	K	PL	7022382-0-DBP	A
			[D]		ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	RELEASE DATE: 26-MAR-85			
			[E]				FILE NAME: ML755A.PLS			EDIT # 4
			[F]							
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TABLE OF CONTENTS

B-TC-7022380-0-DBU	BA123-A BASIC ENCLOSURE (TC)	UNIT VARIATIONS COVERED BY THIS PRINT SET
D-AD-7022380-0-DBU	BA123-A BASIC ENCLOSURE	
K-PL-7022380-0-DBP	BA123-A BASIC ENCLOSURE (PL)	
B-DD-7022380-0-DBU	BA123-A BASIC ENCLOSURE (DD ONLY)	
D-AR-7022380-0-DBU	BA123-A SYSTEM ARRANGEMENT	
B-DD-5417507-0-0	QBUS BACKPLANE 13 SLOTS QUAD (DD)	
K-PL-5417507-0-DBP	QBUS BACKPLANE 13 SLOTS QUAD	
D-UA-5417507-0-0	QBUS BACKPLANE 13 SLOTS QUAD	
D-CS-5417507-0-1	QBUS BACKPLANE 13 SLOTS QUAD	
B-DD-5416596-0-0	BA123-A FRONT PANEL (DD ONLY)	
K-PL-5416596-0-DBP	BA123-A FRONT PANEL	
D-UA-5416596-0 DBU	BA123-A FRONT PANEL	
D-CS-5416596-0-1	BA123-A FRONT PANEL	
B-DD-5416665-0-0	REMOTE SENSE TEMP (DD ONLY)	
K-PL-5416665-0-DBP	REMOTE SENSE TEMP	
D-UA-5416665-0-0	REMOTE SENSE TEMP	
D-CS-5416665-0-1	REMOTE SENSE TEMP	
A-PS-1700859-0-0	CABLE, ASSY AC ON/OFF PWR	
A-PS-1700865-0-0	CABLE, 18 COND P.S. TO BACK PNL	
A-PS-1700942-0-0	CABLE ASSY, FAN DOOR INTERLOCK	
A-PS-1700863-0-0	CABLE, P.S. TO CC FAN & SENSOR	
A-PS-1700860-0-0	CABLE, ASSY, CONSOLE BACKPLANE	
A-PS-1700870-0-0	CABLE, PS TO 2 DRIVES	
A-PS-1700911-0-0	CABLE, PS TO 3 DRIVES	
A-PS-1700083-0-0	US LINE CORD	
A-PS-3023616-0-0	P.S. 460W (H7260)	

7022380

FIELD MAINTENANCE
PRINT SET

DIGITAL EQUIPMENT
CORPORATION

PRINT SET PART
NUMBER MP-02049-01
REV A1

FILE NAME: TC7022380-0-DBUA

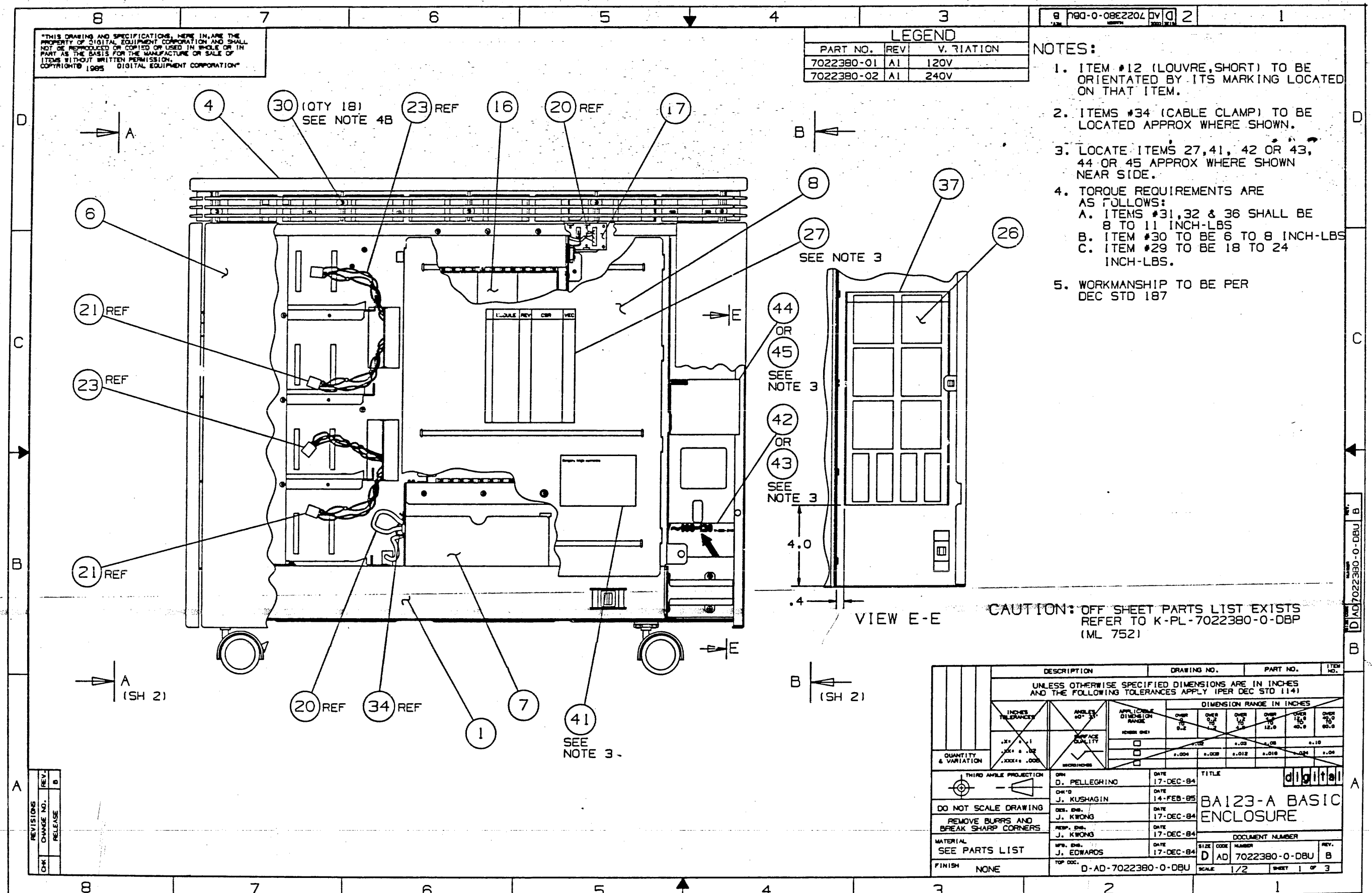
7022380
FIELD MAINTENANCE
PRINT SET

DIGITAL EQUIPMENT
CORPORATION

PRINT SET PART
NUMBER MP-02049-01
REV A1

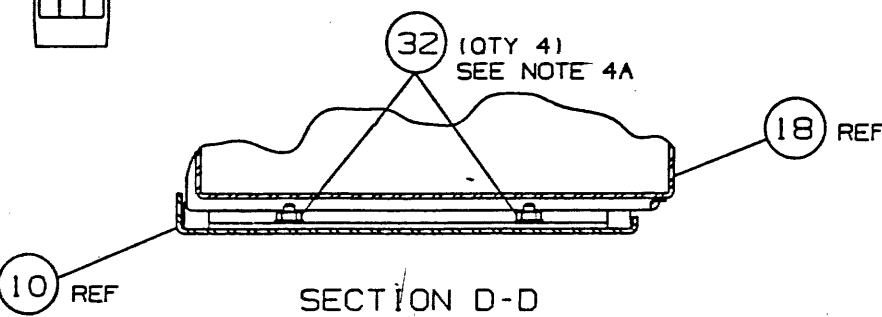
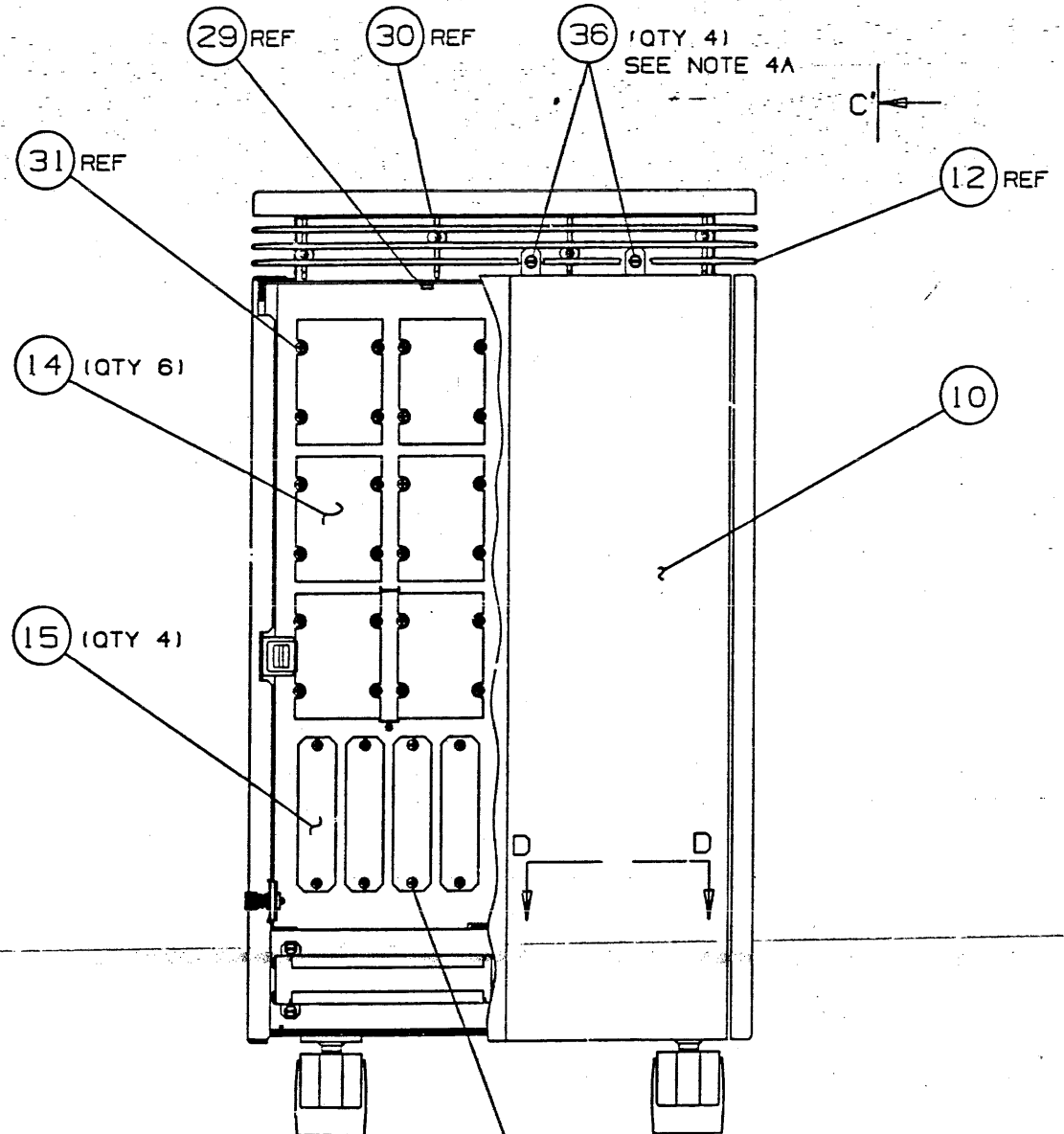
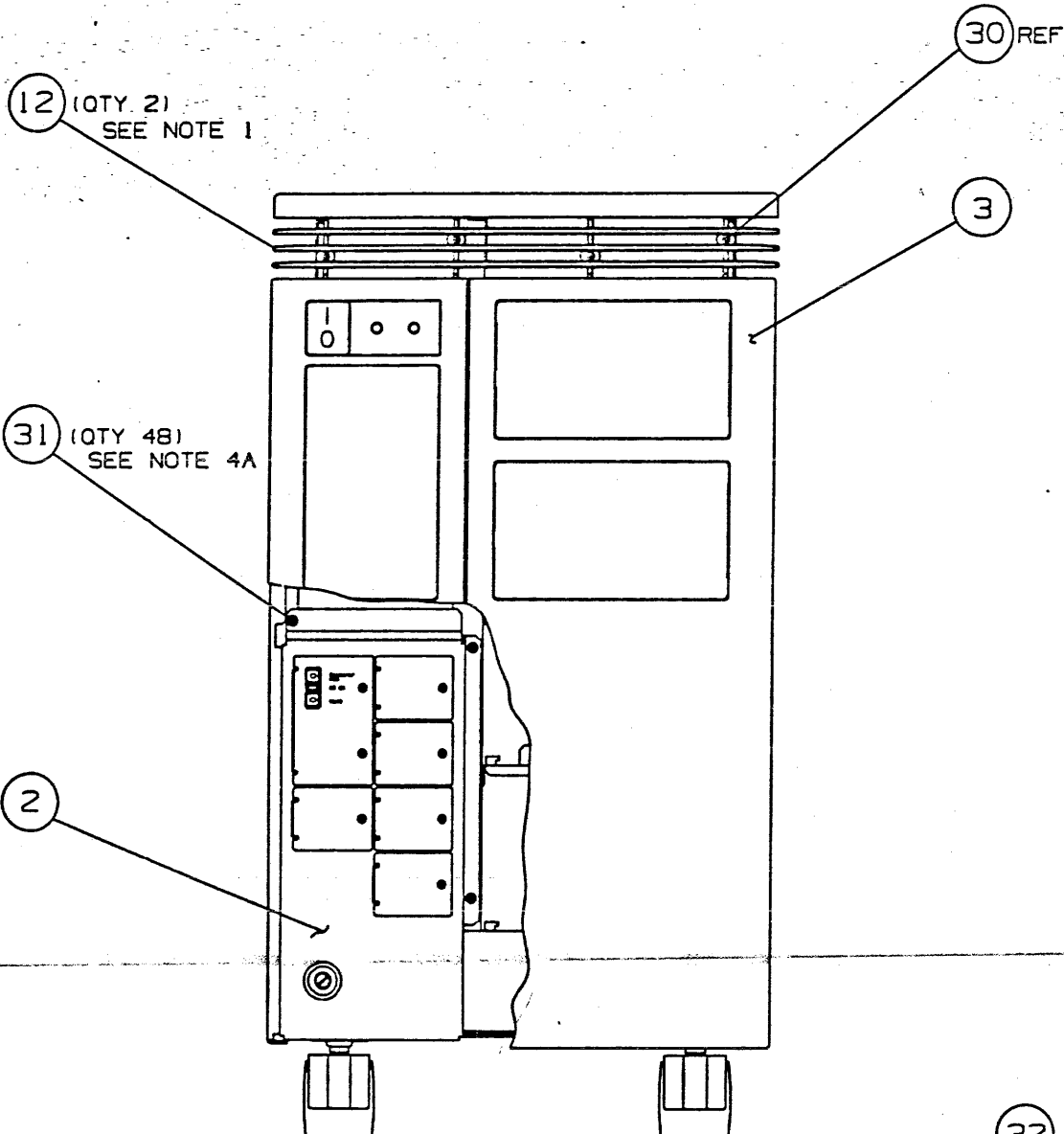
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REVISION HISTORY	REV.	A	DRN.	D. HEALY	DATE	29 NOV 84	TITLE BA123-A BASIC ENCLOSURE		
	ECO NUMBER	INITIAL	CHK'D	D. HEALY	DATE	08 MAR 85			
			DES. ENG.	J. KWONG	DATE	08 MAR 85			
			RESP. ENG.	J. KWONG	DATE	08 MAR 85			
			FIELD SERVICE	J. EVANS	DATE	08 MAR 85			
	DATE			TOP DOC.	B-DD-7022380-0-DBU		SIZE	CODE	NUMBER
						B	TC	7022380-0-DBU	A



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8 7022380-0 DBU B 2

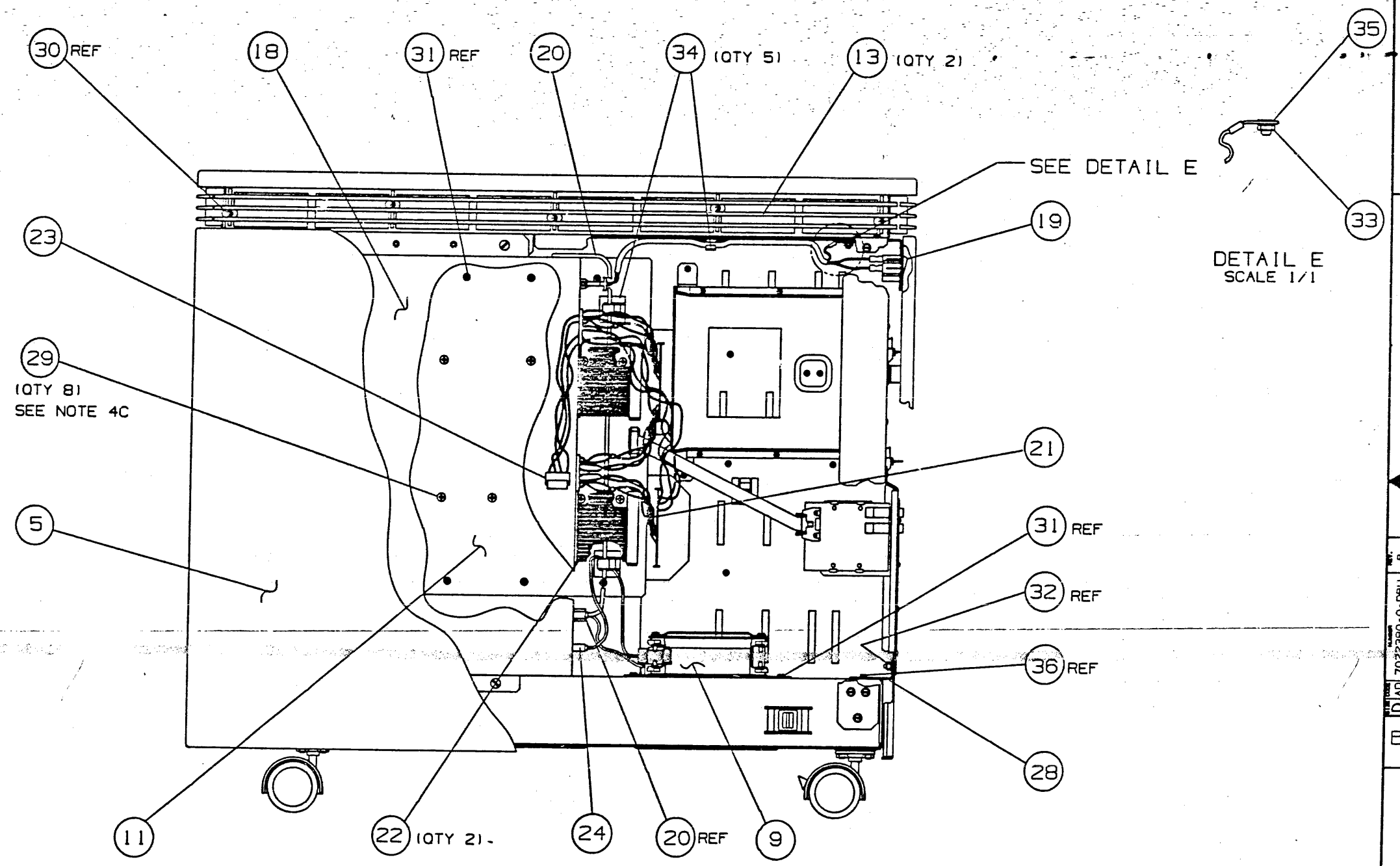


REVISIONS		
CHK	CHANGE NO	REV
	RELEASE	B

TITLE		SIZE	CODE	NUMBER	REV
BA123-A BASIC ENCLOSURE		D	AD	7022380-0-DBU	B
SCALE	1/2	SHEET	2	OF	3
		DIST			

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7022380-0-DBU 2



VIEW C-C

REVISIONS		
CHK	CHANGE NO	REV
RELEASE		B

TITLE BA123-A BASIC ENCLOSURE
SCALE 1/2 SHEET 3 OF 3
NUMBER 7022380-0-DBU
REV B

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION	
						01	02
VARIATION REVISION LEVEL:						A1	A1
1	1	D-AD-7021991-0-DBU	7021991-01		FRAME ASSEMBLY	1	1
2	2	C-AD-7021996-0-DBU	7021996-01		CONTROL PANEL ASSY.	1	1
3	3	D-AD-7021997-0-DBU	7021997-01		PANEL COVER FRONT DISK ASSY	1	1
4	4	D-AD-7021999-0-DBU	7021999-01		TOP COVER ASSY.	1	1
5	5	D-AD-7022000-0-DBU	7022000-01		SIDE PANEL ASSY L/H	1	1
6	6	D-AD-7022001-0-DBU	7022001-01		SIDE PANEL ASSY R/H	1	1
7	7	D-AD-7022003-0-DBU	7022003-01		FAN MOUNT CARD CAGE ASSY.	1	1
8	8	D-IA-7022006-0-DBU	7022006-01		COVER FRONT CARD CAGE ASSY.	1	1
9	9	D-AD-7022296-0-DBU	7022296-01		FAN MOUNT MASS STORAGE ASSY	1	1
10	10	D-AD-7022339-0-DBU	7022339-01		DOOR, REAR ASSY.	1	1
11	11	D-IA-7430692-0-DBU	7430692-01		COVER,REAR,CARD CAGE	1	1
12	12	D-MD-7430709-0-DBU	7430709-01		LOUVRE,SHORT	2	2
13	13	D-MD-7430710-0-DBU	7430710-01		LOUVRE, LONG	2	2
14	14	C-IA-7427574-0-0	7427574-01		PLATE,COVER	6	6
15	15	B-MD-7428683-0-DBU	7428683-01		PLATE,CONNECTOR BLANK	4	4
16	16	E-UA-5417507-0-DBU	5417507-01		QBUS BACKPLANE,13 SLOTS,QUAD	1	1
17	17	D-UA-5416665-0-DBU	5416665-01		REMOTE SENSE OF TEMPERATURE FOR	1	1
18	18	D-PS-3023616-0-DBU	3023616-01		P.S. 460W 2DC REGULATOR BDS,FAN	1	1
19	19	A-PS-1700859-0-0	1700859-01		CABLE ASSY,AC,ON/OFF POWER	1	1
20	20	A-PS-1700863-0-0	1700863-01		CABLE ASSY,FAN	1	1
21	21	A-PS-1700870-0-0	1700870-01		CABLE ASSY,DC POWER DRIVE	1	1
22	22	A-PS-1700865-0-0	1700865-01		CABLE ASSY,18COND	2	2
23	23	A-PS-1700911-0-0	1700911-01		CABLE ASSY,9POS POWER DRIVE	1	1
24	24	A-PS-1700864-0-0	1700864-01		CABLE ASSY,FAN	1	1
25	25	A-PA-3700821-0-0	3700821-01		PKG COMPUTER, BA123 CUSTOMER	1	1
26	26	A-PS-3624253-0-0	3624253-01		LABEL,I/O,BULKHEAD	1	1
27	27	A-PS-3624254-0-0	3624254-01		LABEL,MU BUSINESS COMPUTER	1	1
28	28	C-MD-7431225-0-DBU	7431225-01		BRACKET,CONTROL PANEL	1	1
29	29		9010174-01		SCREW,SEMS PAN PHIL	8-	8
30	30		9000055-01		SCREW,THD RL,PAN POZI F/METL	4	18 18

REVISION HISTORY		BASIC PART NO: 7022380		DRN: S. STEFANICK	DATE: 18-DEC-84	D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: J. KUSHAGIN	DATE: 13-FEB-85	TITLE PARTS LIST BA123-A BASIC ENCLOSURE	
	INITIAL	B	SECTION VARIATION INDEX (A)01,02	DES.ENG: J. KWONG	DATE: 18-DEC-84	DOCUMENT NUMBER	
			(B)	RESP.ENG.: J.KWONG	DATE: 18-DEC-84	SIZE	CODE
			(C)			NUMBER	REV
			(D)			K	PL
			(E)	MFG.ENG.: J. EDWARDS	DATE: 18-DEC-84	7022380-0-DBP B	
			(F)	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	RELEASE DATE: 06-MAR-85	
				B-DD-7022380-0-DBU	B-DD-7022380-0-DBU	FILE NAME:	EDIT #
						ML752B.PLS	23

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AUTOMATED BY: PRETEST.SR557				MIN			QTY	PER	VARIATION
LINE	ITEM	TOP DOCUMENT	PART NUMBER	REV	DESCRIPTION		01	02	
VARIATION REVISION LEVEL:							A1	A1	
31	31		9009701-00		SCREW,SEMS PAN PHIL--	6-	48	48	
32	32		9006560-00		NUT,HEX EXT TOOTH LCKWSHR	6-32	4	4	
33	33		9007651-00		WASHER,LOCK	EXTERNAL STEEL	1	1	
34	34		9010016-00		CLAMP, CABLE, ADH BACK		5	5	
35	35		9006565-00		NUT,HEX EXT TOOTH LCKWSHR	10-32	1	1	
36	36		9010075-03		SCREW,TAP HEXWW	THD RL 6-	4	4	
37	37		9907588-01		ENVELOPE,CLEAR ADH BACK		1	1	
38	38	D-AR-7022380-0-DBU			BA123-A SYSTEM ARRANGEMENT		REF	REF	
39	39	K-SP-BA123-A-DBF			ENG. SPEC BA123-A		REF	REF	
40	40		1700083-00		PWR CORD,TERM 3-14 SJT	125	1	-	
41	41		3615087-04		LABEL,"DANGER-HIGH CURRENT"		1	1	
42	42		3617905-16		LABEL,WARNING EQUIP. RATING,100-		1	-	
43	43		3617905-17		LABEL,WARNING EQUIP. RATING,220-		-	1	
44	44	A-PS-3624471-0-0	3624471-01		LABEL,BUSINESS COMP BA123-A2		1	-	
45	45	A-PS-3624471-0-0	3624471-02		LABEL,BUSINESS COMP BA123-A3		-	1	

46 NOTE: ITEMS #25 AND #40 NOT SHOWN ON FIELD OF DWG.

D I G I T A L	TITLE	BA123-A BASIC ENCLOSURE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
				K	PL	7022380-0-DBP	B

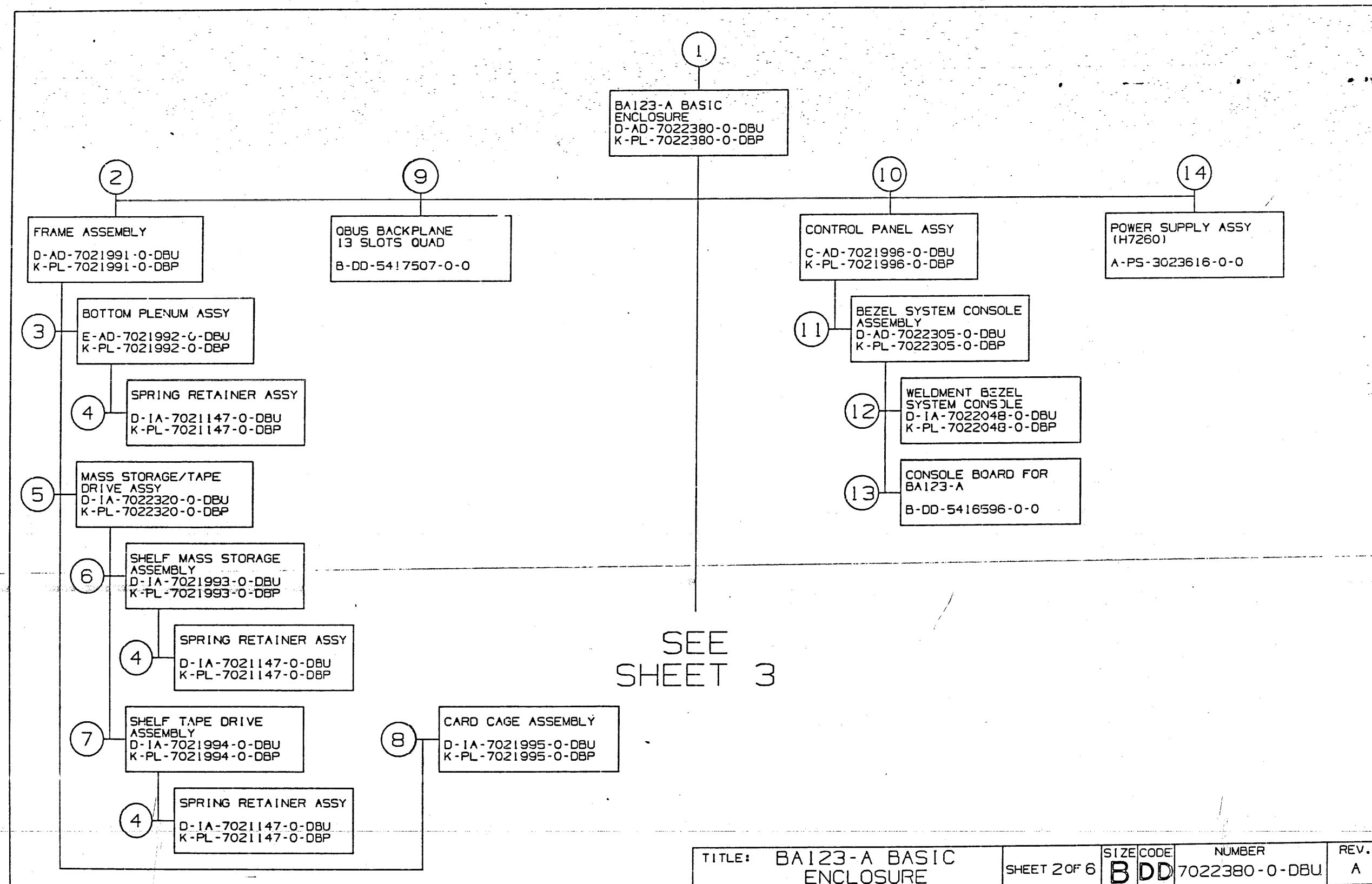
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VAR	TITLE
-----	-------

7022380-01	BA123-A BASIC ENCLOSURE (120 V)
7022380-02	BA123-A BASIC ENCLOSURE (240 V)

REVISIONS		USED ON OPTION/MODEL	DRN. D. HEALY	DATE 29 NOV 84	TITLE digital									
CHK	CHANGE NO.	1730B-C2	CHK'D D. HEALY	DATE 08MAR85	BA123-A BASIC ENCLOSURE									
	INITIAL	1730B-C3	PROJ. ENG. J. KWONG	DATE 08MAR85										
			PROD. J. EDWARDS	DATE 08MAR85										
SHEET 1 OF 6		TOP DOC. B-DD-7022380-0-DBU	DIST.		SIZE B	CODE DD	NUMBER 7022380-0-DBU				REV. A			

MLO 1



TITLE: BAI23-A BASIC ENCLOSURE

SHEET 2 OF 6

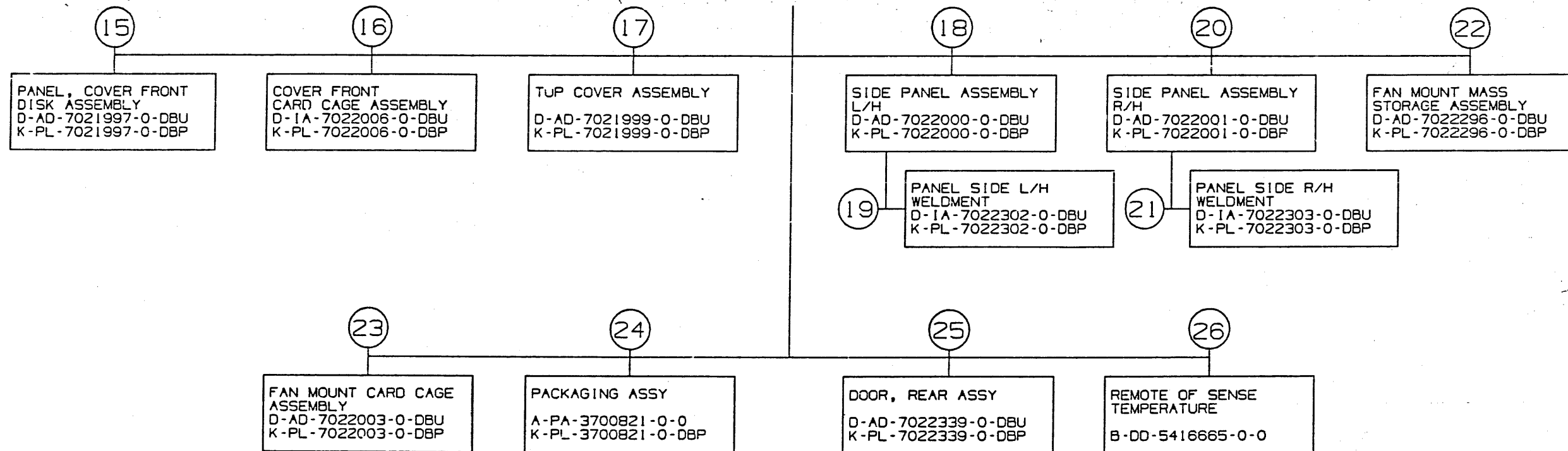
SIZE CODE
B DD

NUMBER
7022380-0-DBU

REV.
A

ML01

CONT FROM SHEET 2



TITLE: BA123-A BASIC
ENCLOSURE

SHEET 3 OF 6

SIZE CODE
B DD

NUMBER
7022380-0-DBU

REV.
A

MLO1

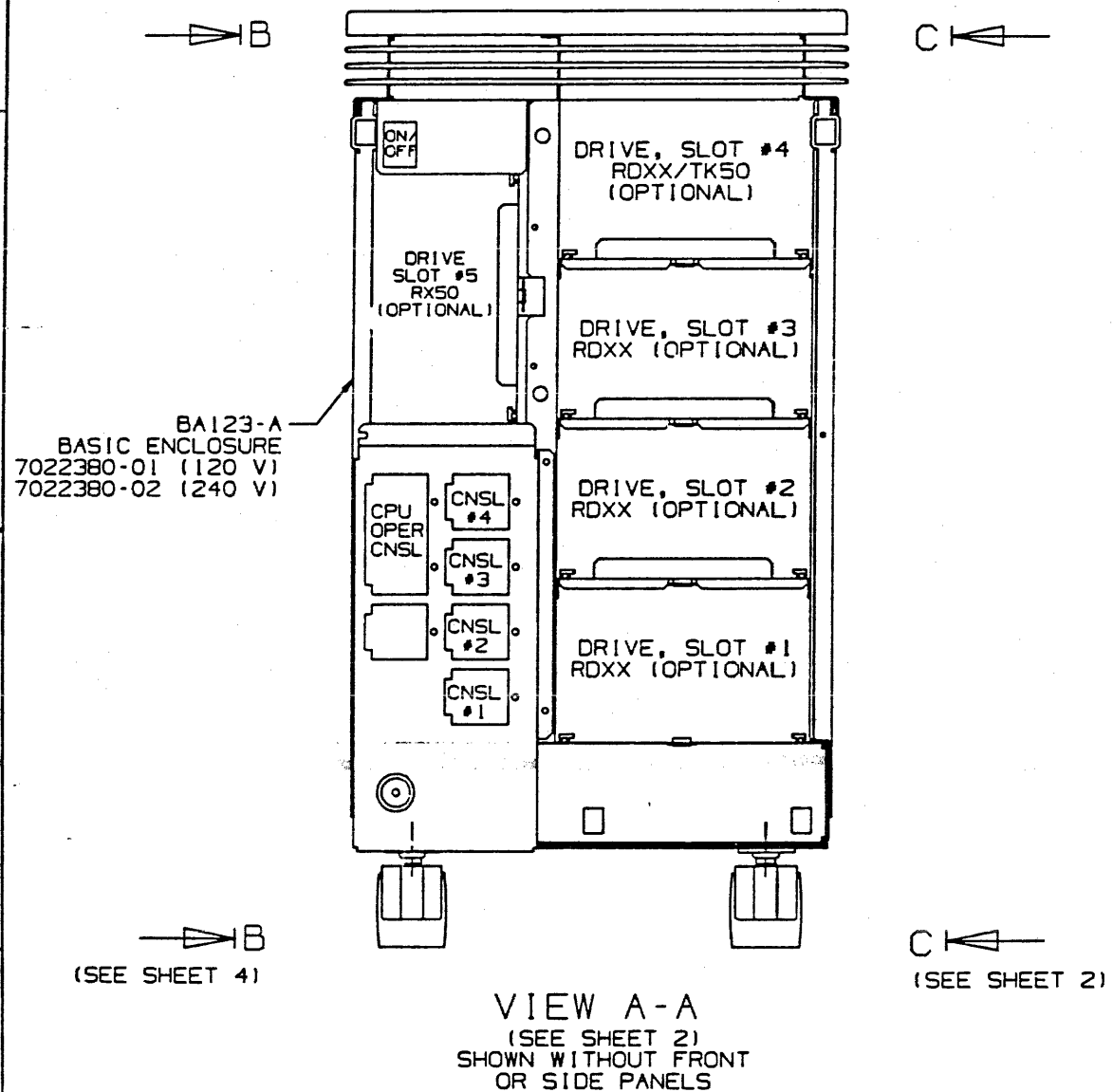
[illegible]

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
					A-SS-7022048-0-1	SILK SCREEN	M
7	D-1A-7021994-0-DBU	SHELF TAPE DRIVE ASSEMBLY	M	13	B-DD-5416596-0-0	CONSOLE BOARD FOR BA123-A	E/M
	K-PL-7021994-0-DBP	SHELF TAPE DRIVE ASSEMBLY (PL)	M				
	D-MD-7430746-0-DBU	SHELF TAPE DRIVE	M				
	D-MD-7427554-0-DBU	HALF SLIDE	M				
				14	A-PS-3023616-0-0	POWER SUPPLY ASSEMBLY (H7260)	E/M
8	D-1A-7021995-0-DBU	CARD CAGE ASSEMBLY	M				
	K-PL-7021995-0-DBP	CARD CAGE ASSEMBLY (PL)	M				
	E-1A-7430690-0-DBU	FRAME CARD CAGE	M	15	D-AD-7021997-0-DBU	PANEL COVER FRONT DISK ASSY	M
	K-PL-7430690-0-DBP	FRAME CARD CAGE (PL)	M		K-PL-7021997-0-DBP	PANEL COVER FRONT DISK ASSY (PL)	M
	D-MD-7429632-0-DBU	CARD GUIDE 12 SLOT	M		E-MD-7430704-0-DBU	PANEL, FRONT	M
	D-1A-7430693-0-DBU	BAFFLE, FCC	M		C-MD-7431191-0-DBU	BRACKET, STUD	M
	K-PL-7430693-0-DBP	BAFFLE, FCC (PL)	M		E-MD-7430708-0-DBU	DOOR, FRONT	M
					C-MD-7431178-0-DBU	BRACKET, DOOR HINGE	M
					D-MD-7431477-0-DBU	PANEL, FILLER	M
9	B-DD-5417507-0-0	OBUS BACKPLANE 13 SLOTS QUAD	E/M				
10	C-AD-7021996-0-DBU	CONTROL PANEL ASSEMBLY	M	16	D-1A-7022006-0-DBU	CARD CAGE FRONT COVER ASSY	M
	K-PL-7021996-0-DBP	CONTROL PANEL ASSEMBLY (PL)	M		K-PL-7022006-0-DBP	CARD CAGE FRONT COVER ASSY (PL)	M
	E-1A-7430705-0-DBU	PANEL CONTROL	M		E-1A-7430691-0-DBU	COVER FRONT CARD CAGE	M
	K-PL-7420705-0-DBP	PANEL CONTROL (PL)	M		K-PL-7430691-0-DBP	COVER FRONT CARD CAGE (PL)	M
	C-MD-7431171-0-DBU	BEZEL, BLANK	M		D-MD-7430828-0-DBU	SUPPORT MODULES	M
					D-MD-7431479-0-DBU	SUPPORT, MODULE REMOVEABLE	M
11	D-AD-7022305-0-DBU	BEZEL, SYSTEM CONSOLE ASSY	E/M	17	D-AD-7021999-0-DBU	TOP COVER ASSEMBLY	M
	K-PL-7022305-0-DBP	BEZEL, SYSTEM CONSOLE ASSY (PL)	E/M		K-PL-7021999-0-DBP	TOP COVER ASSEMBLY (PL)	M
	A-PS-1700860-0-0	CABLE ASSY, CONSOLE BACKPLANE	E/M		D-1A-7430707-0-DBU	COVER, TOP	M
					K-PL-7420707-0-DBP	COVER, TOP (PL)	M
					C-MD-7423357-0-DBU	KEY BUTTON	M
					D-1A-7431045-0-DBU	BRACKET, COVER	M
					K-PL-7431045-0-DBP	BRACKET, COVER (PL)	M
12	D-1A-7022048-0-DBU	WELDMENT, BEZEL SYS CONSOLE	M				
	K-PL-7022048-0-DBP	WELDMENT, BEZEL SYS CONSOLE (PL)	M				
	D-MD-7431169-0-DBU	BRACKET, SUPPORT SYSTEM CONSOLE	M				
	D-MD-7431173-0-DBU	BEZEL, SYSTEM CONSOLE	M				
TYPE: E ELECTRICAL M MECHANICAL E/M ELECTRO/MECHANICAL				TITLE: BA123-A BASIC ENCLOSURE			
				SHEET 5 OF 6			
				SIZE CODE BDD			
				NUMBER 7022380-0-DBU			
				REV. A			

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NOTES:

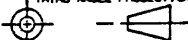
1. RDX SIGNAL DISTRIBUTION (M9058) IS SHOWN FOR REF ONLY AND NOT PART OF BA123-A. (SEE SHEET 5)
2. FOR REAR PANEL RULES AND CONFIGURATION SEE SHEET 3.
3. FOR POWER HARNESS WIRING DIAGRAM SEE SHEET 6.
4. FOR MODULE RULES SEE SHEET 6.
5. MASS STORAGE CONFIGURATION RULES:
THE POWER SUPPLY IN THE BA123-A ENCLOSURE CAN POWER UP TO FOUR OF THE FIVE DRIVE SLOTS ONLY.
THE FOLLOWING ARE SOME OF THE COMBINATIONS:
 - (4) RD52'S
 - RX50, TK50, (2) RD52'S
 - TK50, (3) RD52'S



FILE NAME: AR7022380-0-DBUA

PLOT AT .5000

		DESCRIPTION		DRAWING NO.		PART NO.		ITEM NO.									
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 1141)															
INCHES TOLERANCES		ANGLES 15° 30°		APPLICABLE DIMENSION RANGE		DIMENSION RANGE IN INCHES											
						OVER 0 TO 0.2		OVER 0.2 TO 0.5		OVER 0.5 TO 1.0		OVER 1.0 TO 2.0		OVER 2.0 TO 4.0		OVER 4.0 TO 6.0	
.X1 ± .1		SURFACE QUALITY		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET	
.XX1 ± .02				HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET	
.XXX ± .005		WORKSHOPS		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET	
FIRST HIGHER ASST		QUANTITY & VARIATION		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET		HIGHER SHEET	
D-AD-BA123-A-DOP																	

THIRD ANGLE PROJECTION		DATE		TITLE	
		08-JAN-85		BA123-A	
DO NOT SCALE DRAWING		DATE		SYSTEM	
		08-MAR-85		ARRANGEMENT	
REMOVE BURRS AND BREAK SHARP CORNERS		DATE			
		08-MAR-85			
MATERIAL		DATE			
--//--		08-MAR-85			
		DATE			
		08-MAR-85			
FINISH		DATE			
--//--		08-MAR-85			
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digital

BA123-A
SYSTEM
ARRANGEMENT

DOCUMENT NUMBER		
SIZE	CODE	NUMBER
D	AB	7022380-0-DBU

SCALE	1/2	SHEET 1	OF 8
-------	-----	---------	------

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7022380-0-DBU

7022380-0-DBU

RDXX #4/TK50

RDXX #3

RDXX #2

RDXX #1

(SEE SHEET 1)

(SEE SHEET 3)

VIEW C-C
(SEE SHEET 1)
SHOWN WITHOUT FRONT
OR REAR PANELS

REVISIONS		
CHK	CHANGE NO	REV

TITLE BA123-A
SYSTEM ARRANGEMENT

PLOT AT .5000

SIZE CODE D AR NUMBER 7022380-0-DBU REV A

SCALE 1/2 SHEET 2 OF 6 DIST

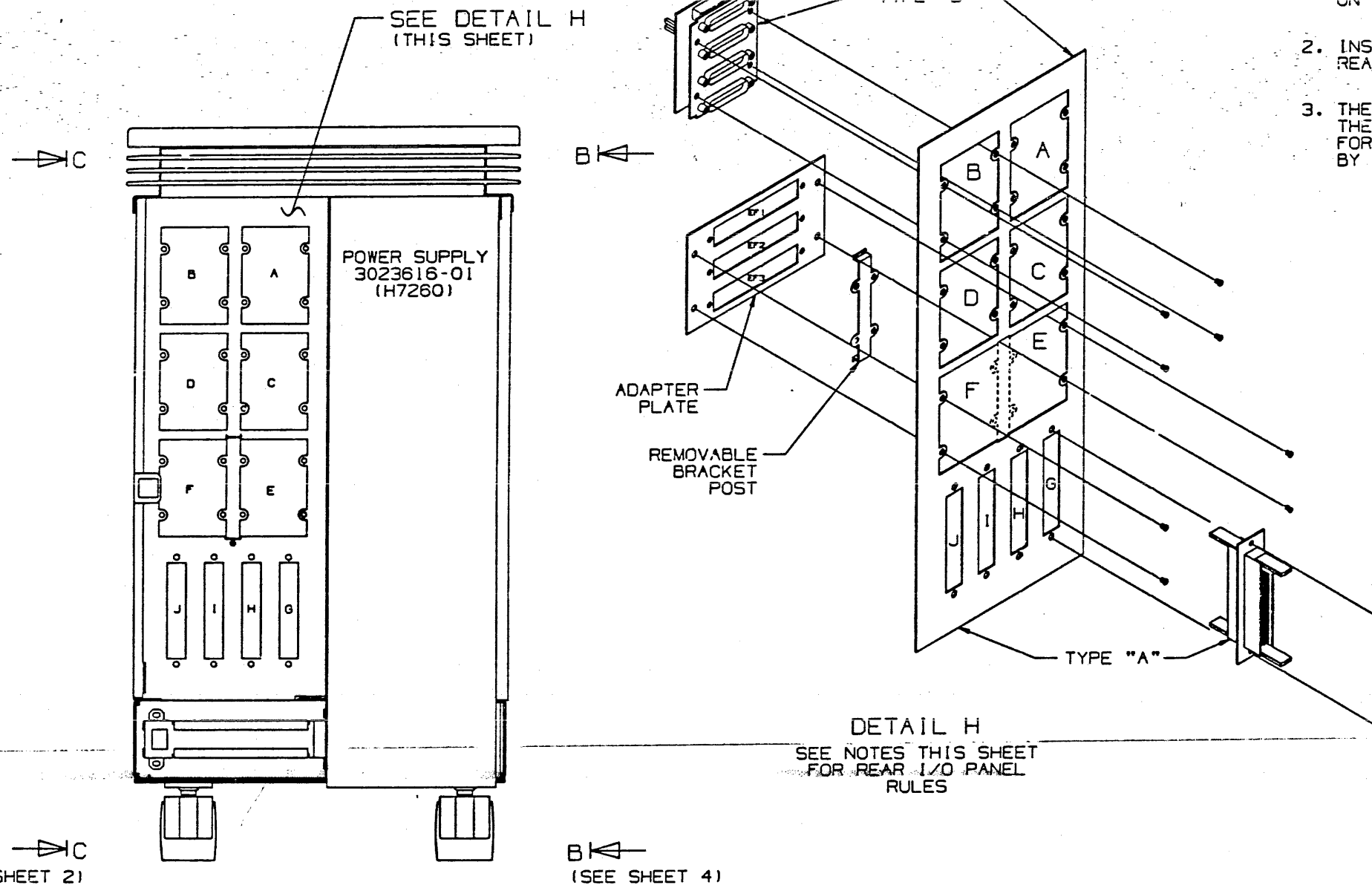
ML01

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NOTES:

REAR I/O PANEL CONNECTION RULES:

1. INSTALL TYPE "B" (2.5" X 3.2") FILTER PANEL ON TO THE REAR I/O PANEL IN THIS SEQUENCE:
A → C → E → B → D → F
2. INSTALL TYPE "A" (1" X 4") FILTER PANEL ON TO REAR I/O PANEL IN THIS SEQUENCE:
G → H → I → J
3. THERE IS A REMOVABLE BRACKET POST BETWEEN THE BOTTOM 2 TYPE "B" CUTOUTS. THIS ALLOWS FOR THE ADDITION OF 3 MORE TYPE "A" CUTOUTS BY INSTALLING AN ADAPTER PLATE (7427720-01)



VIEW D-D
(SEE SHEET 2)
SHOWN WITHOUT REAR
OR SIDE PANELS

DETAIL H
SEE NOTES THIS SHEET
FOR REAR I/O PANEL
RULES

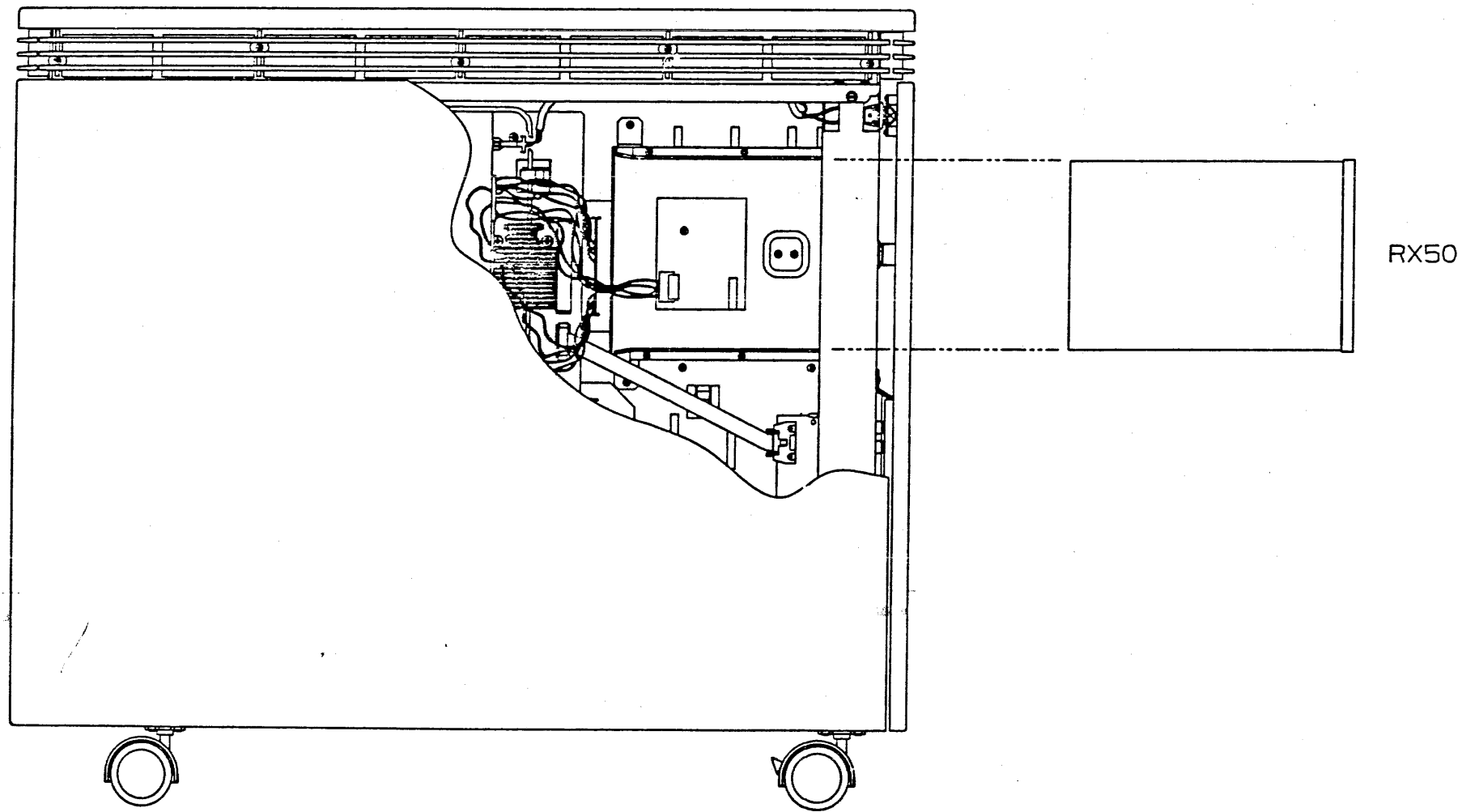
REVISIONS		
CHK	CHANGE NO	REV

PLOT AT .5000

TITLE BAI23-A SYSTEM ARRANGEMENT		SIZE CODE D	NUMBER AR 7022380-0-DBU	REV A
SCALE 1/2	SHEET 3	OF 8	DIST	

MLC1

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VIEW B-B
(SEE SHEET 1)
(TK50 INSTALLATION)

REVISIONS		
CHK	CHANGE NO	REV

TITLE	BA123-A SYSTEM ARRANGEMENT	SIZE/CODE	D/AR	NUMBER	7022380-0-DBU	REV	A
SCALE	1/2	SHEET	4	OF	6	DIST	

PLOT AT .5000

MLO1

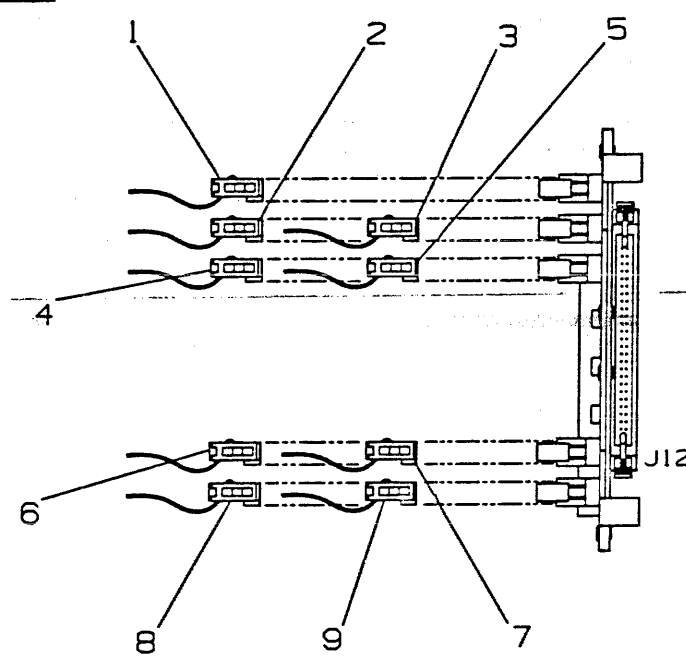
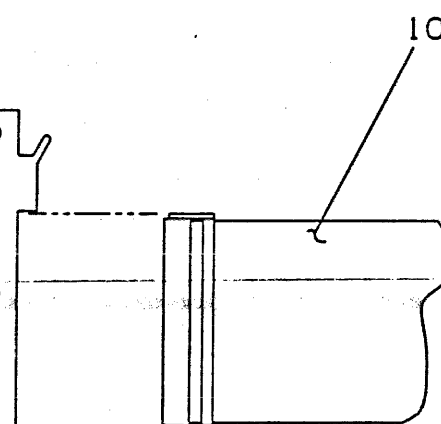
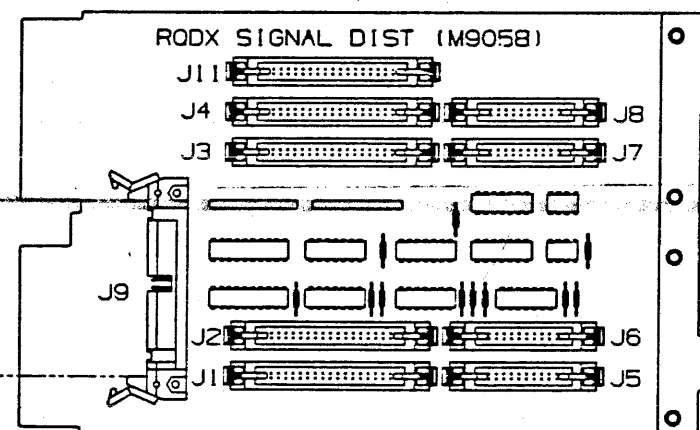
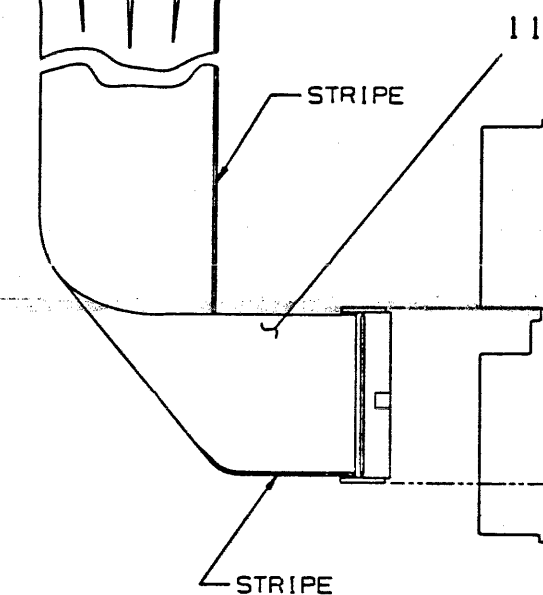
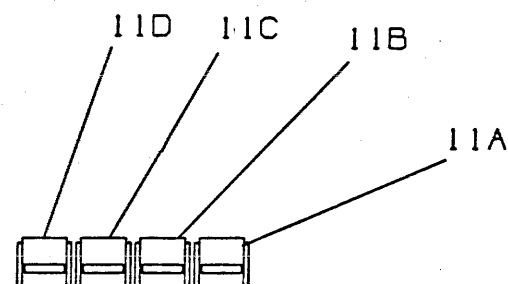
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CABLE LEGEND

ITEM	PART NO.	FROM	TO
1	1700867-01	M9058-J11	RX50
2	1700286-01	M9058-J4	RDXX#4
3	1700282-01	M9058-J8	RDXX#4
4	1700286-01	M9058-J3	RDXX#3
5	1700282-01	M9058-J7	RDXX#3
6	1700286-01	M9058-J2	RDXX#2
7	1700282-01	M9058-J6	RDXX#2
8	1700286-01	M9058-J1	RDXX#1
9	1700282-01	M9058-J5	RDXX#1
10	1700861-01	M9058-J12	RDX2 MODULE
11	1700862-01	M9058-J9	SEE BELOW
11A			RDXX#1 CONSOLE
11B			RDXX#2 CONSOLE
11C			RDXX#3 CONSOLE
11D	1700862-01	M9058-J9	RDXX#4 CONSOLE

NOTES:

1. ITEM #S SHOWN ARE FOR REF ONLY (FOR CABLE HOOK UP ONLY).



REVISIONS		
CHK	CHANGE NO	REV

PLOT AT 1.000

TITLE	BA123-A	SIZE/CODE	NUMBER	REV
	SYSTEM ARRANGEMENT	D AR	7022380-0-DBU	A
SCALE	1/1	SHEET	5 OF 6	DIST

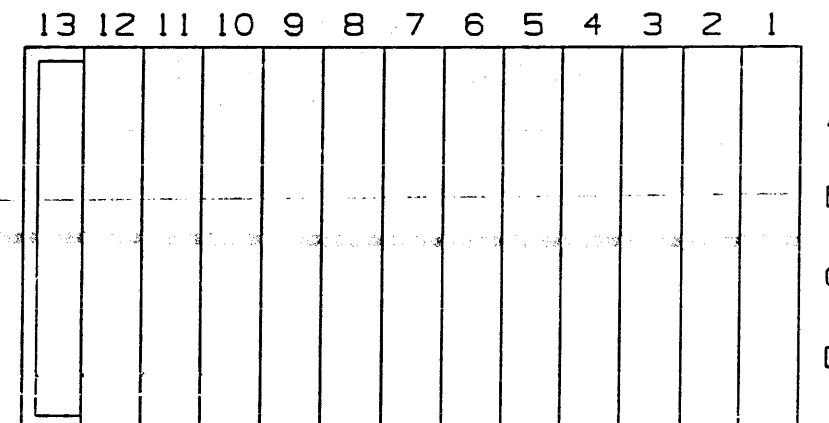
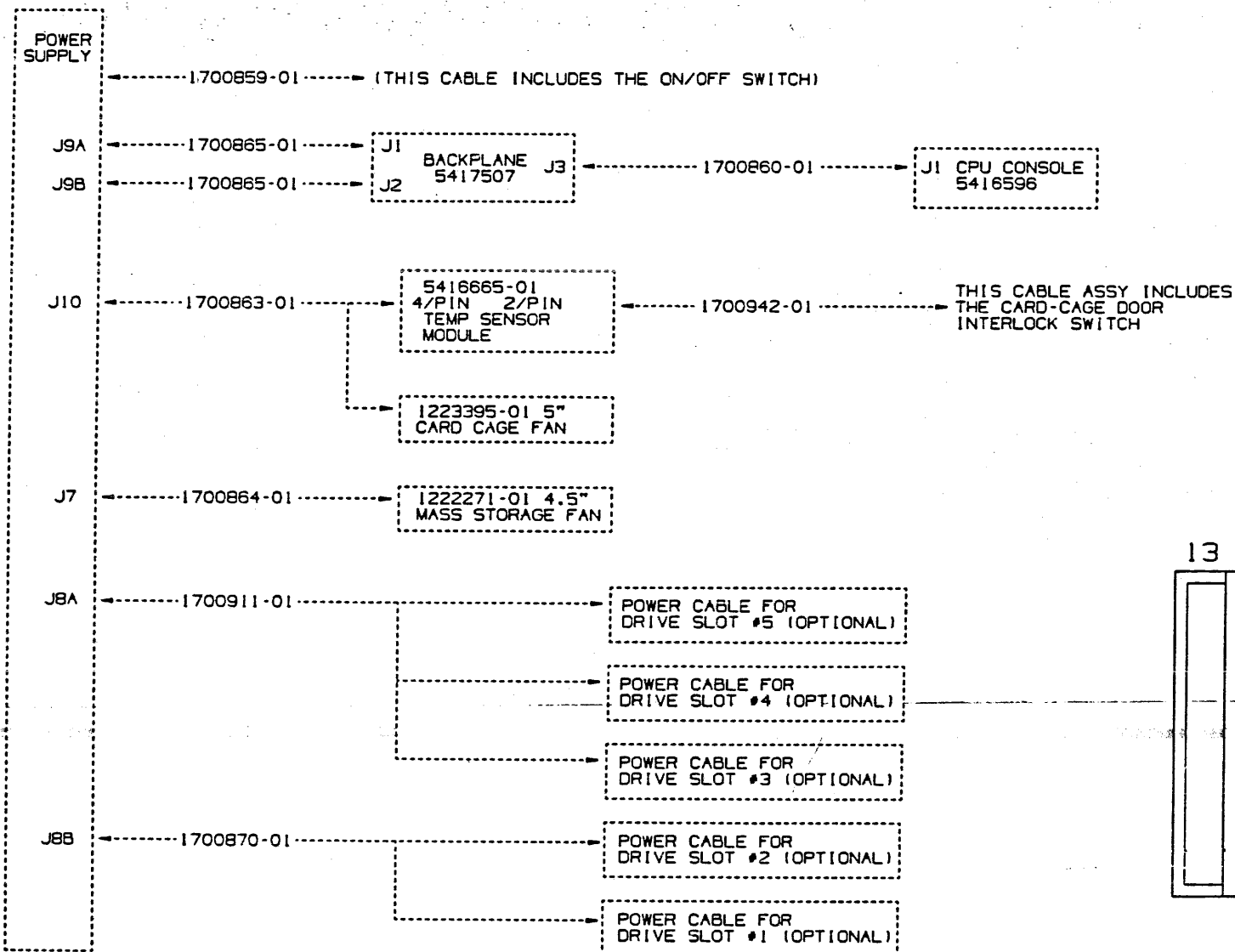
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NOTES: BACKPLANE CONFIGURATION RULES

WHEN CONFIGURING THE BACKPLANE, THE FOLLOWING RULES MUST BE REFERENCED:

1. ALL Q-BUS OPTIONS ARE INSTALLED BELOW THE CPU MODULE.
2. QUAD OPTIONS MAY BE INSTALLED ANYWHERE BELOW THE CPU MODULE.
3. DUAL OPTIONS CAN BE INSTALLED ANYWHERE BETWEEN SLOT 5-12 (i.e. AB OR CD ROWS) AND BETWEEN SLOT 1-4 ON THE AB ROWS BELOW THE CPU. NO DUAL OPTIONS MAY BE INSTALLED IN SLOTS 2 THROUGH 4 ON THE CD ROWS.
4. DUAL OPTIONS ALONE IN A SLOT REQUIRE A M9047 GRANT CARD ADJACENT TO THEM (A OR C ROWS).
5. SLOT 13 RESERVED FOR DIGITAL USE ONLY.



BACKPLANE MODULE UTILIZATION- SEE RULES THIS SHEET

BA123-A BASIC ENCLOSURE POWER HARNESS WIRING DIAGRAM

REVISIONS		
CHK	CHANGE NO	REV

TITLE BA123-A SYSTEM ARRANGEMENT		SIZE CODE D	NUMBER AR 7022380-0-DBU	REV A
SCALE 1/1	SHEET 6 OF 6	DIST		

PLOT AT 1.000

ML01

[illegible]

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
						01	A1	
VARIATION REVISION LEVEL:						A1		
1	1	D-MD-5017506-0-0	5017506-01		ETCHED BOARD	1		
2	2		1214886-05		PCB HEADER 10POS(2X 5).100CC	1		J2
3	3		1215699-07		PCB,HEADER 18POS(1X18).156CC STR	2		J1,J3
4	4		1217535-05		SKT,IC 13PIN SIP TIN SOLD	4		XZ1-XZ4
5	5		7417041-00		288 PIN CONNECTOR	6		J4-J9
6	6		9000033-12		SPACER,THREADED HEX ALUM 8-3	6		
7	7		9006120-06		SCREW, FILL POZI 8-	14		
8	8		9006035-01		SCREW,MACH PAN PHIL 8-	6		
9	9		9009246-00		SPACER,THREADED HEX ALUM 8-3	8		
10	10		7411881-01		DECAL	1		
11	11		1323505-01		R NETWORK 180/390 2.0 % 13PIN	4		Z1-Z4
12	12		7417042-00		72 PIN CONNECTOR	2		J10-J11
13	13		3623593-01		LABEL,BLANK,PAPER,PIN FEED	1		
14	14		9905016-03		CARTON,DIE CUT,SELF LOCK W/FOAM	1		

REVISION HISTORY			BASIC PART NO: 5417507			DRN: S.MANSOR			DATE: 07-JUN-84			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A OF A			CHK'D: F.GAROFALO			DATE: 1-MAR-85			TITLE PARTS LIST		
---	INITIAL	A	SECTION VARIATION INDEX						DATE: 1-MAR-85			13 SLOT BACKPLANE		
			[A] 01											
			[B]											
			[C]			DES.ENG: T.ORR			DATE: 1-MAR-85			DOCUMENT NUMBER		
			[D]									SIZE CODE NUMBER REV		
			[E]											
			[F]			RESP.ENG.: T.ORR			DATE: 1-MAR-85			K PL 5417507-0-DBP A		
			[H]											
			[J]											
			[K]			MFG.ENG.: R.BELIVEAU			DATE: 1-MAR-85			RELEASE DATE: 04-MAR-85		
			[L]											
			[M]			ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:			FILE NAME:		
			[N]			E-UA-5417507-0-0			B-DD-5417507-0-0			TW261A.PLS		
												EDIT #		
												4		
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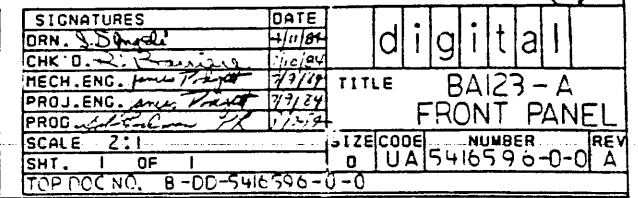
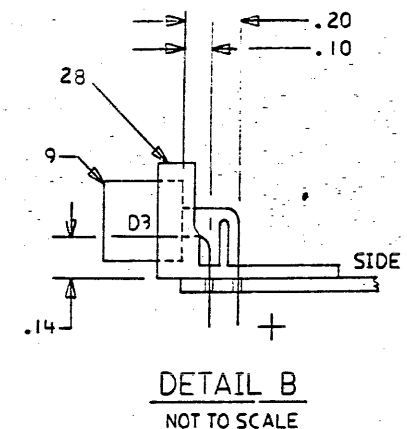
LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
						01	A2	
1	1	D-MD-5016595-0-0	5016595-01		CIRCUIT DRILL AND ETCH	1		
2	2		1000021-00		220.0 MMF 100V 5%200PPM MICA	1		C1
3	3		1005636-00		22 MFD 6V 10% S.TANT	1		C2
4	4		1012784-00		.047 MFD 50V +80-20% CER	2		C3,C4
5	5		1012312-01		.47 MFD 50V +90-20% CER	1		C5
6	6		1017472-00		10 MFD 35V +75-10% AL EL	1		C6
7	7		1100114-00		PIV= 25 IO=135 MA	1		D1
8	8		1119827-01		LED,SUPERBRIGHT,RED T1 PKG	1		D2
9	9		1116622-02		LED 2.0MCD@25M	1		D3
10	10		1121248-01		GREEN LED T1 4MCD 1"LEADS	1		D4
11	11		1110864-00		LED 2MCD@10MA	2		D5,D6
12	12		1218945-03		SW,PB,LT 1PST NO-MOMENTARY .25A	1		S1
13	13		1218945-01		SW,PB,LT 1PST NO-MAINTAIN .25A	1		S2
14	14		1217310-07		SW,DIP 2POS/1PST 5VDC100MA S	1		S3
15	15		1216832-07		*** THIS ITEM IS NOT USED ***	-		
16	16		1219918-01		SKT,SWITCH 4PIN RT ANGLE	2		XS1,XS2
17	17		1300250-00		150.0 .25 W 5.0 % CF	1		R1
18	18		1311523-00		110.0 .25 W 5.0 % CF	1		R2
19	19		1300271-00		220.0 .25 W 5.0 % CF	1		R3
20	20		1302388-00		2.0 K .25 W 5.0 % CF	4		R4,R5,R6,R7
21	21		1300479-00		10.0 K .25 W 5.0 % CF	4		R10-R13
22	22		1302514-00		39.0 K .25 W 5.0 % CF	2		R14,R15
23	23		1302466-00		100.0 K .25 W 5.0 % CF	1		R16
24	24		1300391-00		1.50 K .25 W 5.0 % CF	2		R8,R9
25	25		1914987-00		8641-2 TRANSCEIVER,UNIBUS,QU	1		E1
26	26		1912858-00		LS221 ONE SHOT-DUAL,SCHMIT	1		E2
27	27		1914156-01		LM 393AN VOLT COMPARATOR,DUA	1		E3
28	28	D-MD-7430089-0-DBU	7430089-01		HOLDER,L.E.D.,SINGLE	1		
29	29		1209941-05		PCB,HEADER 10PIN(2X10).100CC 90D	1		J1
30	30		1209941-04		PCB,HEADER LATCH	1		

REVISION HISTORY			BASIC PART NO: 5416596			DRN: M. FUNARO			DATE: 16-MAR-84			D I G I T A L		
ENG	ECO NUMBER	REV	SECTION A - OF A			CHK'D: D. BARRIERE			DATE: 16-MAR-84			TITLE PARTS LIST		
JP	INITIAL	A	SECTION VARIATION INDEX			DES.ENG: J. PADGETT			DATE: 16-MAR-84			DOCUMENT NUMBER		
JP	5416596-ML001	B	[A] 01			RESP.ENG.: J. PADGETT			DATE: 16-MAR-84			SIZE CODE NUMBER		
			[B]			MFG.ENG.: R. BELIVEAU			DATE: 18-MAY-84			RELEASE DATE: 01-MAY-85		
			[C]			ASSEMBLY NUMBER:			TOP DOCUMENT NUMBER:			FILE NAME:		
			[D]			D-UA-5416596-0-0			B-DD-5416596-0-0			Z9241B.PLS		
			[E]									EDIT #		
			[F]									15		
			[G]											
			[H]											
			[I]											
			[J]											
			[K]											
			[L]											
			[M]											
			[N]											

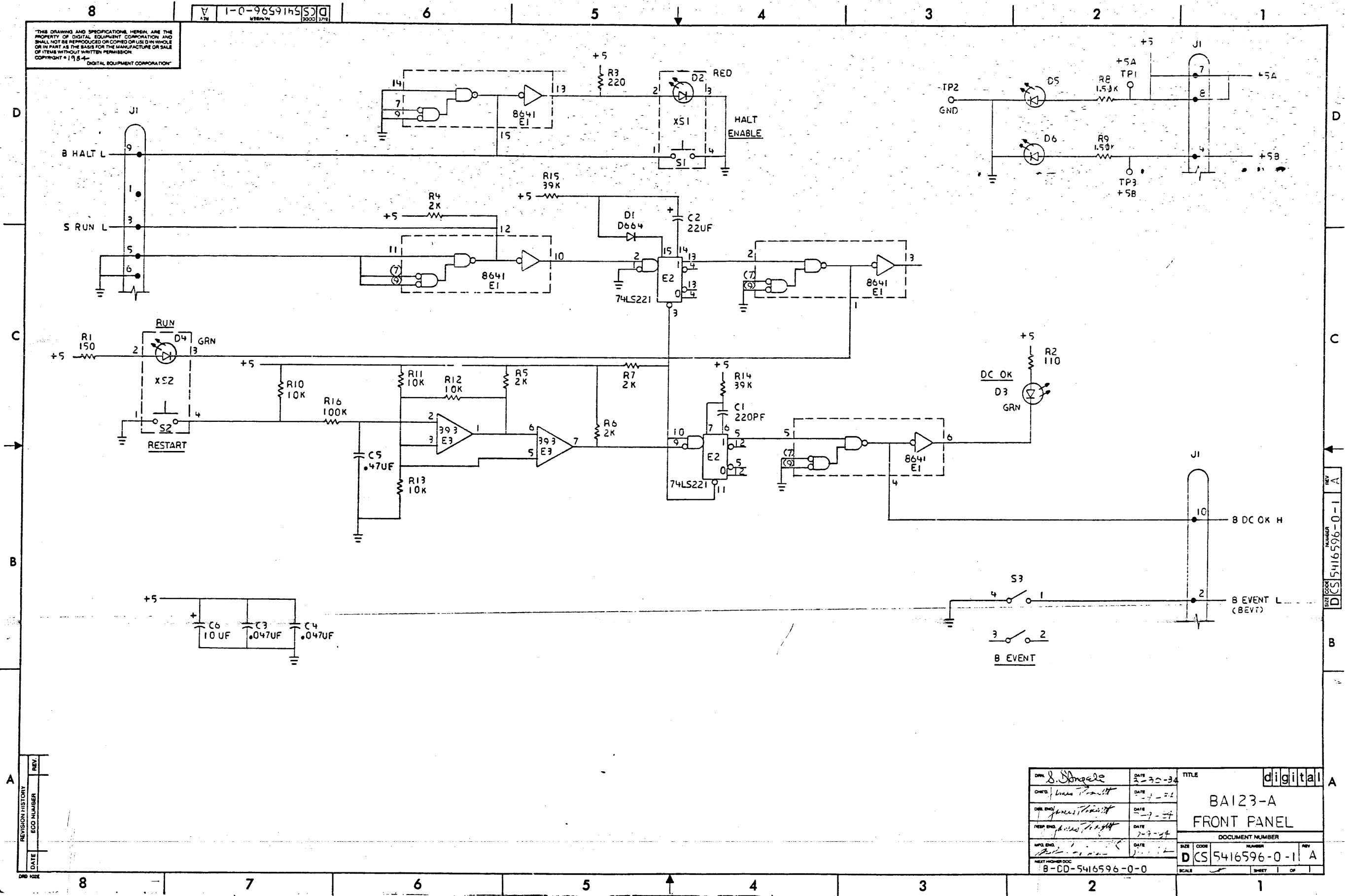
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LINE ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VARIATION 01 A2	REFERENCE DESIGNATOR
31	31	1209941-03		PCB,HEADER LATCH	1	

D I G I T A L	TITLE	BA123-A FRONT PANEL	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
				K	PL	5416596-0-DBP	B



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REV	ECO NUMBER
1	1

DATE	2-20-84	TITLE	digital
CHG. BY	James Wright	DATE	2-2-84
CHK. BY	James Wright	DATE	2-7-84
TRSP. BY	James Wright	DATE	2-9-84
WFO. BY	James Wright	DATE	2-11-84
NEXT HIGHER DOC	B-CD-5416596-0-0	DOCUMENT NUMBER	D CS 5416596-0-1 A
SCALE		SHEET	OF

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DRAWING DIRECTORY

ITEM	DRAWING NO.	NO. OF SHTS	PART NO.	DESCRIPTION	TYPE	REVISIONS											
						A1	A1	A2									
1			5416665-01	TEMP SENSOR BD.		A1	A1	A2									
2	D-UA-5416665-0-0	1		TEMP SENSOR BD.		A	B	C									
3	D-CS-5416665-0-1	1		TEMP SENSOR BD.		A	A	A									
4	K-PL-5416665-0-DBP	1		TEMP SENSOR BD.		A	A	B									
5	K-PC-5416665-0-DBC			P.C. DESIGN DATA BASE		A	A	A									
6			5016664-01	ETCHED CIRCUIT BOARD		B1	B1	B1									
7	B-DD-5016664-0-0	1		DRAWING DIRECTORY		A	A	A									
NOTES:					DD REV	A	B	C									
REVISION HISTORY			CONT. REVISION HISTORY			MADE BY: P.LENNON DATE: 23-OCT-84			d i g i t a l								
ENG	ECO NUMBER	REV	ENG	ECO NUMBER	REV	CHECKED BY: D. BARRIERE DATE: 23-OCT-84			TITLE DRAWING DIRECTORY								
TO	INITIAL	A				DESIGN ENGINEER: T. ORR DATE: 23-OCT-84			TEMP SENSOR BD.								
RL	5416665-ML001	B				RESPONSIBLE ENG: R. LIGENZA DATE: 04-FEB-86											
	5416665-MK002	C				PRODUCTION ENG: NEMI HARRIS DATE: 04-FEB-86											
									SIZE K CODE DD DOCUMENT NUMBER K-DD-5416665-0 REV. C								
									SHEET 1 OF 1 MKO								

AUTOMATED BY VAXKPL (V1.0)

PARTS LIST

SHEET A1 OF A1

LINE	ITEM	TOP DOCUMENT	PART NUMBER	MIN REV	DESCRIPTION	QTY PER VAR/REV	REFERENCE DESIGNATORS
						01	
						A2	
1	1	D-MD-5016664-0-0	50-16664-01		CIRCUIT DRILL & ETCH	1	
2	2		13-24146-01		*** THIS ITEM IS NOT USED ***	-	
3	3		12-23631-03		THERMOSTAT, NO C3 50C	1	
4	4		12-11342-04		MATE-N-LOK 04PIN(1X04).200CC HDR	1	TS1
5	5		12-11342-02		MATE-N-LOK 02PIN(1X02).200CC HDR	1	J1
6	6		90-07254-00		TRANSIPADS #10146	2	J2
7	7		13-25634-01 A		THERMISTOR, EPOXY COATED, 50J0HM 5	1	R1

REVISION HISTORY		KPL MODULE FORMAT		SECTION A OF A		DRN: T. ORR	
ENG	ECO NUMBER	REV	SECTION/VARIATION INDEX	CHK'D	DATE	D I G I T A L	
TO	INITIAL	A	[A] 01	[M]	25-OCT-84	TITLE PARTS LIST	
RL	5416665-MK002	B	[B]	[N]	25-OCT-84	TEMP SENSOR BD	
			[C]	[P]	DES. ENG: T. ORR		
			[D]	[Q]	DATE: 25-OCT-84		
			[E]	[R]	RESP. ENG.: T. ORR	DOCUMENT NUMBER	
			[F]	[S]	DATE: 25-OCT-84	SIZE	CODE
			[G]	[T]		NUMBER	REV
			[H]	[U]		K	PL
			[I]	[V]		5416665-0-D3P	B
			[J]	[W]	MFG. ENG: B. LAIDMAN	RELEASE DATE:	
			[K]	[X]	DATE: 25-OCT-84	RELEASE STATUS: UNDER CHANGE	
			[L]	[Y]			
BASIC PART NUMBER:		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	
5416665		D-UA-5416665-0-0		B-DD-5416665-0-0		MK1283.PLS	
						EDIT #	
						2	

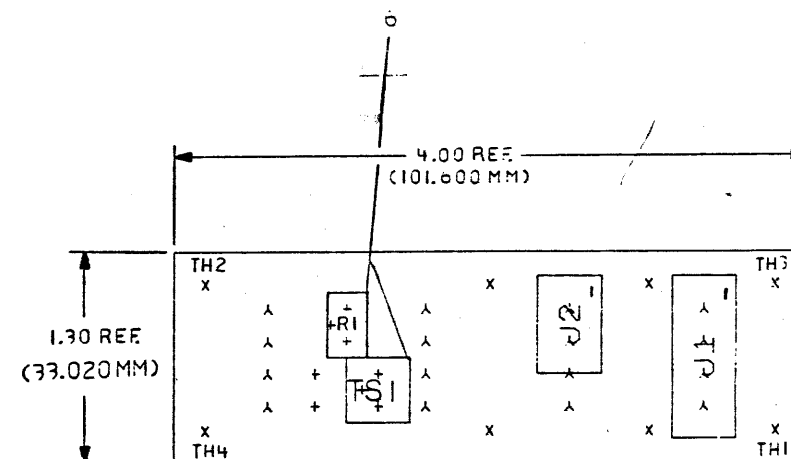
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NOTES:
1: ON SIDE ONE, IDENTIFY POINT OF MANUFACTURE, DATE CODE AND REVISION

STEP E	Y AXIS	STEP	TIMES
REPEAT	X AXIS	STEP	TIMES

CHG	CHANGE NO	REV
1084	INIT	A
54665	B	
ML001	B	
WEINBERG	6/85	
J PADGETT	6/85	
PHASE THREE	C	
A. KRAWIECKI		
R. LIGENZA		
J. K. J.		

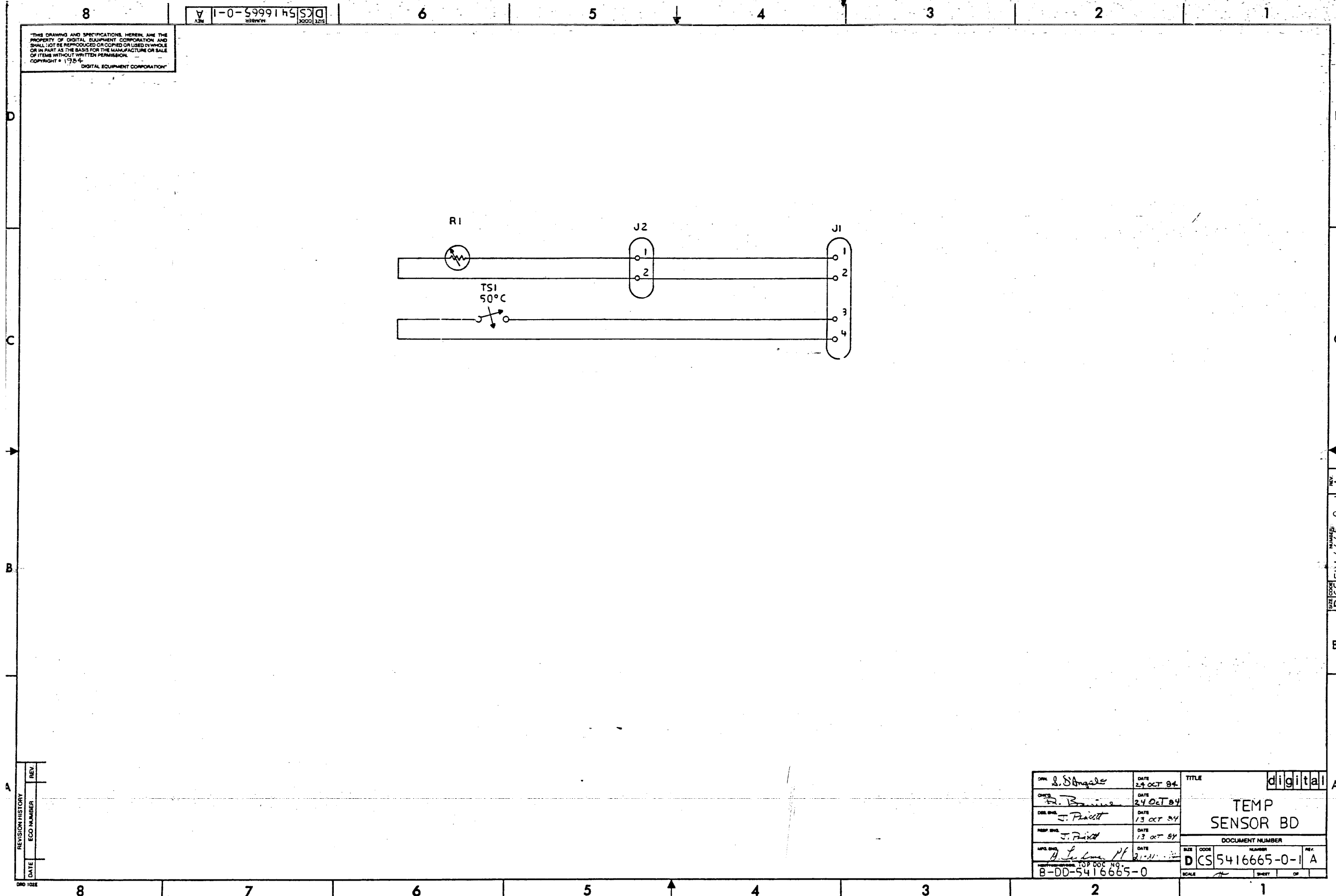


ETCH REV. B1

SIGNATURES	DATE
DRN. J. Padgett	6/85
CHK'D. J. Padgett	6/85
MECH. ENG. J. Padgett	6/85
PROJ. ENG. J. Padgett	6/85
PROD. J. Padgett	6/85
SCALE 2:1	
SHT. 1 OF 1	
TOP D.C. NO. 8-00-541665-0-0	

TITLE	TEMP
SENSOR BD.	
SIZE CODE	0 1A
NUMBER	541665-0-0
REV	C

1 MS# 18-692B



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REV A 1-0-5999155 DCS 5416665-0-1

REVISION HISTORY	
DATE	ECO NUMBER

DESIGNED BY <i>L. S. Angelo</i>	DATE 29 OCT 84	TITLE digital	
CHECKED BY <i>D. Brune</i>	DATE 24 OCT 84	TEMP SENSOR BD	
DATE 13 OCT 84	DATE 13 OCT 84		
DATE 13 OCT 84	DATE 13 OCT 84	DOCUMENT NUMBER	
DATE 21 NOV 84	DATE 21 NOV 84	SIZE CODE D CS	REVISION 5416665-0-1 A
B-DD-5416665-0		SCALE	SHEET OF