DR8-EA
buffered digital I/O
engineering drawings

digital equipment corporation · maynard. massachusetts

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MASTER DRAWING LIST

DWG. NO.	REV. LET.	NO. OF SHEETS	TITLE
A-PL-DR8-EA-Ø	C	1	12 BIT DIGITAL I/O (DR8-EA)
E-CS-M863-Ø-1	#	2	12 BIT BUFFERED DIGITAL I/O
A-SP-DR8-EA-1	REF	12	ENGINEERING SPECIFICATIONS
A-SP-DR8-EA-2	REF	6	ACCEPTANCE SPECIFICATIONS
A-AL-DR8-EA-3		1	ACCESSORY LIST (DR8-EA)

REVISIONS

REV.	DATE	CHG. NO.	APP'D.
A	5/71	DR8E-1	G.B.
B	11/71	DR8E-2	A.W.
C	11/71	DR8E-3	A.W.
D	1/72	LAB8E-13	G.B.

DRN. D. CRABBE	DATE 4/7/71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D.	DATE 4-28-71	
ENG.	DATE 4-28-71	TITLE 12 BIT DIGITAL I/O (DR8-EA)
PROD. ENG.	DATE	
PROD.	DATE	
FIRST USED ON LAB8-E		
SCALE		SIZE A · CODE ML · NUMBER DR8-EA · REV. D
SHEET 1 OF 1		DIST.

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY	D.K. CRABBE	CHECKED	D.K. CRABBE	SECTION	1
DATE	4/7/71	DATE	4/28/71		
ENG	D.K. CRABBE	PROD	P. FAZIO	ISSUED SECT.	1
DATE	4/28/71	DATE	5/3/71		

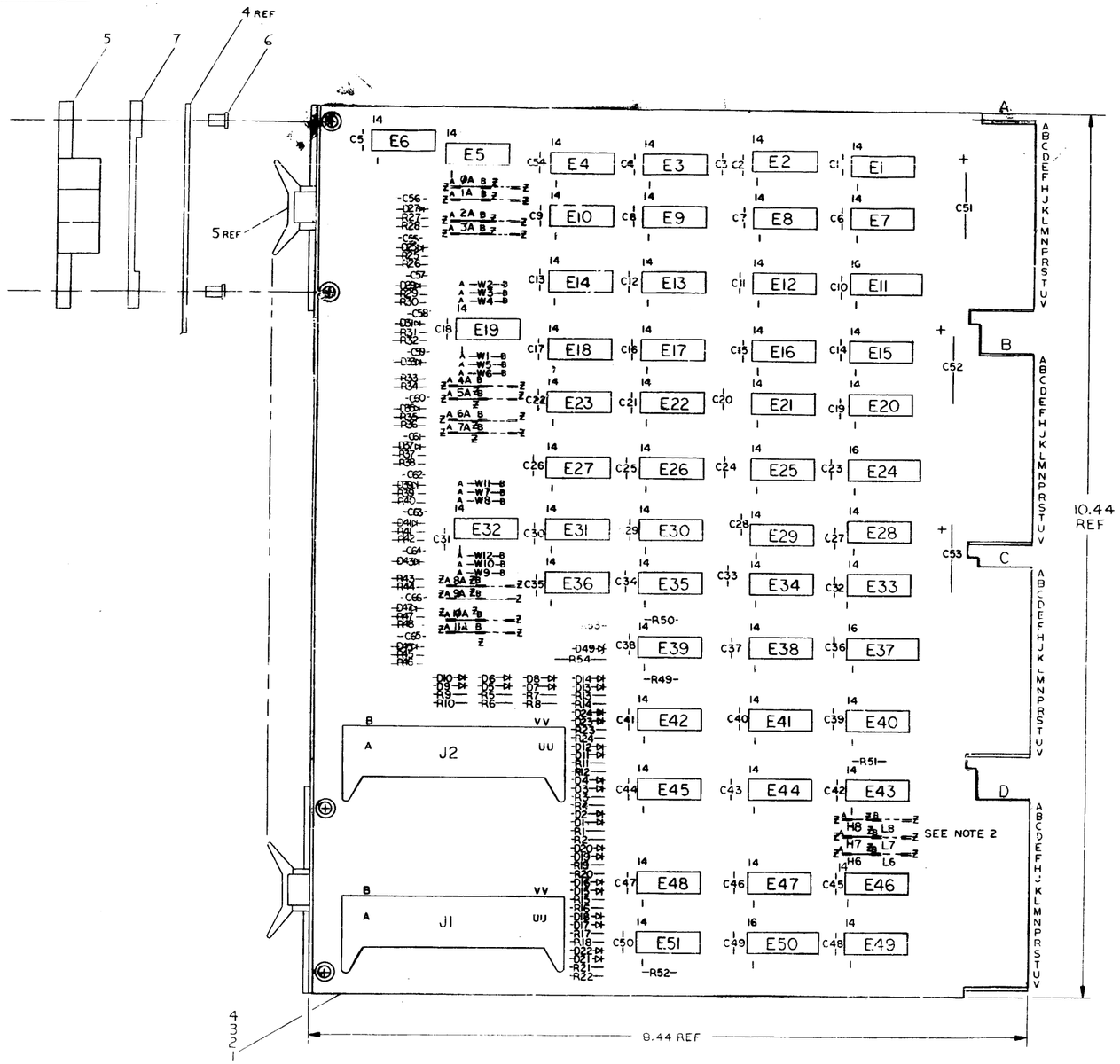
ITEM NO.	DWG NO./PART NO.	DESCRIPTION	QUANTITY/VARIATION
1	M863	12 BIT BUFFERED DIGITAL I/O	1
2	C-IA-7008418-0-0	I/O CABLE ASSY. (BC08S DIAG. JUMPER)	1
3	BC08J-10	I/O CABLE ASSY.	2

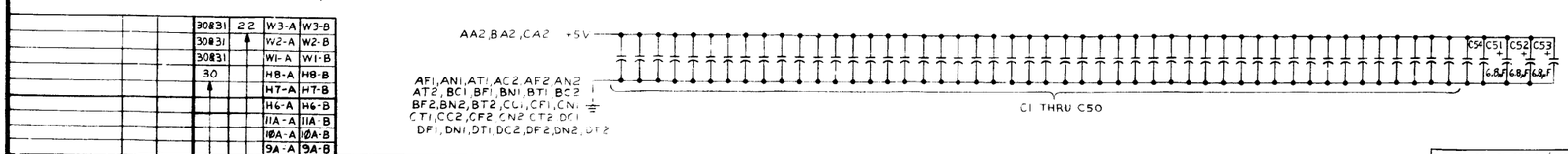
TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV
12 BIT DIGITAL I/O (DR8-EA)		A	PL	DR8-EA-0	C
	SHEET 1 OF 1	DIST.			

DEC FORM DEC [illegible]

NOTES:

1. UNLESS OTHERWISE NOTED:
   CAPACITORS ARE .01µF, 100V, 20% DISC
   DIODES ARE D664
   RESISTORS ARE 1/4W, 5%
2. JUMPERS L6, L7 AND L8 ARE OPTIONAL





			30831	22	W3-A	W3-B							
			30831		W2-A	W2-B							
			30831		W1-A	W1-B							
			30		H8-A	H8-B							
					H7-A	H7-B							
					H6-A	H6-B							
					11A-A	11A-B							
					10A-A	10A-B							
					9A-A	9A-B							
					8A-A	8A-B	30831	22	W12-A	W12-B			
					7A-A	7A-B			W11-A	W11-B			
					6A-A	6A-B			W10-A	W10-B			
					5A-A	5A-B			W9-A	W9-B			
DEC 380	1	8			4A-A	4A-B			W8-A	W8-B			
DEC 8251	8	16			3A-A	3A-B			W7-A	W7-B			
DEC 314	1	8			2A-A	2A-B			W6-A	W6-B			
DEC 8235	8	16			1A-A	1A-B			W5-A	W5-B			
IC TYPE	GND	+5V	30	22	0A-A	0A-B	30831	22	W4-A	W4-B			
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTFULLY. EXCEPTIONS ARE STATED ABOVE			ITEM NO	AWG	FROM PT	TO PT	ITEM NO	AWG	FROM PT	TO PT			
IC PIN LOCATIONS			JUMPER LIST				JUMPER LIST						

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.	
45		SPLIT LUGS	9006735	33
12	C55 THRU C66	CAP 820PF 100V 5% DM	1000027	32
AR		WIRE, BUSSING #22 AWG	9107560-01	31
2	J1, J2	CONNECTOR 40 PIN BERG	1203941	30
AR		TEFLON TUBING	9107265	29
36	D1 THRU D25, D27, D29 D31, D33, D35, D37, D39 THRU D41, D43, D45, D47	DIODE D664	1100114	28
1	D49	DIODE 1N746A 3.3V 5%	1104860	27
51	C1 THRU C50, C54	CAP .01µF 100V 20% DISC	1001610	26
3	C51, C52, C53	CAP 6.8µF 35V 10%	1005306	25
24	R2, R4, R6, R8, R10, R12, R14, R16 R18, R20, R22, R24, R26, R28 R30, R32, R34, R36, R38, R40 R42, R44, R46, R48	RES 47 1/4W 10%	1300204	24
1	R54	RES 82 1/2W 10%	1300225	23
4	R49, R50, R51, R52	RES 1K 1/4W 5%	1300365	22
13	R25, R27, R29, R31, R33, R35 R37, R39, R41, R43, R45, R47, R53	RES 3.3K 1/4W 5%	1300439	21
12	R1, R3, R5, R7, R9, R11, R13 R15, R17, R19, R21, R23	RES 470 1/4W, 5%	1300316	20
13	E1, E4, E7, E10, E13, E15, E18, E20, E23 E28, E31, E33, E36, E51	IC DEC 7474	1905547	19
8	E2, E8, E16, E22, E29, E34, E42, E47	IC DEC 7400	1905575	18
7	E3, E9, E17, E22, E30, E35, E41	IC DEC 7402	1909004	17
6	E12, E25, E38, E40, E43, E49	IC DEC 380	1909485	16
1	E50	IC DEC 8251	1909594	15
6	E13, E14, E26, E27, E39, E44	IC DEC 7404	1909686	14
1	E46	IC DEC 314	1909704	13
2	E45, E48	IC DEC 8881	1909705	12
2	E5, E6	IC DEC 7416	1909928	11
2	E19, E32	IC DEC 7405	1909930	10
3	E11, E24, E37	IC DEC 8235	1909935	9
45		SPLIT LUGS	9006735	8
4		SPACER (CABLE CLAMP)	1202704	7
8		E-Z-LET GS4-11 STIMPSON	9006750	6
4		HANDLE (FLIP CHIP)-MAGENTA	9008337-06	5
1		ETCHED CIRCUIT BOARD	5009339	4
REF		MODULE ECO HISTORY	8-MH-M863-0-5	3
REF		ASSY/DRILLING HOLE LAYOUT	0-MH-M863-0-5	2
REF		X-Y COORDINATE HOLE LOC	X-CO-M863-0-1	1

PARTS LIST

ETCH BOARD REV E

FIRST USED ON OPTION/MOD: LAB R/E

D664	1IN3606		
DEC NO.	EIA NO.	DEC NO.	EIA NO.

SEMICONDUCTOR CONVERSION CHART

WALLACK 11-5-71	WALLACK 11-5-71	ORIGINATED
MH M863-0-0001		E 0
		F 1

DRAWN 12-29-71
CHECKED 12-29-71
ENGR 12-29-71
MFG 12-29-71
QUALITY 12-29-71
PROJ MGR 12-29-71

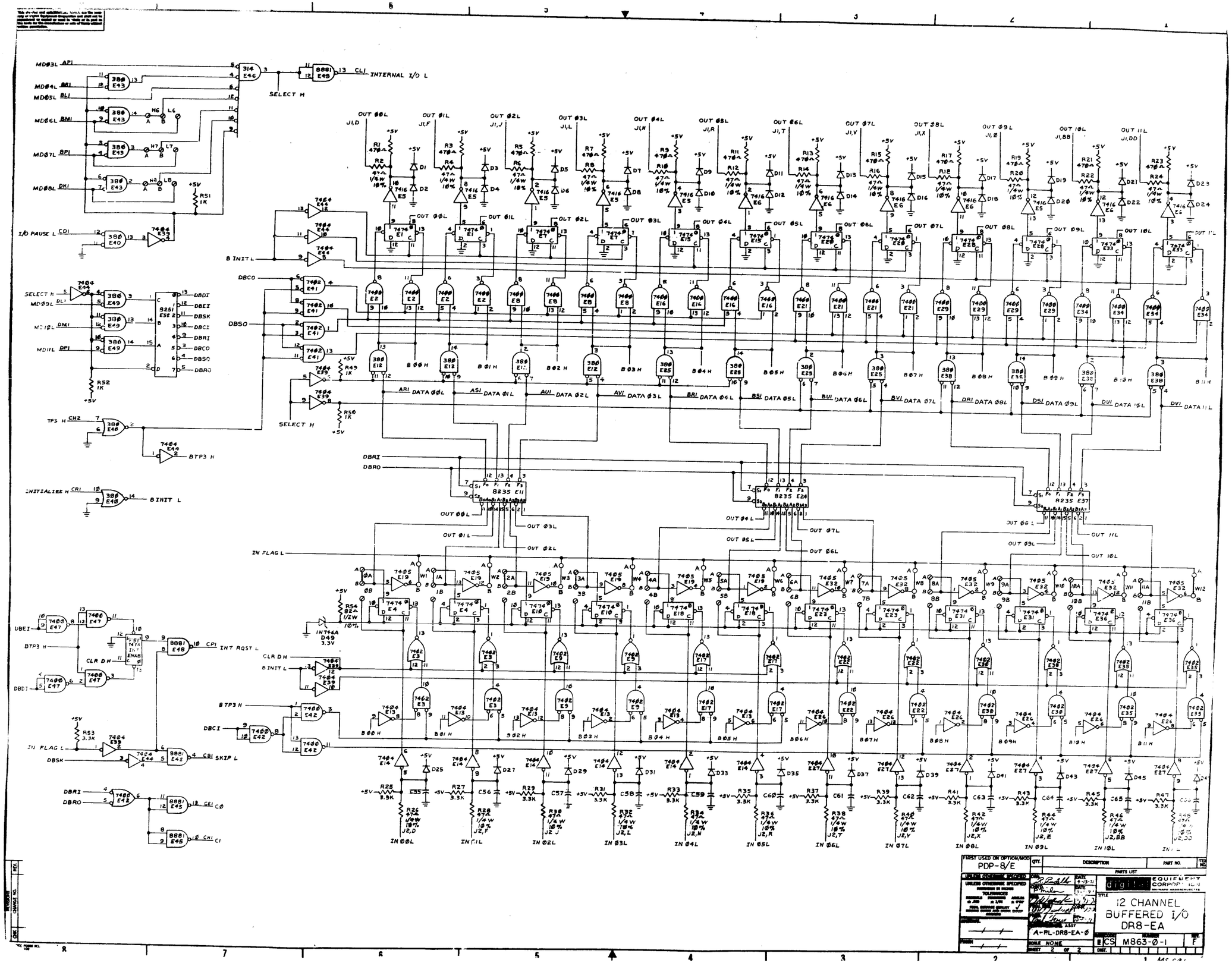
A-PL-DR8-EA-0

SCALE 2/1
SHEET 1 OF 2

[redacted] EQUIPMENT CORPORATION
MARLBORO, MASSACHUSETTS

TITLE
12 CHANNEL
BUFFERED I/O
DR8-EA

SIZE E | CODE CS | NUMBER M863-0-1 | REV F
SHT.


INTERNAL I/O L
SELECT H
I/O PAUSE L
B INIT L
INT RQST L
SKIP L
BTP3 H
INITIALIZE H
IN FLAG L
PDP-8/E
12 CHANNEL
BUFFERED I/O
DR8-EA
A-PL-DR8-EA-0
SCALE NONE
SHEET 2 OF 2
M863-0-1
F

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ACCESSORY LIST

MADE BY K.Symes DATE 12-17-71	CHECKED M Laurence DATE 12/21/71	SECTION
ENG D Burdinsky DATE 12-17-71	PROD Paul Rogers DATE 1/4/72	ISSUED SECT.

LEGEND
- D DOCUMENT
- DN DOCUMENT CHANGE NOTICE
- PA PAPER TAPE ASCII
- PB PAPER TAPE BINARY
- PM PAPER TAPE READ-IN-MODE

ITEM NO.	DWG NO./PART NO.	DESCRIPTION	QUANTITY/VARIATION							KIT CHECK BY ____ DATE	INSTALLATION CHECK BY ____ DATE
1	M863	12 Channel Digital I/O Module	1								
2	BC08S-1	1 foot I/O Cable	1								
3	BC08J-10	10 foot Cable	2								
4	DR8-EA	DR8-EA Print Set	1								
5	LAB8-E	LAB8-E [illegible]t set	1								
6	MAINDEC 8E-DØ0A-AB	DR8-EA [illegible]gnostic - Binary Tape	1								
7	MAINDEC 8E-DØ0A-DL	DR8-EA Diagnostic - Document	1								
8	DEC-LB-HRZA-D	LAB8-E User's Handbook	1								
9	DEC-LB-HXZA-D	LAB8-E Maintenance Manual	1								
		NOTE: When item 8 or 9 is temporarily waived, ship the following:									
	A-SP-DR8EA-1	DR8E Engineering Specification	1								
	A-SP-DR8EA-2	DR8E Acceptance Procedure	1								

TITLE	ASSY. NO.	SIZE	CODE	NUMBER	REV.	ECO NO
DR8-EA Accessory List	SHEET 1 OF 1	A	AL	DR8-EA-3		

DIST.

DEC FORM NO.
DRA 121