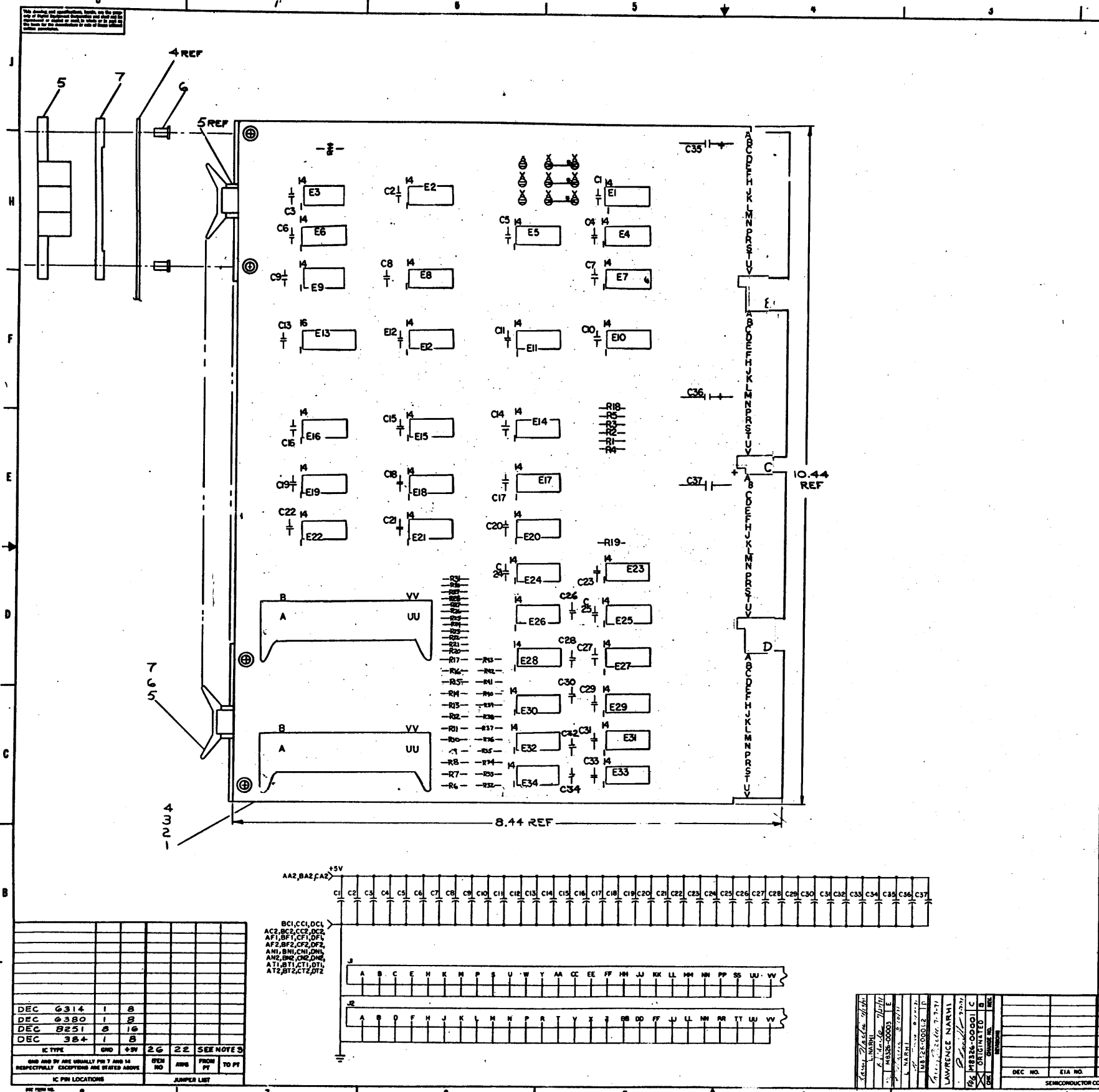


**DB8-E
interprocessor buffer
engineering drawings**

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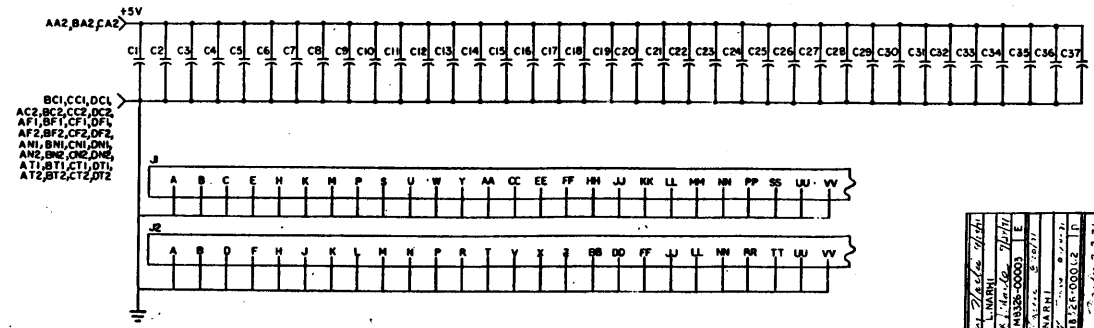


NOTES:
 1. UNLESS OTHERWISE NOTED:
 RESISTORS - 1/4W 5%
 CAPACITORS - .01uF 100V 20%
 2. 8=INDICATES SPLIT LUG
 3. JUMPERS B,D,F ARE CUSTOMER OPTIONS PER TABLE - DO NOT INSERT. JUMPERS A,C,E ARE NOT OPTIONS - INSERT.

| CODE | JUMPERS | | | | | |
|------|---------|---|---|---|---|---|
| | A | B | C | D | E | F |
| 50 | X | | X | X | X | X |
| 51 | X | | X | X | X | X |
| 52 | X | | X | X | X | X |
| 53 | X | | X | X | X | X |
| 54 | X | X | X | X | X | X |
| 55 | X | X | X | X | X | X |
| 56 | X | X | X | X | X | X |
| 57 | X | X | X | X | X | X |

| | | |
|----------|---|----|
| DEC 6314 | 1 | 8 |
| DEC 6380 | 1 | 8 |
| DEC 8251 | 8 | 16 |
| DEC 384 | 1 | 8 |

| IC TYPE | QTY | REF | FROM | TO |
|---------|-----|-----------|------|----|
| 2G | 22 | SEE NOTES | | |



| | | | |
|---------------------------|----------------------------|---------------|----|
| 12 R22-P31 | RES 68K 1/4W 5% | 13-01423 | 27 |
| 13 WIRE#22AWG SOLID BUS | | 1907560-01 | 24 |
| 14 E1E11, E1E23-E27 | I.C. DEC 7474 | 1905547 | 25 |
| 15 E3-E7 | I.C. DEC 7400 | 1905573 | 24 |
| 16 E8, E12, E28-E34 | I.C. DEC 7440 | 1905579 | 24 |
| 17 E2 | I.C. DEC 384 | 1909486 | 20 |
| 18 E13 | I.C. DEC 8251 | 1909394 | 21 |
| 19 E7, E14 | I.C. DEC 7404 | 1909684 | 20 |
| 20 E9, E16, E17, E19, E22 | I.C. DEC 97401 | 1909973 | 19 |
| 21 E1, E19, E15, E18, E21 | I.C. DEC 6380 | 1909971 | 18 |
| 22 R1, R2 | I.C. DEC 6314 | 1909972 | 19 |
| 23 R6-R17 | RESISTOR 100 1/4W 5% | 1300229 | 16 |
| 24 R1, R2 | RESISTOR 150 1/4W 5% | 1300250 | 15 |
| 25 R3, R4, R5, R19, R18 | RESISTOR 220 1/4W 5% | 1300271 | 14 |
| 26 R32-R44 | | | |
| 27 C35, C36, C37 | CAP 6.8uF 35V 20% TAN | 1000067 | 12 |
| 28 C1-C34 | CAP. 0.1uF 100V 20% DISC | 1001610 | 11 |
| 29 | CONNECTOR, 40 PIN | 1209941 | 10 |
| 30 | SPLIT LUG | 2006735 | 8 |
| 31 | | | |
| 32 | SPACER (CABLE CLAMP) | 1202704 | 7 |
| 33 | EYELET GS4-11 STIMPSON | 9006750 | 6 |
| 34 | HANDLE FLIP CHIP - MAGENTA | 900835700 | 5 |
| 35 | ETCHED CIRCUIT BOARD | 5009511 | 4 |
| 36 | MODULE HISTORY LIST | 8-11-1982-244 | 3 |
| 37 | ASSY/DRILLING HOLE LAYOUT | D-AM-88326-2 | 2 |
| 38 | X-Y COORDINATE HOLE LOC. | K-CO-88326-4 | 1 |

| | | | |
|--------------------------------|-----------|------|---------|
| ETCH BOARD REV | E | DATE | 10/1/77 |
| DESIGNER | W. J. ... | DATE | 10/1/77 |
| PROJ. ENG. | W. J. ... | DATE | 10/1/77 |
| PROB. ENG. | W. J. ... | DATE | 10/1/77 |
| ORIGINATED | B | DATE | 10/1/77 |
| DESIGNED | B | DATE | 10/1/77 |
| SEMICONDUCTOR CONVERSION CHART | | | |

| | | | |
|---------|---------|---------|---------|
| DEC NO. | EIA NO. | DEC NO. | EIA NO. |
| | | | |

| | | | | | |
|------|-----------------|-------------|----------|------|----|
| QTY. | REF DESIGNATION | DESCRIPTION | PART NO. | FROM | TO |
| | | | | | |

EQUIPMENT CORPORATION
 INTERPROCESSOR BUFFER
 ECS MB326-0-1

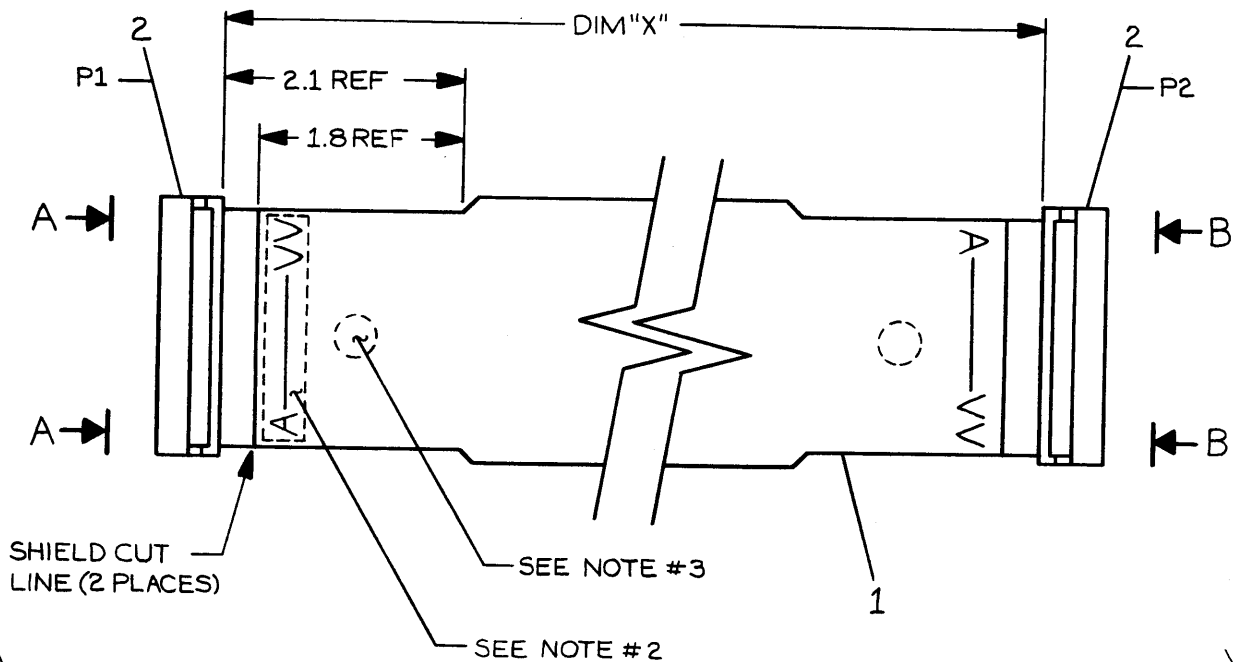
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| WIRE TABLE | | | |
|------------|-------|-------|------|
| FROM | TO | FROM | TO |
| P1-A | P2-VV | P1-Y | P2-X |
| P1-B | P2-UU | P1-Z | P2-W |
| P1-C | P2-TT | P1-AA | P2-V |
| P1-D | P2-SS | P1-BB | P2-U |
| P1-E | P2-RR | P1-CC | P2-T |
| P1-F | P2-PP | P1-DD | P2-S |
| P1-H | P2-NN | P1-EE | P2-R |
| P1-J | P2-MM | P1-FF | P2-P |
| P1-K | P2-LL | P1-HH | P2-N |
| P1-L | P2-KK | P1-JJ | P2-M |
| P1-M | P2-JJ | P1-KK | P2-L |
| P1-N | P2-HH | P1-LL | P2-K |
| P1-P | P2-FF | P1-MM | P2-J |
| P1-R | P2-EE | P1-NN | P2-H |
| P1-S | P2-DD | P1-PP | P2-F |
| P1-T | P2-CC | P1-RR | P2-E |
| P1-U | P2-BB | P1-SS | P2-D |
| P1-V | P2-AA | P1-TT | P2-C |
| P1-W | P2-Z | P1-UU | P2-B |
| P1-X | P2-Y | P1-VV | P2-A |

| LEGEND | | |
|----------|---------|-------------------|
| NUMBER | DIM "X" | PRECUT LENGTH |
| BC08R-01 | 1FT | 1FT 1.5IN±1IN |
| BC08R-02 | 2FT | 2FT 1.5IN±1IN |
| BC08R-03 | 3FT | 3FT 1.5IN±1IN |
| BC08R-04 | 4FT | 4FT 1.5IN±1IN |
| BC08R-06 | 6FT | 6FT 1.5IN±2IN |
| BC08R-08 | 8FT | 8FT 1.5IN±2IN |
| BC08R-10 | 10FT | 10FT 1.5IN±2IN |
| BC08R-12 | 12FT | 12FT 1.5IN±3IN |
| BC08R-20 | 20FT | 20FT 1.5IN±3IN |
| BC08R-25 | 25FT | 25FT 1.5IN±3IN |
| BC08R-30 | 30FT | 30FT 1.5IN±6FT |
| BC08R-50 | 50FT | 50FT 1.5IN±1FT |
| BC08R-60 | 60FT | 60FT 1.5IN±1.2FT |
| BC08R-75 | 75FT | 75FT 1.5IN±1.5FT |
| BC08R-A0 | 100FT | 100FT 1.5IN±2FT |
| BC08R-A3 | 130FT | 130FT 1.5IN±2.6FT |
| BC08R-A6 | 160FT | 160FT 1.5IN±3.2FT |

- NOTES:
1. ASSEMBLE THIS CABLE PER PROCESS SPEC #7606485-0-0.
 2. CONNECTOR LEGEND IDENTIFICATION TO BE PLACED ON SHIELD SIDE OF CABLE IN THIS AREA AS SHOWN.
 3. INSPECTION & TEST STAMPS TO BE PLACED AT EACH END OF THE CABLE ASSEMBLY.

| | |
|----|----|
| UU | W |
| SS | TT |
| PP | RR |
| MM | NN |
| KK | LL |
| HH | JJ |
| EE | FF |
| CC | DD |
| AA | BB |
| Y | Z |
| W | X |
| U | V |
| S | T |
| P | R |
| M | N |
| K | L |
| H | J |
| E | F |
| C | D |
| A | B |



| | |
|----|----|
| B | A |
| D | C |
| F | E |
| J | H |
| L | K |
| N | M |
| R | P |
| T | S |
| V | U |
| X | W |
| Z | Y |
| BB | AA |
| DD | CC |
| FF | EE |
| JJ | HH |
| LL | KK |
| NN | MM |
| RR | PP |
| TT | SS |
| VV | UU |

VIEW A-A
CONN. LEGEND REF.

VIEW B-B
CONN. LEGEND REF.

| REV. | CHANGE NO. | CHK | DATE |
|------|------------|------------|---------|
| J | 0004 | JK | 3-6-74 |
| J | 0004 | P. GARDNER | 7/17/73 |

REVISED & REDRAWN
P. GARDNER

| QTY. | DESCRIPTION | PART NO. | ITEM NO. |
|------|-------------------------------|----------|----------|
| 2 | CONNECTOR, 40 SOCKET | 1211206 | 2 |
| A/R | CABLE, 40 COND. FLAT W/SHIELD | 1700004 | 1 |

| FIRST USED ON OPTION/MODEL | | PARTS LIST | |
|--|---------|-----------------------|--------------|
| UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES | | DRN. D. FONTAINE | DATE 8-28-70 |
| TOLERANCES | | CHK'D. J. FLEMING | DATE 8-28-70 |
| DECIMALS | ANGLES | ENG. P. GARDNER | DATE 9-3-70 |
| .xxx = .005 | ±0° 30' | PROJ. ENG. P. GARDNER | DATE 9-3-70 |
| .xx = .02 | | PROD. DONALD | DATE 9-4-70 |
| .x = .1 | | | |
| REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY √ | | TITLE | |
| MATERIAL | | BC08R I/O CABLE | |
| FINISH | | SIZE CODE | NUMBER |
| | | C UA | BC08R -0-0 |
| | | SCALE | REV. |
| | | NONE | J |
| | | SHEET | |
| | | 1 OF 1 | |

REV. J
NUMBER BC08R-0-0
SIZE CODE CUA

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DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 8/12/71

TITLE DB8-E INTERPROCESSOR BUFFER SPEC.

REVISIONS

| REV | DESCRIPTION | CHG NO | ORIG | DATE | APPD BY | DATE |
|-----|-------------|------------|-------|------|----------|---------|
| A | ECO CHANGE | DB8E-00004 | NARHI | 4/72 | L. Narhi | 4/25/72 |

| | | | | | |
|-----------------|-------------------------|---------------|---------|----------------|-------|
| ENG Larry Narhi | APPD <i>Larry Narhi</i> | SIZE A | CODE SP | NUMBER DB8-E-1 | REV A |
|-----------------|-------------------------|---------------|---------|----------------|-------|

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DB8-E INTERPROCESSOR BUFFER

1. Overall Description

The DB8-E is an interprocessor buffer designed to plug directly into the PDP8/E OMNIBUS™. This option allows two 8/E's to transfer data between themselves, one 12 bit word at a time, at a maximum rate of approximately 5K Hz, allowing for software overhead.

Jumpers on the board allow the user to select any device code between 50 and 57 so up to 8 DB8-E's can be connected to one PDP8/E. The interconnecting cables allow the 8/E's to be up to 100 feet apart.

This option was designed primarily as an interprocessor buffer for the 8/E, however, this option may be used single ended as a data path between an 8/E and user designed logic.

2. General Specification

2.1 The basic option, DB8-EA, consists of one module, M8326, and one ~~BC08-R~~ cables, up to 100' long. The DB8-EB is one M8326 module and a pair of BC08-R cables, and (2) cable adapters #5409209.

2.2 Option jumpers on the module allows the device code to be set to any value between 50 and 57.

2.3 The entire option is contained on one 8 1/2" 8/E Quad module.

2.4 Temperature limits are: 32°F to 131°F
(0°C to 55°C)

Power required is: +5.0 volts at 600 MA

3. Specification of Vendor Supplied Equipment

See applicable purchase specifications for board components.

4. Programming

| | | | |
|---------------|---------|----------------|-------|
| SIZE A | CODE SP | NUMBER DB8-E-1 | REV A |
|---------------|---------|----------------|-------|

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DB8-E INTERPROCESSOR BUFFER

4.1 The IOT codes are:

| <u>Mnemonic</u> | <u>Octal Code</u> | <u>Function</u> |
|-----------------|-------------------|---|
| DBRF | 65X1 | Skip if the receive set to a 1. |
| DBRD | 65X2 | Read incoming data into the AC, clear receive flag. |
| DBTF | 65X3 | Skip if the done flag is set to a 1. |
| DBTD | 65X4 | Load the AC into the transmit buffer, transmit and set the transmit flag. |
| DBEI | 65X5 | Enable the Interrupt Request line. |
| DBDI | 65X6 | Disable the Interrupt Request line. |
| DBCD | 65X7 | Clears the done flop. |

NOTE: Initialize disables interrupts from occurring.

- 4.2 There are no maintenance instructions.
- 4.3 Data format is 12 parallel bits in and 12 parallel bits out.
- 4.4 There are no operator controls.

5. Interface Specification

Data is handled in the following manner for a transmit IOT.

- a) First, the AC loaded into the transmit buffer in 8/E #1. Immediately, the data becomes true at the inputs to the receivers in 8/E #2.
- b) At the trailing edge of the IOT, the done flag is set in 8/E #2 signifying that data is ready to be read; and if Interrupt Enable is true, Interrupt Request occurs.

| | | | |
|-----------|------------|-------------------|----------|
| SIZE A | CODE SP | NUMBER DB8-E-1 | REV A |
|-----------|------------|-------------------|----------|

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DB8-E INTERPROCESSOR BUFFER

For a receive IOT, the following takes place:

- a) 8/E #2 issues IOT 65X7 to clear the "Done F/F"
- b) When the read IOT occurs, the receivers are enabled in 8/E #2 and the data is loaded into the AC on a jam transfer.
- c) At the trailing edge of the IOT the flag in #2 8/E is cleared readying it for another transfer.

The same sequence of events will occur when transferring data from 8/E #2 to 8/E #1. Each data output can sink 8 MA (5 TTL loads) and still maintain standard TTL noise immunity. Rise and fall times, without cable, are < 50 ns. Data is true at +3.0 volts and false at 0 volts. Outputs are series terminated with 100 ohms. Each data input presents one unit load (1.6 MA) to a driver. Each input is clamped to -.6 volts. Data is true at +3.0 volts and false at 0V.

| | | | |
|-----------|------------|-------------------|----------|
| SIZE A | CODE SP | NUMBER DB8-E-1 | REV A |
|-----------|------------|-------------------|----------|

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**DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS**

ENGINEERING SPECIFICATION

DATE 6/2/71

TITLE DB8-EA/DB8-EB INTERPROCESSOR BUFFER TEST PROCEDURE

REVISIONS

| REV | DESCRIPTION | CHG NO | ORIG | DATE | APPD BY | DATE |
|-----|-------------|------------|---------|---------|-----------------|---------|
| A | ECO CHANGE | DB8E-00002 | L.NARHI | 10/2-71 | <i>L. Narhi</i> | 10/2/71 |

| | | | | | |
|------------------------|--------------------------|---------------|---------|----------------|--------------|
| ENG <i>Larry Narhi</i> | APPD <i>B. X. Nelson</i> | SIZE A | CODE SP | NUMBER DB8-E-2 | REV A |
|------------------------|--------------------------|---------------|---------|----------------|--------------|

DEC FORM NO. DRA 107

SHEET 1 of 3

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DB8-EA/DB8-EB INTERPROCESSOR BUFFER TEST PROCEDURE

1.0 EQUIPMENT

- 1.1 PDP8/E Standard (2 each)
- 1.2 Heat Box
- 1.3 453 Scope and voltage probes
- 1.4 Teletype (2 each)
- 1.5 Binary Loader Tape
- 1.6 MAINDEC-8E-DOPA-PB
DB8-E Interprocessor Buffer Test
- 1.7 M8326 tester module with one BC08R tester cable

2.0 PAPERWORK

- 2.1 Check paperwork in the envelope making sure it is complete as required by DEC Standard # 101.
 - 2.1.1 Test and Inspection Record
 - 2.1.2 Key sheet and ECO status sheet will contain both CS and etch revision.
 - 2.1.3 Quality Control Inspection Report.
 - 2.1.4 PDP8/E Progress Report.

3.0 TEST STATION SET UP (PART I)

- 3.1 Connect one BC08R cable from the output to input of the DB8E (M8326) , to be tested.
- 3.2 Insert M8326 in PDP8-E # 1.

4.0 TEST STATION SET UP (PART II)

- 4.1 Insert M8326 tester module in PDP8-E # 2.
- 4.2 Insert M8326 module that is to be tested in PDP8-E # 1.
- 4.3 Take one BC08R cable and connect one end to the output of DB8-E in 8E # 1 and the other to the input of DB8-E in 8E # 2.
- 4.4 Take another BC08R and connect the same with 8E # 2 to 8E #1.

5.0 LOADING PROCEDURE

- 5.1 Load binary loader in 8E # 1 and # 2.
- 5.2 Load diagnostic in 8E # 1 and # 2.

6.0 DB8-E CHECKOUT

- 6.1 The following test programs are to be run 5 minutes each.
 - 6.1.1 Part 1 (Test station set up Part 1).
 - 6.1.2 Part 2 (Test station set up Part 2)

NOTE: The DB8-E being tested is the one in 8E # 1.

| | | | |
|---------------|---------|----------------|--------------|
| SIZE A | CODE SP | NUMBER DB8-E-2 | REV A |
|---------------|---------|----------------|--------------|

DEC FORM NO 16-1022
DRA 108

SHEET 2 OF 3

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE DB8-EA/DB8-EB INTERPROCESSOR BUFFER TEST PROCEDURE

7.0 HEAT TEST

- 7.1 Heat test is to be run after successful completion of all previously indicated tests.
- 7.2 Connect M8326 as per test station set up part 1.
- 7.3 Lower heat box over 8E # 1.
- 7.4 Turn on heat to box. Once indicator light goes off run part 1 test for 10 minutes.
- 7.5 Turn the heat switch off and open the two ports on the left side of the heat box.
- 7.6 Allow 15 minutes for the machine to cool before raising the heat box.
- 7.7 Terminate the test once the machine has run for 5 minutes of room temperature.

8.0 FINAL OPERATION AND INSPECTION

- 8.1 Remove the tested DB8E (M8326) from 8E # 1.
- 8.2 Disconnect cables from board.
- 8.3 If M8326 is to be used on a DB8-EA, attach one BC08R to the module. When used as a DB8-EB, two BC08R cables and two 54-09209 cable adapters will be required to go with the M8326.
- 8.4 Check that the following paperwork has been completed:
 - Envelope
 - ECO Status Sheet
 - QC Sheet
 - 8E Progress Report

| SIZE | CODE | NUMBER | REV |
|------|------|---------|-----|
| A | SP | DB8-E-2 | A |

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MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION DATE 9/10/71

TITLE ACCEPTANCE PROCEDURE FOR THE DB8-E INTERPROCESSOR BUFFER

REVISIONS

| REV | DESCRIPTION | CHG NO | ORIG | DATE | APPD BY | DATE |
|-----|-------------|------------|---------|---------|-----------------|----------|
| A | ECO CHANGE | DB8E-00002 | L.NARHI | 10/2-71 | <i>L. Narhi</i> | 10/15/71 |

| | | | | | | | | | | | |
|-----|-------------|------|------------------|------|---|------|----|--------|---------|-----|---|
| ENG | Larry Narhi | APPD | <i>C. Shover</i> | SIZE | A | CODE | SP | NUMBER | DB8-E-3 | REV | A |
|-----|-------------|------|------------------|------|---|------|----|--------|---------|-----|---|

ENGINEERING SPECIFICATION CONTINUATION SHEET

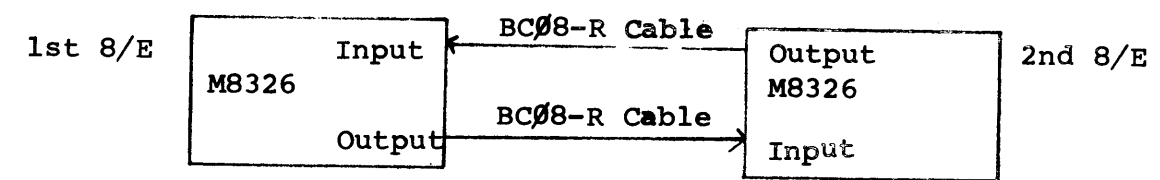
TITLE ACCEPTANCE PROCEDURE FOR THE DB8-E INTERPROCESSOR BUFFER

- 1.Ø Equipment Required
 - 1.1 One or two M8326 modules
 - 1.2 Two (2) BCØ8-R or two (2) BCØ8-J cables
 - 1.3 MAINDEC-8E-DØPA, Interprocessor Buffer diagnostic
 - 1.4 Two (2) PDP8/E computers with TTY

- 2.Ø Check the keysheet and Construction Requisition to see which of the following is required:
 - 2.1 DB8-EA, which is one M8326 module and one BCØ8-R cable or
 - 2.2 DB8-EB, which is one M8326 module and two BCØ8-J cables.

- 3.Ø Check the M8326(s) to see that the module has:
 - 3.1 Proper circuit revision
 - 3.2 Date code
 - 3.3 Heat tested

- 4.Ø If the DB8-EB is required go to paragraph 5.Ø for acceptance testing. If the DB8-EA is required continue with this paragraph.
 - 4.1 Insert one M8326 into each 8/E.
 - 4.2 Connect one BCØ8-R cable to the "Input" connector on one of the M8326's, connect the other end of the cable to the "Output" connector of the other M8326. Reverse the procedure for the other cable.
The system would then look like this:



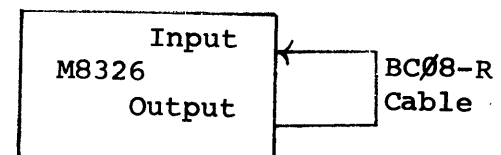
| | | | | | | | |
|------|---|------|----|--------|---------|-----|---|
| SIZE | A | CODE | SP | NUMBER | DB8-E-3 | REV | A |
|------|---|------|----|--------|---------|-----|---|

TITLE ACCEPTANCE PROCEDURE FOR THE DB8-E INTERPROCESSOR BUFFER

- 4.3 Load the diagnostic into both 8/E's.
- 4.4 Start up one 8/E at location 2000g
- 4.5 Start the other 8/E at location 1000g
- 4.6 Minimum acceptance consists of running the diagnostic for 15 minutes.

5.0 For the DB8-EB acceptance:

- 5.1 Insert the M8326 into the 8/E.
- 5.2 Connect one end of a BC08-R cable into the "Input" connector on the M8326. Connect the other end of the cable into the "Output" connector on the same M8326. The system would then look like:



- 5.3 Load the diagnostic into the 8/E.
- 5.4 Load address 0200g, hit clear, set the SR = 0, hit Continue.
- 5.5 Minimum acceptance consists of running the diagnostic for 15 minutes.

6.0 Shipping Hardware

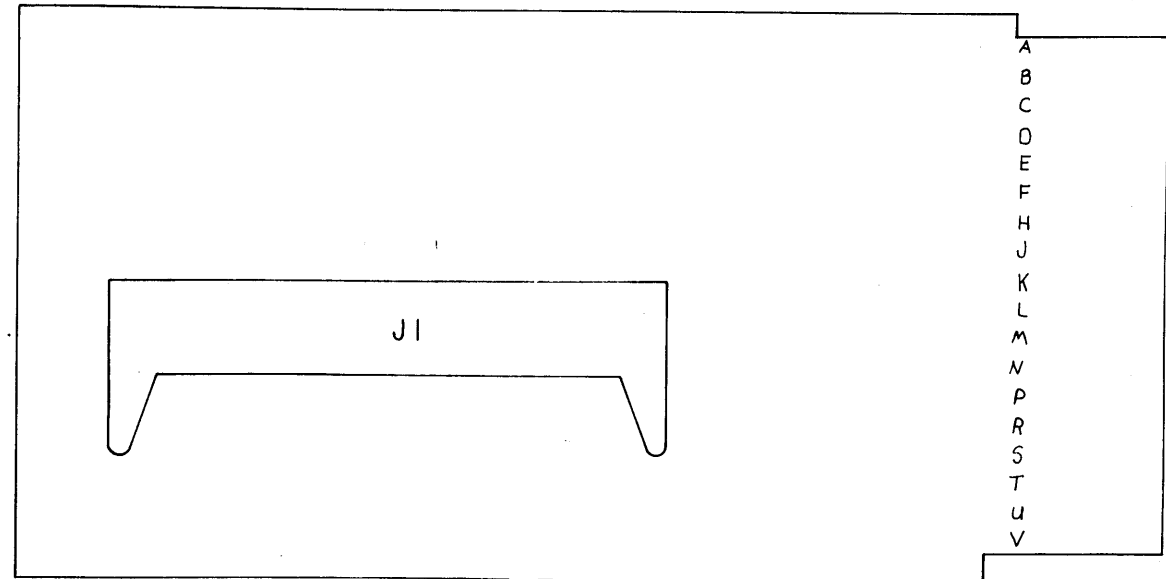
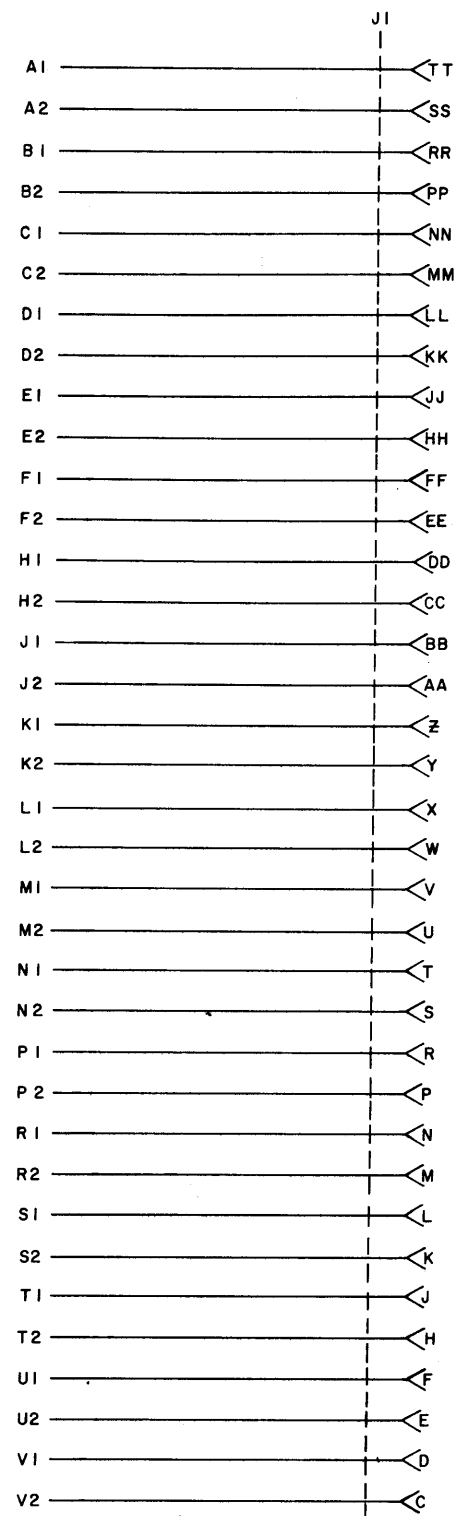
- 6.1 M8326 Module 1 per
- 6.2 BC08R-25 1 per (DB8-EA only)
- 6.3 BC08 R 2 per (DB8-EB only)
- 54-09209 ADAPTERS 2 PER (DB8-EB ONLY)

7.0 Shipping software

- 7.1 LIBKIT-8E-DB8E
- 7.2 Print set as per A-ML-DB8-E including engineering specs.
- 7.3 8/E Maintenance Manual Vol II

| SIZE | CODE | NUMBER | REV |
|------|------|---------|-----|
| A | SP | DB8-E-3 | A |

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| A/R | REF. DESIGNATION | DESCRIPTION | DEC PART NO. | QTY |
|-----|------------------|------------------------------|------------------|-----|
| | | GRIPLETS | 1210244-0 | 6 |
| 1 | J1 | RIGHT ANGLE HEADER | 1209941 | 5 |
| 1 | | ETCHED CIRCUIT BOARD | 5009208 | 4 |
| | | MODULE ECO HISTORY | B-MH-5409209-0-4 | 3 |
| | | ASSY/DRILLING HOLE LAYOUT | C-AH-5409209-0-5 | 2 |
| | | X-Y COORDINATE HOLE LOCATION | K-C0-5409209-0-4 | 1 |
| | | PARTS LIST | | |

| REVISIONS | CHK | CHG NO. | REV. |
|-----------|-----|---------|------|
| | | | |

| | | | |
|-------|-----------|------|----------|
| DRN. | S. Cooper | DATE | 11/23/71 |
| CHK'D | M. Fuller | DATE | 11/23/71 |
| ENGR. | | DATE | 1-12-72 |
| PROD. | | DATE | |

| TRANSISTOR & DIODE CONVERSION CHART | | | |
|-------------------------------------|-----|-----|-----|
| DEC | EIA | DEC | EIA |
| | | | |
| | | | |
| | | | |

digital I/O CABLE ADAPTER

EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

| | | | |
|------|------|-------------|------|
| SIZE | CODE | NUMBER | REV. |
| C | CS | 5409209-0-1 | A |

PRINTED CIRCUIT REV. B

REV. A
NUMBER 5409209-0-1
SIZE CODE C CS

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ACCESSORY LIST

LEGEND

D DOCUMENT
DN DOCUMENT CHANGE NOTICE
PA PAPER TAPE ASCII
PB PAPER TAPE BINARY
PM PAPER TAPE READ-IN-MODE

QUANTITY / VARIATION

MADE BY J. Mc Cluskey
DATE 1/30/72
ENG B. Smith
DATE 2-8-72

CHECKED B. Smith
DATE 2-8-72
PROD B. Smith
DATE 2-8-72

SECTION
ISSUED SECT.

| ITEM NO. | DWG NO. / PART NO. | DESCRIPTION | QUANTITY / VARIATION | | KIT CHECK | BY | DATE | INSTALLATION CHECK | BY | DATE |
|----------|--------------------|--|----------------------|---------|-----------|----|------|--------------------|----|------|
| | | | DB8-E-A | DB8-E-B | | | | | | |
| 1 | M8326 | DB8/E Control Module | 1 | 1 | | | | | | |
| 2 | BC08R-25 | DB8/E Cable | 1 | 0 | | | | | | |
| 3 | BC08R-10 | DB8/E Cable | 0 | 1 | | | | | | |
| 4 | 54-09209 | DB8/E Adapters | 0 | 2 | | | | | | |
| 5 | Maindec-8E-DOS8-PB | DB8/E Interprocessor Buffer Test Paper Tape | 1 | 1 | | | | | | |
| 6 | Maindec-8E-DOS8-D | DB8/E Interprocessor Buffer Test Document | 1 | 1 | | | | | | |
| 7 | A-ML-DB8-E | DB8/E Print Set | 1 | 1 | | | | | | |
| 8 | DB8/E | DB8/E Interprocessor Buffer Maintenance Manual | 1 | 1 | | | | | | |
| | | Note When Item 8 Is Temporarily Waived | | | | | | | | |
| | | Ship The Following | | | | | | | | |
| | A-SP-DB8-E-1 | DB8/E Engineering Specifications | | | | | | | | |
| | A-SP-DB8-E-3 | DB8/E Acceptance Procedure | | | | | | | | |

TITLE **DATA BUFFER** ASSY. NO. _____ SIZE CODE **A AL** NUMBER **DB8-E-4** REV. _____ ECO NO _____

SHEET _____ OF _____ DIST. _____