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89-	1	INPRNM	-- Initialize PRN generator with repeat range M
90-	1	EDPRNM	-- Generate pseudo-random number in specified range M

1           000001                    PROASM =           1                    ;Assemble for Professional

```

2           .TITLE  TSINIT -- TSX startup initialization
3           .ENABL  LC
4           .ENABL  AMA
5 000000    .DSABL  GBL
6 000000    .CSECT  TSINIT
7
8           ;
9           ; There are two external assembly-time switches related to assembling
10          ; TSINIT for execution on a PRO or a PDP-11.
11          ;
12          ; The following values for the PROASM flag are defined:
13          ; 0 ==> Assemble for PDP-11 (not Pro) only.
14          ; 1 ==> Assemble for Pro only.
15          ; 2 ==> Assemble for either PDP-11 or Pro execution.
16          ;
17          ; The following values for the PROCID flag are defined:
18          ; 0 ==> Do not lock system to ID number.
19          ; 1 ==> Lock system to ID number.
20          ;
21          . IF      NDF,PROASM      ; If PROASM not defined
22          PROASM =    0              ; Default value for PROASM if not defined
23          . ENDC    ; NDF,PROASM
24          ;
25          . IF      NDF,PROCID      ; If PROCID not defined
26          . IF      EQ,<PROASM-1>    ; If assembling for PRO only
27          PROCID =    1              ; Then check ID by default
28          . IFF      ; If not assembling for PRO only
29          PROCID =    0              ; Then don't check ID number
30          . ENDC    ; EQ,<PROASM-1>
31          . ENDC    ; NDF,PROCID
32          ;
33          . IF      EQ,PROASM
34          . GLOBL   TSXPRO
35          TSXPRO =    0              ; Define dummy base for TSXPRO if not PRO
36          . ENDC
37          ;
38          ; -----
39          ; TSINIT is the initialization module of TSX that is executed once
40          ; during system startup. Time-sharing character buffers and other
41          ; run-time data areas are allocated over TSINIT.
42          ;
43          ; Copyright 1980, 1981, 1982, 1983, 1984, 1985.
44          ; S&H Computer Systems, Inc.
45          ; Nashville, TN USA
46          ;
47          ; Macro calls
48          ;
49          . MCALL   . LOOKUP, . ENTER, . READW, . SAVESTATUS, . GVAL
50          . MCALL   . TRPSET, . SETTOP, . CLOSE, . TTYOUT, . PRINT, . PURGE
51          . MCALL   . DELETE, . WRITW, . SERR, . HERR, . EXIT, . UNLOCK
52          . MCALL   . FETCH, . RELEASE, . LOCK, . GTIM, . DATE, . DSTATUS
53          . MCALL   . SCCA, . CSTAT
54          ;
55          ; Global definitions
56          ;
57          . GLOBL   TSINIT, INITGO, INITOP, PPTERM, PROITP, PROASM, PISRT

```

```

58          .GLOBL  DSKBUF,PROBUF,FNDHRB,HANXMR
59          ;
60          ;   Following global only needed for the Pro distribution
61          ;   creation program - MAKPRO and installation program - INSTSX
62          ;
63          .IF    NE,PROASM
64          ;
65          ;** Assemble this code if we are generating for a Pro
66          ;
67          .GLOBL  PROSIZ,PROINI,PROLIN,PROHAN,PRONOP
68          .GLOBL  PIHAN,PIDPTR,PIDRIV
69          .IFF    ;NE,PROASM
70          ;
71          ;** Assemble this code if we are not generating for a Pro
72          ;
73          .GLOBL  TSXPRO
74          TSXPRO =      0
75          ;
76          ;** End of conditional Pro code
77          ;
78          .ENDC   ;NE,PROASM
79          ;
80          ;
81          ;   Global references
82          ;
83          .GLOBL  HANDSK,MAXDEV,NDVRCB,HANRCB,HANRCO
84          .GLOBL  LXCL,VSYDMP,STKLVL,INTSSZ,INDFIL,NXIVMH,EXCBUF
85          .GLOBL  VNUIP,NSIP,INSTBL,INSTBN,II##SZ,DCCSIZ,VNUMDC,NUMCDB
86          .GLOBL  NSCP,SCPFHD,SP##SZ,CSHDEV,CSHDVN,VMXCSH,CD##SZ
87          .GLOBL  LSTPL,LMXNUM,MXCSR,MXVEC,RSR,INVEC,VHIMEM,CXTPAG
88          .GLOBL  LSWPBK,LSTSL,SWDBLK,SWPCHN,NUMDEV,CS#NMX,SCHED
89          .GLOBL  H.DSTS,DVSTAT,HANENT,H.GEN,FORK,INTEN,PNAME
90          .GLOBL  $SXON,LSW10,LHIRBB,LHIRBE,LHIRBP,LHIRBG,LHIRBA
91          .GLOBL  LHIRBS,LHIRBC,VNCSLO,VNCXOF,VNCXON,SDDVU,VMSCHR,MAXSLO
92          .GLOBL  MPARO,MPAR16,PARENL,MPARFL,TSEMT,VDBFLG,DX#EBA
93          .GLOBL  H.SIZ,HANSIZ,H.DVSZ,DEVSIZ,LOMAP,MMENBL,UPAR7
94          .GLOBL  PSW,HIMAP,FSTD,LSTD,LINBUF,LINSIZ,NUMCCB,TK1SEC
95          .GLOBL  FRKINI,FRKGEN,NUMFRK,FQ##SZ,H.CSR,H.INS,VSWPSL,DMYDEV
96          .GLOBL  LINEND,LOTBUF,LOTSIZ,LOTEND,KMNTOP,KMNI,NSL,NDL
97          .GLOBL  DX$MPH,DX$NHM,DX$IBH,HANPAR,HANXIT,MAPPAR,LINSPC
98          .GLOBL  KMNPGS,KMNSTK,KMNSTR,KMNCHN,SROMMR,KPARO,PROFLG
99          .GLOBL  EMMAP,IOMAP,SR3MMR,IOPAGE,MAPSIZ,SR3FLG,NSPLDV
100         .GLOBL  UPARO,KPDRO,UPDRO,KPAR7,BASMAP,PTWRD,PTBYT,LOKMEM
101         .GLOBL  GTBYT,MPPHY,RELOC,BRKPT,TSGEN,TSEXEC,VSWPFL
102         .GLOBL  CW$GDH,CW$BTH,CW$LGS,CW$FB,CW$FGJ,MSGBAS,RPRVEC
103         .GLOBL  CW$USR,CW$XM,CONFIG,CW$50H,JMPO,DTLX,USRBAS,WINBAS
104         .GLOBL  DATIML,DATIMH,RMON,CONFIG2,SG$ELG,SG$IOT,CSHBAS
105         .GLOBL  SG$PAR,SG$MTS,SG$MMU,SG$MTM,LTPAR,LOKBAS,CSHVEC,LOKVEC
106         .GLOBL  SYSGEN,AUTHAN,AHEND,CLKRTI,TRP4,CW$PRO,TIOVEC
107         .GLOBL  TRP10,TRP20,TRP24,EMTENT,TRP34,INIIMP,MHNSIZ
108         .GLOBL  TK1VAL,INRECV,OTRECV,INMXV,OTMXV,DHBFSZ,MXTYPE
109         .GLOBL  ZCLR,MXRBUF,MXDTR,INTMX1,$PHONE,LCDTYP,TIOBAS
110         .GLOBL  LDHB1B,LDHB1P,LDHB2B,CLVERS,CXTSIZ,CXTWDS,CXTPDR
111         .GLOBL  CLORSZ,TSXSIT,JM##SZ,VMXMON,MONFQH,CXTRMN,CXTBAS
112         .GLOBL  ILSW2,$NOIN,LSW3,XMLPR,CW$ESP,CLTOTL,RMNPDR,MA$SYS
113         .GLOBL  SFCB,SFCBND,SFCBFH,SFCBSZ,NSPLFL,NSPLBL,INTSTK,INTSND
114         .GLOBL  NFRESB,PVSPBL,VMXWIN,DW##SZ,LDVERS,CW$QBS

```

```

115 . GLOBL FC#LBN, VMLBLK, VMXSF, VMXSFC, FF##SZ, FW##SZ, SWPJOB, SWPPOS
116 . GLOBL TSR, RBR, RDINT, LSTMX, SS, CHAIN, JSWLOC, MU#TXT, SLTSIZ
117 . GLOBL NUMIOQ, FREIOQ, UMODE, FPTRAP, MXLNT, DI#LD, DI#CL, CLSTS
118 . GLOBL FREPGS, IOQSIZ, SYUNIT, UMSYTP, DI#TT, CXTBUF, SSEND
119 . GLOBL SYINDX, MONVEC, KMNBAS, SDANAM, VBUSTP, MINCTR
120 . GLOBL NUMRDB, RDB, RDBEND, RT#SKP, RT#TOP, NLINES, SHRRCB, SHRRCN
121 . GLOBL RT#BAS, UPMODE, SPLNB, CSHALC, NIOL, CHNSIZ, RC##SZ, VNGR
122 . GLOBL UPAR6, UPDR6, RT##SZ, VINABT, #DEAD, LSW6
123 . GLOBL SYTIMH, SYSDAT, TRP250, ODTTRP, TRP14, SYTIML
124 . GLOBL DS#ABT, CL#ORB, CL#ORE, CL#ORG, CL#ORP, CL#ORA
125 . GLOBL #TAB, #FORM, CO#TAB, CO#FF, CO#DEF, CL#EPS, CLEOFS
126 . GLOBL CL#OPT, CL#STA, CL#ORS, LSTLIN, VCSHNB, CL#EPP, CL#EPN
127 . GLOBL CCLSAV, SPLND, SDCB, SPLDEV, SPLANM, MIODBG
128 . GLOBL SDNAME, SDCHAN, SDCBSZ, SPLDVN, DTYPE
129 . GLOBL DS#NRD, DX#NMT, #BBIT, CO#BBT, UEXRTN, VUXIFL
130 . GLOBL SPLBLK, SPLCHN, MVSIZ, MEMPAR, UEXINT, DX#NRD
131 . GLOBL NMSNMB, SNMSHD, SB##SZ, PMSIZE, PMPAR
132 . GLOBL NUMDCD, MEM256, LOKCSH, DC##SZ
133 . GLOBL JCXPGS, MXJMEM, VDFMEM, DFJMEM, TK5VAL, TK3SVL
134 . GLOBL VPAR6, IOTIMR, ERRLOG, VNFCSH, FC##SZ
135 . GLOBL O. ADR, O. BLK, O. PAR, O. SIZ, VPAR5, KPAR5, DZOINT, DHOINT
136 . GLOBL OVRADD, #OVRH, SYSMAP, MAPSYS, VSLEDT, LCLUNT
137 . GLOBL UBUSMP, UMRADR, IOMAP, QBUS, UNIBUS, DX#NST
138 . GLOBL DVFLAG, DX#DMA, RT#NAM, DS#DIR, LDDEVX, DS#VSZ
139 . GLOBL INDSAV, INDDBL, INDTSV, INDDBS, DS#SFN
140 . GLOBL SYNAME, UCLNAM, RSFBLK, VPLAS, SEGCHN
141 . GLOBL MXJADR, #MEMSZ, PHYMEM, SG#TSX, CDX#DH
142 . GLOBL CLHEAD, CLSIZE, CLDEVX, C. CSW, C. SBLK, C. DEVG, C1DEVX
143 . GLOBL VU#CL, VUCLMC, UK##SZ, US##SZ, UC##SZ, UCLBLK, UCLDAT
144 . GLOBL VLDSYS, VMXMSG, VMAXMC, MB##SZ, MR##SZ, CS#OPN, CS#ENT
145 . GLOBL DX#MAP, MIOFLG, MI#SBP, MI#LNK, MIOBHD, VMIOSZ
146 . GLOBL VMIOBF, MI##SZ, MW##SZ, MIONWB, MIOWHD, MW#LNK
147 . GLOBL CSHSIZ, CSHBFP, CA#BLK, CA#DVU, CA#WCT, VMXMRB
148 . GLOBL CA#UFL, CA#UBL, CA#HFL, CA#HBL, CA#HSH, NUMRDB
149 . GLOBL SRTSIZ, SMRSIZ, CCBHD, CC##SZ, CDX#DZ, MF#LIN
150 . GLOBL CDX#DL, HF#TSB, MH#SCR, LMXLN, HF#LIN, HF#RIE, HF#TIE
151 . GLOBL MH#LPR, DM#CSR, MF#LE, DM#LSR, HF#MC, MF#CS, MF#CM
152 . GLOBL CDX#VH, VH#CSR, VH#LPR, MH#PBR, VF#TIE, VF#RIE, VF#MR
153 . GLOBL VF#LIN, VF#SC, VF#RE, VH#LCR, VHOINT, TTINCP
154 . GLOBL #HARD, LOUTIR, LINIR, NEDCHR, CLOTIR, CLINCP, FSTIOL, LSTHL
155 . GLOBL SYSVER, SYSUPD, DI#DU, DI#XL, DI#MU, CL#LIX
156 . GLOBL CL#LEN, DI#PI, GENTOP
157 . GLOBL LSW5, DX#NCA, KPAR6, CLKVEC

```

-----  
; Macros to enable and disable interrupts

```

;
. MACRO DISABL ;Disable interrupts
BIS #340, @#PSW
. ENDM DISABL

. MACRO ENABL
BIC #340, @#PSW
. ENDM ENABL

```

-----  
; Offsets in block 0 of ODT REL file.

```

172          000040          STA      =      40          ; PROGRAM START ADDRESS
173          000042          STK      =      42          ; INITIAL STACK POINTER
174          000052          RSZ      =      52          ; ROOT SIZE
175          000056          OSZ      =      56          ; OVERLAY SIZE
176          000060          RID      =      60          ; REL FILE ID
177          000062          RBD      =      62          ; DISPLACEMENT TO 1ST REL BLOCK
178          001000          ODTBAS  =      1000         ; BASE ADDRESS ODT WAS LINKED FOR
179
180          ; Data areas
181
182 000000          AREA:      .BLKW      8.
183 000020 000000 000000 000000 NFSBLK: .WORD      0,0,0,0,0,0 ; EXTENDED TO 6 WORDS FOR .CSTAT
184 000026 000000 000000 000000
184 000034 000000          ODTFLG: .WORD      0
185 000036 000000          ODTTOP: .WORD      0
186 000040 000000          CCAFLG: .WORD      0
187 000042 000000          CLK100: .WORD      0
188 000044 000000          RTTRP4: .WORD      0
189 000046 000000          RTMNVC: .WORD      0
190 000050          SAVBLK: .BLKW      5
191 000062 075250 100020 000000 TSXSAV: .RAD50  /SY TSX   SAV/
191 000070 073376
192 000072 075250 100003 051646 KMNNAM: .RAD50  /SY TSKMONSAV/
192 000100 073376
193 000102 075250 011504 000000 CCLNAM: .RAD50  /SY CCL   SAV/
193 000110 073376
194 000112 000000 000000 000000 DSTBLK: .WORD      0,0,0,0
194 000120 000000
195 000122 000000          XMVBAS: .WORD      0
196 000124 000000          NMXHAN: .WORD      0
197 000126 000000          HMAP:   .WORD      0
198 000130 000000          FETDEV: .WORD      0
199 000132 000000          TOPMEM: .WORD      0
200 000134 000000          FMEMHI: .WORD      0          ;64-byte block # below high alloc memory
201 000136 000000          FMEMLO: .WORD      0          ;64-byte block # above top of low alloc memory
202 000140 000000          OVLBAS: .WORD      0          ;Start loading overlays over TSINIT from here
203 000142 000000          FILBLK: .WORD      0
204 000144 000000          CURDEV: .WORD      0
205 000146 000000          CURNAM: .WORD      0
206 000150 000000          PROBUF: .WORD      0
207 000152 025556          WRKBUF: .WORD      INITOP
208 000154 004000          WRKSIZ: .WORD      2048.
209 000156 052077          R50MSG: .RAD50  /MSG/
210 000160 110466          R50WIN: .RAD50  /WIN/
211 000162 046543          R50LOK: .RAD50  /LOK/
212 000164 103112          R50USR: .RAD50  /USR/
213 000166 012700          R50CSH: .RAD50  /CSH/
214 000170 077167          R50TIO: .RAD50  /TIO/
215 000172 100040          R50TT:  .RAD50  /TT /
216 000174 075250          R50SY:  .RAD50  /SY /
217 000176 045640          R50LD:  .RAD50  /LD /
218 000200 062550          R50PI:  .RAD50  /PI /
219 000202 012240          R50CL:  .RAD50  /CL /
220 000204 012276          R50CLO: .RAD50  /CLO/
221 000206 012305          R50CL7: .RAD50  /CL7/
222 000210 013630          R50C1:  .RAD50  /C1/
223 000212 013666          R50C10: .RAD50  /C10/

```

```

224 000214 013675          R50C17: .RAD50 /C17/
225 000216 105610          R50VM:  .RAD50 /VM /
226 000220 046770          R50LS:  .RAD50 /LS /
227 000222 057164          R50ODT: .RAD50 /ODT/
228 000224 100040 015270 075250 SKPDEV: .RAD50 /TT DK SY CL C1 PI /
    000232 012240 013630 062550
    000240 000000
229 000242      000      110          GTLIN:  .BYTE  0,110
230 000244 075250 114730 000000 HANNAM: .RAD50 /SY XXX  TSX/
    000252 100020
231 000254 075250 075273 057164 ODTBLK: .RAD50 /SY SYSODTREL/
    000262 070524
232 000264 075250 035164 000000 INDNAM: .RAD50 /SY IND  SAV/
    000272 073376
233 000274 000000          RLBF:   .WORD  0
234 000276 000000          RLBFND: .WORD  0
235 000300 000000          ODTSTA: .WORD  0
236 000302 000000          MEMLIM: .WORD  0
237 000304 000000          HGENFL: .WORD  0
238
239          ; Initialization configuration word
240
241 000306 000000          ICONFG: .WORD  0          ; Initialization configuration word
242
243          ; Flag bits in ICONFIG
244
245          000001          EXTLSI  =      1          ; Q-bus system with more than 256Kb
246
247          ; Simulated shared run-time control block for PI handler
248
249 000310 075250 062550 000000 PISRT:  .RAD50 /SY PI  TSX/
    000316 100020
250 000320 000000 000000 000000          .WORD  0,0,0
251
252          ; Byte data cells
253
254 000326      000          PPTERM: .BYTE  0          ; 1 if printer port is T/S terminal
255          .EVEN

```

```

1
2 ; -----
3 ; The following tables are used to determine the minimum RT-11 monitor
4 ; and update versions required for particular devices.
5 ; There are three arguments for each handler definition:
6 ;   Arg 1 = Handler id code.
7 ;   Arg 2 = Minimum acceptable RT-11 version.
8 ;   Arg 3 = Minimum acceptable RT-11 update within the version.
9 ;
10 ; .MACRO HANVER DEVID,RTVERS,RTUPDT
11 ; .BYTE DEVID ;ID code for device type
12 ; .BYTE RTVERS ;Minimum RT-11 version
13 ; .BYTE RTUPDT ;Minimum update level within version
14 ; .ENDM HANVER
15 ; Define offsets into handler version table
16 ;
17 000000 HV$ID = 0 ;Handler identification code
18 000001 HV$VER = 1 ;Minimum RT-11 version
19 000002 HV$UPD = 2 ;Minimum update level within version
20 000003 HV$$SZ = 3 ;Size of handler version table entry
21 ;
22 ; Define minimum versions for various handlers
23 ; *****
24 ; *** Note RT-11 version update numbering system changed ***
25 ; *** at 5.2 so that the update number for 5.2 is lower ***
26 ; *** than for 5.1C. See the CLVX entries below for ***
27 ; *** version and update correlations. ***
28 ; *****
29 000330 HVTBL:
30 000330 HANVER DI$DU,5.,0. ;DU - 5/0 (5.0)
31 000333 HANVER DI$XL,5.,2. ;XL - 5/6 (5.1B) ;Fixed for 5.2 SCB
32 000336 HANVER DI$MU,5.,4. ;MU - 5/4 (5.4)
33 000341 HVEND:
34 .EVEN

```

```

1
2 ; -----
3 ; The following table defines default control flags for certain devices.
4 ;
5     000000 DV$NAM = 0 ; Rad50 name of device
6     000002 DV$FLG = 2 ; Flags for device
7     000004 DV$$SZ = 4 ; Size of a table entry
8 ;
9     .MACRO DEFFLG DEV, FLAGS
10    .RAD50 / 'DEV/ ; DV$NAM
11    .WORD FLAGS ; DV$FLG
12    .ENDM DEFFLG
13 DVFLBS:
14 DEFFLG <CR>, <DX$MPH>
15 DEFFLG <CT>, <DX$MPH>
16 DEFFLG <DB>, <DX$DMA!DX$MPH>
17 DEFFLG <DD>, <DX$NHM>
18 DEFFLG <DL>, <DX$DMA!DX$MPH!DX$IBH>
19 DEFFLG <DM>, <DX$DMA!DX$NHM>
20 DEFFLG <DP>, <DX$DMA>
21 DEFFLG <DS>, <DX$DMA>
22 DEFFLG <DT>, <DX$DMA>
23 DEFFLG <DU>, <DX$DMA!DX$NHM!DX$NST>
24 DEFFLG <DW>, <DX$MPH>
25 DEFFLG <DX>, <DX$MPH>
26 DEFFLG <DY>, <DX$DMA!DX$NHM>
27 DEFFLG <DZ>, <DX$MPH>
28 DEFFLG <FW>, <DX$DMA>
29 DEFFLG <LP>, <DX$MPH>
30 DEFFLG <LS>, <DX$MPH>
31 DEFFLG <MM>, <DX$DMA!DX$MPH!DX$IBH>
32 DEFFLG <MS>, <DX$DMA!DX$MPH!DX$IBH>
33 DEFFLG <MT>, <DX$DMA!DX$MPH!DX$IBH>
34 DEFFLG <MU>, <DX$DMA!DX$NHM!DX$IBH!DX$NST>
35 DEFFLG <NL>, <DX$MPH>
36 DEFFLG <PC>, <DX$MPH>
37 DEFFLG <RF>, <DX$DMA>
38 DEFFLG <RK>, <DX$DMA!DX$MPH>
39 DEFFLG <VM>, <DX$EBA!DX$NCA!DX$NHM>
40 DEFFLG <XC>, <DX$MPH>
41 DEFFLG <XL>, <DX$MPH>
42 DVFLND:
43 ; -----
44 ; The following table specifies which version number is to be
45 ; returned by CL in response to the XL/CL .SPFUN used by
46 ; VTCOM to match it to the handler.
47 ; The macro has 3 arguments:
48 ; 1. Minimum RT-11 version number where this CL version should be used.
49 ; 2. Minimum RT-11 update number where this CL version should be used.
50 ; 3. CL version number that starts with specified RT-11 version.
51 ;
52 ;
53 .MACRO CLVX RTV, RTU, CLV
54 .BYTE RTU, RTV
55 .WORD CLV
56 .ENDM CLVX
57 ;

```

```

58      ; Define CL versions based on RT-11 versions
59      ;
60 000522 CLVTTBL:
61 000522      CLVX      5,1,16.      ;Version 5.1
62 000526      CLVX      5,6,16.      ;Version 5.1B
63 000532      CLVX      5,35,16.     ;Version 5.1
64 000536      CLVX      5,44,16.     ;Version 5.1C
65 000542      CLVX      5,2,17.      ;Version 5.2
66 000546      CLVX      5,3,17.      ;Version 5.3
67 000552      CLVX      5,4,18.      ;Version 5.4
68 000556      CLVEND:
69      ;
70      ;-----
71      ; RT-11 v5.4 changed the structure of the LD translation tables.
72      ; The following table is used to determine which table format
73      ; to return to LD spfun 372.
74      ;
75      ; Use original format for the following versions:
76      ;
77 000556      LD1TBL:
78 000556      .BYTE      5,0          ;Version 5.0
79 000560      .BYTE      5,1          ;Version 5.1
80 000562      .BYTE      5,6          ;Version 5.1B
81 000564      .BYTE      5,35         ;Version 5.1
82 000566      .BYTE      5,44         ;Version 5.1C
83 000570      .BYTE      5,2          ;Version 5.2
84 000572      .BYTE      5,3          ;Version 5.3
85 000574      .WORD      0
86      ;
87      ;-----
88      ; The following data structures are used to hold information about
89      ; TSX-Plus overlays as they are being initialized.
90      ;
91      ; Offsets in structure for each overlay
92      ;
93      000000      OS$SIZ =          0          ;Total space needed for overlay
94      000002      OS$FLG =          2          ;0==>Load into XM space, 1==>over TSINIT
95      000004      OS$OVL =          4          ;Pointer to overlay table entry
96      000006      OS$$SZ =          6          ;Size of each overlay entry
97      ;
98      000031      MAXOVL =          25.         ;Maximum number of system overlays
99      ;
100 000576      OSTABL: .BLKB      OS$$SZ*MAXOVL ;Reserve room for table
101 001024      OSEND:  ;-Define end of table
102 001024      OSLAST: .WORD      OSTABL      ;Pointer past last used entry in table
103      ;
104      ; Table of system overlays that must be loaded over TSINIT
105      ;
106 001026      LOWOVL: .RAD50      /CSH/        ;TSCASH
107 001030      .RAD50      /TIO/          ;TSTIO
108 001032      .RAD50      /LOK/          ;TSLOCK
109 001034      LOWEND: ;End of table

```

```

1 ; -----
2 ; Text messages
3 ;
4 ; . NLIST BEX
5 001034 077 124 123 TSXHD: . ASCII /?TSX-F-/<200>
6 001044 111 156 166 BADLIN: . ASCII 'Invalid status register address for T/S line: '<200>
7 001123 111 156 166 BDVMSG: . ASCII 'Invalid interrupt vector address for T/S line: '<200>
8 001203 114 151 156 BDLMSG: . ASCII /Line # = /<200>
9 001215 000 CRLF: . BYTE 0
10 001216 124 123 130 REQMIS: . ASCII /TSX generation did not include device /<200>
11 001265 103 141 156 BADOPN: . ASCIIZ /Cannot open program swap file/
12 001323 103 141 156 RSFERR: . ASCIIZ /Cannot open PLAS region swap file/
13 001365 116 165 155 CONSPC: . ASCII /Number of contiguous blocks needed = /<200>
14 001433 103 141 156 BDSPOP: . ASCII /Cannot open spooled device: /<200>
15 001470 111 156 163 BOSF: . ASCIIZ /Insufficient disk space for spool file/
16 001537 103 141 156 NOKMON: . ASCIIZ /Cannot find "SY:TSKMON.SAV" file/
17 001600 103 141 156 NOCCL: . ASCIIZ /Cannot find "SY:CCL.SAV" file/
18 001636 103 141 156 CFHMSG: . ASCII /Cannot find device handler file: /<200>
19 001700 105 162 162 ERHMSG: . ASCII /Error reading device handler file: /<200>
20 001744 111 156 166 ERHNDV: . ASCII /Invalid RT-11 version for device handler: /<200>
21 002016 124 123 130 TSXRUN: . ASCIIZ /TSX is already running/
22 002045 110 141 156 HSGER: . ASCII /Handler not generated with extended memory support: /<200>
23 002132 103 157 155 NOCLOK: . ASCIIZ /Computer line time clock (50 or 60 Hz) is not working/
24 002220 123 171 163 NXMMSG: . ASCIIZ /System is not equipped with memory management hardware/
25 002307 123 171 163 NEXMSG: . ASCIIZ /System is not equipped with extended memory management hardware/
26 002407 103 141 156 NOODT: . ASCIIZ /Cannot locate "SY:SYSODT.REL" file/
27 002452 115 141 160 HN2BIG: . ASCII /Mapped handler is larger than BKB: /<200>
28 002516 105 162 162 ODTRDM: . ASCIIZ /Error on read of SYSODT rel file/
29 002557 110 141 156 NOSYDV: . ASCIIZ /Handler for SY device was not loaded/
30 002624 107 145 156 TOOBIG: . ASCIIZ /Generated TSX system is too large/
31 002666 122 145 144 REDUCE: . ASCII /Reduce size of TSGEN by /<200>
32 002717 056 040 142 BYTES: . ASCIIZ /. bytes/
33 002727 111 156 163 PHSOVF: . ASCIIZ /Insufficient total physical memory for generated system/
34 003017 103 141 156 COSRT: . ASCII /Cannot open shared run-time file: /<200>
35 003062 103 141 156 SVERR: . ASCIIZ /Cannot locate "SY:TSX.SAV"/
36 003115 111 156 163 TSXSIZ: . ASCIIZ /Insufficient memory to load all mapped system regions/
37 003203 105 162 162 RDERR: . ASCIIZ /Error reading "SY:TSX.SAV"/
38 003236 111 156 163 SRTOVF: . ASCIIZ /Insufficient memory to load all shared run-time systems/
39 003326 111 156 163 CSHOVF: . ASCIIZ /Insufficient memory space for data cache/
40 003377 103 141 156 INDOPN: . ASCIIZ /Cannot open TSXIND file/
41 003427 103 141 156 UCLOPN: . ASCIIZ /Cannot open TSXUCL data file/
42 003464 040 101 102 R5OCHR: . ASCII / ABCDEFGHIJKLMNOPQRSTUVWXYZ$. *0123456789/
43 . EVEN
44 . LIST BEX

```

\*\*\* TSX Initialization \*\*\*

```

1          .SBTTL *** TSX Initialization ***
2          .SBTTL *** Initialization taking over control from RT-11 ***
3          ;-----
4          ; The initialization code from this point onward takes over
5          ; control from RT-11.
6          ; This code is placed at the front of TSINIT so that non-initialized
7          ; data structures can be allocated over it.
8          ;
9 003534    TAKOVR:
10         ;
11         ; Read in system overlays that go over TSINIT
12         ;
13 003534   004737   004426'      CALL    INIOVL      ;Read overlays over TSINIT
14         ;
15         ; Set pointer to monitor offset vector
16         ;
17 003540   012737   0000000 0000000    MOV     #MONVEC,@#RMON ;SET POINTER TO MONVEC TABLE
18         ;
19         ; Initialize last word in interrupt stack area so we won't report a
20         ; stack overflow if an interrupt occurs.
21         ; Set STKLVL to 0 to cause INTEN not to switch to interrupt
22         ; stack during initialization.
23         ;
24 003546   012777   123456 0000000    MOV     #123456,@INTSND ;Say stack has not overflowed
25 003554   105037   0000000          CLRB    STKLVL      ;Say we are already on interrupt stack
26         ;
27         ; If we are running on a Professional, disable its interrupts
28         ;
29         .IF     NE,PROASM
30 003560   105737   0000000          TSTB   PROFLG      ;Are we running on a PRO?
31 003564   001403          BEQ     7$          ;Br if not on a PRO
32 003566   004737   0000000          CALL   PRNOP      ;Disable its interrupts
33 003572   000407          BR     5$          ;Ignore unexpected interrupts on PRO
34         .ENDC   ;NE,PROASM
35         ;
36         ; Set up vectors to catch unexpected interrupts
37         ; Note: We encode the interrupt vector address in the PS --
38         ; the low-order two bits of the address are dropped (they are
39         ; always zero) and the remainder of the address is encoded in the
40         ; PS fields priority (high-order 3 bits) and n-z-v-c (low-order 4 bits).
41         ;
42 003574   012702   0000000    7$:    MOV     #UEXINT,R2      ;SEND UNEXPECTED INTERRUPTS TO THIS ROUTINE
43 003600   012700   000044          MOV     #44,R0        ;120 ENCODED IN PS FIELDS
44 003604   105737   0000000          TSTB   VUXIFL        ;ARE WE TO IGNORE UNEXPECTED INTERRUPTS?
45 003610   001004          BNE    10$           ;BR IF NOT
46 003612   012702   0000000    5$:    MOV     #UEXRTN,R2    ;ROUTINE TO GO TO TO IGNORE INTERRUPT
47 003616   012700   000340          MOV     #340,R0       ;SET PRIO=7 IN PS
48 003622   012701   000120    10$:   MOV     #120,R1       ;INIT ALL VECTORS STARTING AT 120
49 003626   010221          1$:    MOV     R2,(R1)+      ;SET PC FOR INTERRUPT
50 003630   010021          MOV     R0,(R1)+      ;SET PS FOR INTERRUPT (ENCODED ADDRESS VALUE)
51 003632   105737   0000000    6$:    TSTB   VUXIFL        ;ARE WE TO IGNORE UNEXPECTED INTS?
52 003636   001411          BEQ    2$            ;BR IF YES
53 003640   105737   0000000          TSTB   PROFLG        ;IS THIS A PRO?
54 003644   001006          BNE    2$            ;BR IF YES
55 003646   005200          INC    R0             ;ADVANCE ENCODED ADDRESS
56 003650   032700   000020          BIT    #20,R0        ;DID WE CARRY INTO "T"-FIELD?
57 003654   001402          BEQ    2$            ;BR IF NOT

```

\*\*\* Initialization taking over control from RT-11 \*\*\*

```

58 003656 062700 000020          ADD    #20,R0          ;FORCE CARRY OUT OF T-FIELD AND INTO PRID FIELD
59 003662 020127 000420          2#:   CMP    R1,#420        ;DONE ALL INTERRUPT VECTORS OF INTEREST?
60 003666 103757                   BLO    1#             ;BR IF NOT
61 003670 010237 000060          MOV    R2,@#60       ;CATCH CONSOLE TERMINAL VECTOR TOO
62 003674 012737 000014 000062    MOV    #14,@#62      ;ENCODED 60
63 003702 010237 000064          MOV    R2,@#64
64 003706 012737 000015 000066    MOV    #15,@#66      ;ENCODED 64
65                                     ;
66                                     ; Direct clock interrupt to a dummy RTI instruction to avoid it causing
67                                     ; trouble during the initialization process when things aren't set up
68                                     ; and ready to go.
69                                     ;
70 003714 012700 000340          11#:  MOV    #340,R0       ;PRIORITY 7 PS
71 003720 012737 000000G 000000G    MOV    #CLKRTI,@#CLKVEC;Send clock interrupt to RTI instruct for now
72 003726 010037 000002G          MOV    R0,@#CLKVEC+2
73                                     ;
74                                     ; Take over traps, EMT, BPT, etc.
75                                     ;
76 003732 005001                   CLR    R1             ;Start at location 0
77 003734 012721 000137          MOV    #137,(R1)+    ;[JMP @#JMPO] ==> 0
78 003740 012721 000000G          MOV    #JMPO,(R1)+  ;CATCH JUMPS TO LOCATION 0
79 003744 012721 000000G          MOV    #TRP4,(R1)+  ;TRAP 4
80 003750 005021                   CLR    (R1)+
81 003752 012721 000000G          MOV    #TRP10,(R1)+ ;TRAP 10
82 003756 005021                   CLR    (R1)+
83 003760 012721 000000G          MOV    #TRP14,(R1)+ ;TRAP 14 (BREAKPOINTS)
84 003764 010021                   MOV    R0,(R1)+
85 003766 012721 000000G          MOV    #TRP20,(R1)+ ;IOT TRAP
86 003772 005021                   CLR    (R1)+
87 003774 012721 000000G          MOV    #TRP24,(R1)+ ;POWER FAIL
88 004000 010021                   MOV    R0,(R1)+
89 004002 012721 000000G          MOV    #EMTENT,(R1)+;EMT
90 004006 005021                   CLR    (R1)+
91 004010 012721 000000G          MOV    #TRP34,(R1)+ ;TRAP
92 004014 005021                   CLR    (R1)+
93 004016 012737 000000G 000114    MOV    #MEMPAR,@#114 ;MEMORY PARITY TRAP
94 004024 010037 000116          MOV    R0,@#116
95 004030 012737 000000G 000244    MOV    #FPTRAP,@#244 ;TRAP 244 -- FLOATING POINT TRAP
96 004036 010037 000246          MOV    R0,@#246     ;Enter FPU trap at priority 7
97 004042 012737 000000G 000250    MOV    #TRP250,@#250 ;TRAP 250 -- MEMORY MANAGEMENT TRAP
98 004050 005037 000252          CLR    @#252
99                                     ;
100                                    ; Initialize the system mapped region.
101                                    ;
102 004054 010546                   MOV    R5,-(SP)      ;SAVE THE CURRENT CONTENTS OF R5
103 004056 012705 000006          MOV    #6,R5        ;INITIALIZE TO THE FIRST REGION
104 004062 004737 000000G          CALL   MAPSYS        ;CALL THE SYSTEM MAPPING ROUTINE
105 004066 012605                   MOV    (SP)+,R5     ;RESTORE THE PREVIOUS CONTENTS OF R5
106                                     ;
107                                     ; Set up Unibus mapping if needed
108                                     ;
109 004070 004737 004764'          CALL   SETUMP        ;SET UP UNIBUS MAPPING
110                                     ;
111                                     ; Initialize time-sharing lines.
112                                     ;
113 004074 004737 005376'          CALL   LININI        ;INIT LINES & SET UP VECTORS
114                                     ;

```

\* \* \* Initialization taking over control from RT-11 \* \* \*

```

115          ; Enable memory management
116          ; (The kernel-mode mapping registers are already set up)
117          ;
118 004100 052737 0000000 0000000     BIS      #MMENBL,@#SR0MMR;Turn on memory management
119 004106 105737 0000000             TSTB     SR3FLG           ;Does machine have memory management reg 3?
120 004112 001415                     BEQ      4$              ;Br if register does not exist (no ext. mem.)
121 004114 023727 0000000 010000     CMP      PHYMEM,#4096.   ;Does machine have at least 256Kb phys memory?
122 004122 103411                     BLD      4$              ;Br if not
123 004124 052737 0000000 0000000     BIS      #EMMAP,@#SR3MMR;Set extended memory on
124 004132 105737 0000000             TSTB     MEM256          ;Will TSX-Plus use at least 256Kb?
125 004136 001403                     BEQ      4$              ;Br if not
126 004140 052737 0000000 0000000     BIS      #IOMAP,@#SR3MMR;Turn on 22-bit memory management for I/O
127          ;
128          ; Initialize the memory allocation table
129          ;
130 004146 013737 0000000 0000000 4$:   MOV      MAPPAR,@#KPAR5  ;Map to memory allocation table
131 004154 013702 0000000             MOV      LOMAP,R2       ;Point to 1st user-page entry
132 004160 105022                     8$:   CLRB     (R2)+          ;Say page is free
133 004162 020237 0000000             CMP      R2,HIMAP       ;Done all user pages?
134 004166 103774                     BLD      8$              ;Loop if not
135 004170 112712 0000000             MOVB    #MA$SYS,(R2)    ;Set flag marking start of system pages
136          ;
137          ; Set up I/O device interrupt vectors.
138          ;
139 004174 004737 004766'             CALL     DEVVEC          ;SET UP DEVICE INTERRUPT VECTORS
140          ;
141          ; If we are running on a Professional, initialize the PI handler
142          ;
143          .IF      NE,PROASM
144 004200 105737 0000000             TSTB     PROFLG         ;Are we running on a Professional?
145 004204 001404                     BEQ      3$              ;Br if not
146 004206 004737 0000000             CALL     PROHAN         ;Initialize the PI handler
147 004212 004737 005314'             CALL     PIDVEN         ;Make device table entry for PI
148          .ENDC    ;NE,PROASM
149          ;
150          ; Initialize interrupt stack area
151          ;
152 004216 013702 0000000 3$:   MOV      INTSND,R2      ;Point to base of stack area
153 004222 012700 123456             MOV      #123456,R0     ;Get initialization value
154 004226 010022                     12$:  MOV      R0,(R2)+       ;Initialize the interrupt stack area
155 004230 020237 0000000             CMP      R2,INTSTK     ;Finished?
156 004234 103774                     BLD      12$            ;Loop if not
157 004236 112737 177777 0000000     MOVB    #-1,STKLVL     ;Say we are not running on interrupt stack
158          ;
159          ; Enter TSEXEC to complete initialization
160          ;
161 004244 000137 0000000             JMP      INIJMP         ;ENTER INITIALIZATION ROUTINE IN TSEXEC
162          ;
163          ; Abort the initialization
164          ;
165 004250 013737 000042' 0000000 INISTP: MOV     CLK100,@#CLKVEC ;Restore RT-11 clock vector
166 004256 013737 000044' 000004     MOV     RTRP4,@#4      ;Restore trap 4 vector
167 004264 013737 000046' 0000000     MOV     RTMNVN,@#RMDN  ;Restore RT-11 monitor pointer
168 004272          9$:   .EXIT          ;RETURN TO RT-11

```

```

1          .SBTTL  LODINI -- Load a segment over TSINIT
2          ;-----
3          ;  LODINI is called to read an overlay segment over TSINIT.
4          ;
5          ;  Inputs:
6          ;  R2 = Pointer to OSTABL entry for segment to be loaded.
7          ;  R5 = 64-byte block # where segment is to be loaded.
8          ;
9 004274 010146  LODINI: MOV     R1, -(SP)
10 004276 010346      MOV     R3, -(SP)
11 004300 010446      MOV     R4, -(SP)
12          ;
13          ;  Get pointer to linker-built overlay entry
14          ;
15 004302 016201 000004      MOV     OS$OVL(R2), R1  ;Get pointer to linker-built table
16          ;
17          ;  Determine how much code to read from the segment
18          ;
19 004306 016204 000000      MOV     OS$SIZ(R2), R4  ;Get # 64-byte blks allocated for segment
20 004312 072427 000005      ASH     #5, R4          ;Convert to # words
21 004316 020461 000000G    CMP     R4, 0. SIZ(R1) ;Compare with original segment code size
22 004322 101402          BLOS   1$             ;Br if segment was truncated by init
23 004324 016104 000000G    MOV     0. SIZ(R1), R4 ;Get code size
24          ;
25          ;  Read the segment into memory
26          ;
27 004330 010503 1$:      MOV     R5, R3          ;Get 64-byte block #
28 004332 072327 000006      ASH     #6, R3          ;Convert to byte address
29 004336          .READW  #AREA, #17, R3, R4, 0. BLK(R1)
30 004372 103406          BCS     10$           ;Br if error on read
31          ;
32          ;  Store the physical address of the segment into the overlay descriptor
33          ;
34 004374 010561 000000G    MOV     R5, 0. PAR(R1) ;Remember physical address of segment
35          ;
36          ;  Finished
37          ;
38 004400 012604          MOV     (SP)+, R4
39 004402 012603          MOV     (SP)+, R3
40 004404 012601          MOV     (SP)+, R1
41 004406 000207          RETURN
42          ;
43          ;  Error on read
44          ;
45 004410 10$:      .PRINT  #TSXHD
46 004416          .PRINT  #RDERR
47 004424          .EXIT

```

```

1          .SBTTL  INIOVL -- Load system overlays over TSINIT
2          ;-----
3          ; INIOVL is called to load into memory those system overlays that
4          ; are to be placed over the TSINIT code.
5          ;
6          ; Inputs:
7          ; Overlay segment information is in OSTABL.
8          ;
9 004426 010246 INIOVL: MOV     R2,-(SP)
10 004430 010546      MOV     R5,-(SP)
11          ;
12          ; Initialize pointer to start of memory area for overlays
13          ;
14 004432 013705 000140'      MOV     OVLBAS,R5      ;Start of area for overlays
15 004436 072527 177772      ASH     #-6,R5        ;Convert to 64-byte #
16 004442 042705 176000      BIC     #176000,R5    ;Clear possible propagated sign bits
17          ;
18          ; Begin loop to load each overlay that goes over TSINIT
19          ;
20 004446 012702 000576'      MOV     #OSTABL,R2    ;Point to 1st overlay segment entry
21 004452 005762 000002      1$:   TST     OS$FLG(R2) ;Does this segment go over TSINIT?
22 004456 001406              BEQ     2$            ;Br if not
23 004460 004737 004636'      CALL    KEYSEG       ;Remember base of some segments
24 004464 004737 004274'      CALL    LODINI       ;Load the segment
25 004470 066205 000000      ADD     OS$SIZ(R2),R5 ;Advance memory pointer
26 004474 062702 000006      2$:   ADD     #OS$$SZ,R2 ;Point to entry for next segment
27 004500 020237 001024'      CMP     R2,OSLAST   ;Finished all segments?
28 004504 103762              BLD     1$           ;Loop if not
29          ;
30          ; Initialize entry point vector for TSTIOX segment
31          ;
32 004506 013702 000000G      MOV     TIOBAS,R2    ;Get addr of base of TSTIOX
33 004512 072227 000006      ASH     #6,R2        ;Convert to byte address
34 004516 012705 000000G      MOV     #TIOVEC,R5  ;Point to entry point vector
35 004522 004737 004600'      CALL    ENTVEC      ;Set up entry point vector
36          ;
37          ; Initialize entry point vector for TSCASH segment
38          ;
39 004526 013702 000000G      MOV     CSHBAS,R2   ;Get addr of base of TSCASH
40 004532 001406              BEQ     3$            ;Br if TSCASH not loaded
41 004534 072227 000006      ASH     #6,R2        ;Convert to byte address
42 004540 012705 000000G      MOV     #CSHVEC,R5  ;Point to entry point vector
43 004544 004737 004600'      CALL    ENTVEC      ;Set up entry point vector
44          ;
45          ; Initialize entry point vector for TSLOCK segment
46          ;
47 004550 013702 000000G      3$:   MOV     LOKBAS,R2   ;Get addr of base of TSLOCK
48 004554 001406              BEQ     9$            ;Br if TSLOCK not loaded
49 004556 072227 000006      ASH     #6,R2        ;Convert to byte address
50 004562 012705 000000G      MOV     #LOKVEC,R5  ;Point to entry point vector
51 004566 004737 004600'      CALL    ENTVEC      ;Set up entry point vector
52          ;
53          ; Finished
54          ;
55 004572 012605      9$:   MOV     (SP)+,R5
56 004574 012602      MOV     (SP)+,R2
57 004576 000207      RETURN

```

```

1          .SBTTL  ENTVEC -- Set up entry point vector for overlay
2          ;-----
3          ; ENTVEC is called to set up addresses in an entry point vector for
4          ; overlay segments such as TSCASH that are loaded at addresses different
5          ; from where they are linked.
6          ;
7          ; Inputs:
8          ;   R2 = Address of base of segment.
9          ;   R5 = Pointer to vector that is to be initialized (word with -1 terminates)
10         ;
11 004600 010246  ENTVEC: MOV     R2,-(SP)
12 004602 010446          MOV     R4,-(SP)
13 004604 010546          MOV     R5,-(SP)
14 004606 010204          MOV     R2,R4          ;Get addr of base of segment
15 004610 062704 000004  ADD     #4,R4          ;Point to start of vector in segment
16 004614 005722          TST     (R2)+          ;Get value to use to relocate offsets
17 004616 012415 1$:    MOV     (R4)+,(R5)  ;Get offset to entry point within segment
18 004620 060225          ADD     R2,(R5)+      ;Convert to absolute address
19 004622 005715          TST     (R5)          ;Any more words to initialize?
20 004624 001774          BEQ     1$           ;Br if yes
21         ;
22         ; Finished
23         ;
24 004626 012605          MOV     (SP)+,R5
25 004630 012604          MOV     (SP)+,R4
26 004632 012602          MOV     (SP)+,R2
27 004634 000207          RETURN
  
```

KEYSEG -- Remember memory position of system overlays

```

1          .SBTTL  KEYSEG -- Remember memory position of system overlays
2          ;-----
3          ; KEYSEG is called to remember the physical memory position of some
4          ; key system overlay segments.
5          ;
6          ; Inputs:
7          ; R2 = Pointer to segment entry in OSTABL overlay table.
8          ; R5 = Base 64-byte block physical memory for segment.
9          ;
10         004636 010446 KEYSEG: MOV     R4,-(SP)
11         ;
12         ; Get the name of the segment out of the linker-built segment block
13         ;
14         004640 016200 000004         MOV     OS$OVL(R2),R0 ;Point to linker-built entry
15         004644 016004 000000G       MOV     0.ADR(R0),R4 ;Get the name of the segment
16         ;
17         ; See if this is a segment whose address we want to remember
18         ;
19         004650 020437 000156'        CMP     R4,R5MSG      ;Is this the TSMSG segment?
20         004654 001003                BNE     1$           ;Br if not
21         004656 010537 000000G       MOV     R5,MSGBAS    ;Remember base of TSMSG segment
22         004662 000436                BR      8$
23         004664 020437 000160'        1$:    CMP     R4,R5WIN    ;Is this the TSWIN segment?
24         004670 001003                BNE     3$           ;Br if not
25         004672 010537 000000G       MOV     R5,WINBAS   ;Remember base of TSWIN segment
26         004676 000430                BR      8$
27         004700 020437 000164'        3$:    CMP     R4,R5USR    ;Is this the TSUSR segment?
28         004704 001003                BNE     4$           ;Br if not
29         004706 010537 000000G       MOV     R5,USBAS    ;Remember base of TSUSR segment
30         004712 000422                BR      8$
31         004714 020437 000162'        4$:    CMP     R4,R5LOK    ;Is this the TSLOCK segment?
32         004720 001003                BNE     5$           ;Br if not
33         004722 010537 000000G       MOV     R5,LOKBAS   ;Remember base of TSLOCK segment
34         004726 000414                BR      8$
35         004730 020437 000166'        5$:    CMP     R4,R5CSH    ;Is this the TSCASH segment?
36         004734 001003                BNE     6$           ;Br if not
37         004736 010537 000000G       MOV     R5,CSHBAS   ;Remember base of TSCASH segment
38         004742 000406                BR      8$
39         004744 020437 000170'        6$:    CMP     R4,R5TIO    ;Is this the TSTIOX segment?
40         004750 001003                BNE     8$           ;Br if not
41         004752 010537 000000G       MOV     R5,TIOBAS   ;Remember base of module
42         004756 000400                BR      8$
43         ;
44         ; Finished
45         ;
46         004760 012604        8$:    MOV     (SP)+,R4
47         004762 000207                RETURN

```

KEYSEG -- Remember memory position of system overlays

```

1          .IF      NE,<PROASM-1> ; Assemble for PDP-11
2          .SBTTL   SETUMP -- Set up Unibus mapping if needed
3          ;-----
4          ; SETUMP is called to set up the Unibus map registers for 11/44 and
5          ; 11/70 systems which more than 256Kb of memory.
6          ; If Unibus mapping is needed, the Unibus map registers # 0-4 are
7          ; initialized for a 1-to-1 mapping with the low 40Kb of memory
8          ; so that I/O to system buffers in the low memory area can be done without
9          ; having to do Unibus mapping.
10         ;
11         ; Outputs:
12         ;   UBUSMP:  1==>Do Unibus mapping;  0==>Don't do Unibus mapping.
13         ;
14         SETUMP:  MOV      R2,-(SP)
15                 MOV      R3,-(SP)
16                 MOV      @#4,-(SP) ; SAVE TRAP VECTOR
17                 MOV      #9@,#4   ; CATCH TRAPS
18         ;
19         ; See if this is a type of maching that needs unibus mapping
20         ;
21                 TSTB     UBUSMP    ; Is UNIBUS mapping needed?
22                 BEQ      9$        ; Br if not
23         ;
24         ; Unibus mapping is needed
25         ; Load unibus map registers # 0-4 to point to low 48Kb of memory.
26         ;
27         2$:      MOV      #UMRADR,R5 ; POINT TO CONTROL REGISTER FOR UNIBUS MAP 0
28                 CLR      R4        ; START MAPPING TO BOTTOM OF MEMORY
29                 MOV      #5,R0     ; LOAD 5 MAP REGISTERS
30         1$:      MOV      R4,(R5)+  ; SET LOW-ORDER VALUE IN MAP REGISTER
31                 CLR      (R5)+     ; CLEAR HIGH-ORDER VALUE IN MAP REGISTER
32                 ADD      #8192.,R4 ; ADVANCE MEMORY ADDRESS
33                 SOB     R0,1$      ; LOOP IF MORE MAP REGISTERS TO LOAD
34         ;
35         ; Turn on Unibus mapping
36         ;
37                 BIS      #IOMAP,@#SR3MMR ; ENABLE UNIBUS MAPPING
38         ;
39         ; Finished
40         ;
41         9$:      MOV      (SP)+,@#4  ; RESTORE TRAP VECTOR
42                 MOV      (SP)+,R5
43                 MOV      (SP)+,R4
44                 RETURN
45         .IFF     ; NE,<PROASM-1> ; Following code for Pro-only assembly
46         ;
47         ; Define dummy SETUMP routine for Pro
48         ;
49         SETUMP:  RETURN
50         .ENDC   ; NE,<PROASM-1>

```

004764 000207

DEVVEC -- Set up device vectors

```

1          .SBTTL  DEVVEC -- Set up device vectors
2          ;-----
3          ; DEVVEC is called to set up device interrupt vectors for handlers
4          ; that have been loaded.
5          ;
6 004766 010146 DEVVEC: MOV     R1,-(SP)
7 004770 010346      MOV     R3,-(SP)
8 004772 010546      MOV     R5,-(SP)
9 004774 013746 0000000 MOV     @#KPAR5,-(SP) ; Save PAR 5 mapping
10         ;
11         ; Begin loop to set up vectors for each device
12         ;
13 005000 012701 000002      MOV     #2,R1          ; Get index # of 1st device after TT
14 005004 016103 0000000 1$:  MOV     HANENT(R1),R3 ; Get handler entry point address
15 005010 020327 0000006      CMP     R3,#6          ; Is this a real device?
16 005014 101436      BLOS    6$           ; Br if not
17         ;
18         ; See if we need to map PAR 5 to this handler
19         ;
20 005016 016100 0000000      MOV     HANPAR(R1),R0 ; Get PAR 5 base for this handler
21 005022 001402      BEQ     2$           ; Br if this is not a mapped handler
22 005024 010037 0000000      MOV     R0,@#KPAR5   ; Map PAR 5 to the handler
23         ;
24         ; Clear CQE and LQE in handler header
25         ;
26 005030 005023 2$:  CLR     (R3)+        ; Clear LQE (4th word in handler)
27 005032 005013      CLR     (R3)         ; Clear CQE (5th word in handler)
28 005034 162703 0000010      SUB     #10,R3       ; Point to 1st word of handler
29         ;
30         ; Set up interrupt vectors for this handler
31         ;
32 005040 005005      CLR     R5           ; Assume vector base address is 0
33 005042 005713      TST     (R3)         ; Any vectors to set up?
34 005044 001422      BEQ     6$           ; Br if no vectors to set up
35 005046 002403      BLT     5$           ; Br if multiple-vector
36 005050 004737 005140'      CALL    SETVEC       ; Set up the vector
37 005054 000416      BR      6$           ; Finished
38         ;
39         ; Multiple vectors.
40         ;
41 005056 012300 5$:  MOV     (R3)+,R0      ; Get offset to list of vector info
42 005060 006300      ASL     R0           ; Get byte offset to vector list
43 005062 060003      ADD     R0,R3       ; Get absolute address of vector table
44 005064 005713      TST     (R3)         ; Is this a PRO device with floating vectors?
45 005066 002005      BGE     7$           ; Br if not
46 005070 005723      TST     (R3)+        ; Point to word with device ID
47 005072 012346      MOV     (R3)+,-(SP) ; Get device ID
48 005074 004777 0000000      CALL    @RPRVEC     ; Get base vector location for device
49 005100 012605      MOV     (SP)+,R5     ; This is base of vector locations
50 005102 004737 005140' 7$:  CALL    SETVEC       ; Set up the vector
51 005106 005713      TST     (R3)         ; Any more vectors to set up?
52 005110 003374      BGT     7$           ; Br if yes
53         ;
54         ; See if there are more devices to set up.
55         ;
56 005112 062701 000002 6$:  ADD     #2,R1         ; Advance device table index
57 005116 020137 0000000      CMP     R1,NUMDEV   ; More to do?

```

```
58 005122 101730          BLOS      1$          ;Br if yes
59                      ;
60                      ; Finished
61                      ;
62 005124 012637 000000G   MOV      (SP)+, @#KPAR5
63 005130 012605          MOV      (SP)+, R5
64 005132 012603          MOV      (SP)+, R3
65 005134 012601          MOV      (SP)+, R1
66 005136 000207          RETURN
```

SETVEC -- Set up an interrupt vector for a device

```

1          .SBTTL  SETVEC -- Set up an interrupt vector for a device
2          ;-----
3          ; SETVEC is called to set up one interrupt vector for a device.
4          ;
5          ; Inputs:
6          ; R1 = Device index number.
7          ; R3 = Pointer into device handler to 3 word cells:
8          ;     1. Address of interrupt vector.
9          ;     2. Offset to interrupt entry point in handler.
10         ;     3. PS for interrupt.
11         ; R5 = Base address to add to vector locations.
12         ;
13         ; Outputs:
14         ; R3 = Points beyond 3 word info block in handler.
15         ;
16         ; Size of interrupt catching routine compiled for interrupts to
17         ; mapped handlers:
18         ;
19         ; MPIVSZ = 26. ; Amt of code compiled for mapped ints
20         ;
21         ; SETVEC: MOV R4, -(SP)
22         ;
23         ; See if this is a mapped handler
24         ;
25         ; TST HANPAR(R1) ; Is this a mapped handler
26         ; BNE 1$ ; Br if yes
27         ;
28         ; This is an unmapped handler.
29         ; Vector interrupts directly to the handler.
30         ;
31         ; MOV (R3)+, R0 ; Get address of interrupt vector
32         ; ADD R5, R0 ; Add base address to vector location
33         ; MOV R3, (R0) ; Store address of cell in handler
34         ; ADD (R3)+, (R0)+ ; Add offset to interrupt entry point
35         ; MOV (R3)+, (R0) ; Set PS for interrupt
36         ; BIS #340, (R0) ; Make sure priority = 7
37         ; BR 9$
38         ;
39         ; This is a mapped handler.
40         ; Vector the interrupt to a routine that performs the following functions:
41         ; 1. Save the current PAR 5 mapping.
42         ; 2. Map PAR 5 to the handler.
43         ; 3. Push a dummy PC and PS on stack that will send return from handler
44         ; to a routine that will restore the PAR 5 mapping.
45         ; 4. Jump into the handler interrupt entry point.
46         ;
47         ; 1$: MOV XMVBAS, R4 ; Point to area where we store interrupt rtn
48         ; MOV (R3)+, R0 ; Get address of interrupt vector
49         ; ADD R5, R0 ; Add base address to interrupt location
50         ; MOV R4, (R0)+ ; Direct interrupt to our routine
51         ; MOV #013746, (R4)+ ; [ MOV @#KPAR5, -(SP) ]
52         ; MOV #KPAR5, (R4)+
53         ; MOV #012737, (R4)+ ; [ MOV #par5val, @#KPAR5 ]
54         ; MOV HANPAR(R1), (R4)+
55         ; MOV #KPAR5, (R4)+
56         ; MOV #012746, (R4)+ ; [ MOV #340, -(SP) ]
57         ; MOV #340, (R4)+

```

SETVEC -- Set up an interrupt vector for a device

```

1      .IF      NE,PROASM
2      .SBTTL  PIDVEN -- Make device table entry for PI device
3      ;-----
4      ; If we are running on a Professional computer, PIDVEN is called to
5      ; make an entry in the device tables for the PI device.
6      ;
7 005314 010146  PIDVEN: MOV      R1,-(SP)
8      ;
9      ; Increase number of defined devices and get device table entry index
10     ; to use for the PI device.
11     ;
12 005316 062737 000002 000000G      ADD      #2,NUMDEV      ;One more device
13 005324 013701 000000G      MOV      NUMDEV,R1      ;Get device table index
14     ;
15     ; Set up information about the PI device
16     ;
17 005330 013761 000200' 000000G      MOV      R5OPI,PNAME(R1) ;Set device name
18 005336 012761 000000C 000000G      MOV      #<DS$SFN!DI$PI>,DVSTAT(R1) ;Set device status flags
19 005344 005061 000000G      CLR      DVFLAG(R1)      ;Clear other flags
20 005350 005061 000000G      CLR      DEVSIZ(R1)      ;Clear device size
21 005354 012761 000006G 000000G      MOV      #PIHAN+6,HANENT(R1);Set handler entry point (4th word)
22 005362 012761 000000G 000000G      MOV      #PROSIZ,HANSIZ(R1) ;Set handler size
23     ;
24     ; Finished
25     ;
26 005370 012601      MOV      (SP)+,R1
27 005372 000207      RETURN
28     .ENDC      ; NE,PROASM

```

PIDVEN -- Make device table entry for PI device

```

1          .IF      NE,<PROASM-1> ;If assembling for PDP-11
2          .SBTTL  LININI -- Initialize time-sharing lines
3          ;-----
4          ; LININI is called to initialize the time-sharing lines.
5          ; This consists of setting up interrupt vectors and setting control
6          ; flags in the status registers.
7          ;
8          LININI: MOV      R1,-(SP)
9                  MOV      R2,-(SP)
10                 MOV      R3,-(SP)
11                 MOV      R4,-(SP)
12                 MOV      R5,-(SP)
13          ;
14          ; Set up interrupt vectors for DL11 lines
15          ;
16                 MOV      #2,R1          ;Index for 1st line
17                 MOV      #340,R4       ;Priority 7 PS
18 1$:          BIT      ##DEAD,LSW3(R1) ;Is this line uninstalled?
19                 BNE      B$           ;Br if yes
20                 BIT      ##HARD,LSW3(R1) ;Is this line connected to hardware?
21                 BEQ      B$           ;Br if not
22                 CMP      LCDTYP(R1),#CDX$DL ;Is this a DL11 line?
23                 BNE      B$           ;Br if not
24          ;
25          ; DL-11 line
26          ;
27                 MOV      INVEC(R1),R5  ;GET ADDRESS OF INPUT VECTOR
28                 MOV      #INRCV,R2    ;END OF RECEIVING VECTOR
29                 MOV      #<OTRECV-6>,R3 ;START OF INPUT INTERRUPT ENTRY POINTS
30                 MOV      R1,R0        ;GET LINE NUMBER
31                 ASL      R0           ;4 BYTES PER INPUT INTERRUPT ENTRY POINT
32                 SUB      R0,R2        ;GET ADDRESS OF INPUT INTERRUPT ENTRY POINT
33                 ADD      R1,R0        ;6 BYTES PER OUTPUT INTERRUPT ENTRY POINT
34                 ADD      R0,R3        ;GET ADDRESS OF OUTPUT INTERRUPT ENTRY POINT
35                 MOV      R2,(R5)+     ;SET PC FOR INPUT INTERRUPT ENTRY POINT
36                 MOV      R4,(R5)+     ;SET PS FOR INPUT INTERRUPT
37                 MOV      R3,(R5)+     ;SET PC FOR OUTPUT INTERRUPT
38                 MOV      R4,(R5)+     ;SET PS FOR OUTPUT INTERRUPT
39          ;
40          ; Try next line
41          ;
42  B$:          ADD      #2,R1          ;ADVANCE LINE INDEX NUMBER
43                 CMP      R1,#LSTHL    ;MORE TO DO?
44                 BLOS     1$           ;BR IF YES
45          ;
46          ; Initialize multiplexers.
47          ;
48  SETMUX:     MOV      #LSTMX,R1        ;Get last mux index #
49                 BEQ      SETLIN       ;Br if there are no mux lines
50 3$:          CMP      MXTYPE(R1),#CDX$DZ ;Is this a DZ11, DH11, or DHV11?
51                 BEQ      1$           ;Br if DZ11
52                 CMP      MXTYPE(R1),#CDX$VH ;Is this a DHV11?
53                 BNE      2$           ;Br if not
54                 CALL     VHINIT       ;Initialize a DHV11
55                 BR      4$
56 2$:          CALL     DHINIT          ;Initialize a DH11
57                 BR      4$

```

PIDVEN -- Make device table entry for PI device

```

58          1$:      CALL      DZINIT          ; Initialize a DZ11
59          4$:      SUB        #2,R1          ; More to enable?
60                      BNE      3$           ; Br if yes
61          ;
62          ; Enable all lines
63          ;
64          SETLIN:  MOV        #LSTHL,R1      ; INDEX # OF LAST REAL LINE
65          4$:      BIT        ##DEAD,LSW3(R1) ; IS THIS LINE INSTALLED?
66                      BNE      2$           ; BR IF NOT
67                      BIT        ##HARD,LSW3(R1) ; Is this line connected to hardware?
68                      BEQ      2$           ; Br if not
69                      BIT        ##PHONE,ILSW2(R1) ; IS THIS A DIAL-UP LINE?
70                      BEQ      3$           ; BR IF NOT
71                      BIS        ##NOIN,LSW3(R1) ; IGNORE INPUT TILL DIAL UP OCCURS
72          3$:      MOV        LCDTYP(R1),R5   ; Get comm device type code
73                      MOV        LMXNUM(R1),R0 ; IS THIS A DL-11 OR MUX LINE?
74                      BEQ      1$           ; BR IF DL-11
75                      CMP        R5,#CDX#DZ   ; Is this a DZ11 or DH11?
76                      BEQ      6$           ; Br if DZ11
77                      CMP        R5,#CDX#VH   ; Is this a DH11 or DHV11?
78                      BEQ      7$           ; Br if DHV11
79          ;
80          ; DH11 line
81          ;
82                      CALL      DHLPRM       ; Set line parameters for DH11 line
83                      BR        2$
84          ;
85          ; DHV11 line
86          ;
87          7$:      CALL      VHLPRM       ; Set line parameters for DHV11 line
88                      BR        2$
89          ;
90          ; DZ-11 line
91          ;
92          6$:      MOV        LMXLN(R1),R2    ; Get line # within mux group
93                      BIS        #017030,R2  ; Set line enable flags
94                      MOV        R2,@MXLPR(R0) ; Enable the line
95                      BR        2$
96          ;
97          ; DL-11 line
98          ;
99          1$:      MOV        TSR(R1),R2      ; ADDRESS OF TRANSMITTER STATUS REGISTER
100          MOV        (R2),R3                ; CLEAR TRANSMITTER STATUS REGISTER
101          CLR        (R2)
102          MOV        RBR(R1),R2            ; ADDRESS OF RECEIVER BUFFER REGISTER
103          MOV        (R2),R3                ; CLEAR RECEIVER BUFFER REGISTER
104          MOV        RSR(R1),R2            ; ADDRESS OF RECEIVER STATUS REGISTER
105          CLR        (R2)
106          MOV        #RDINT,(R2)           ; ENABLE RECEIVER INTERRUPTS
107          ;
108          ; Do next line
109          ;
110          2$:      SUB        #2,R1
111          BGT        4$
112          ;
113          ; Finished
114          ;

```

115	MOV	(SP)+, R5
116	MOV	(SP)+, R4
117	MOV	(SP)+, R3
118	MOV	(SP)+, R2
119	MOV	(SP)+, R1
120	RETURN	

PIDVEN -- Make device table entry for PI device

```

1          .SBTTL  DHLPRM -- Set line parameters for a DH11 line
2          ;-----
3          ; DHLPRM is called to set up the line parameters for a DH11 line.
4          ;
5          ; Inputs:
6          ; R1 = Physical line index number.
7          ;
8          DHLPRM:
9          ;
10         ; Enable DM11 for this line
11         ;
12         MOV     LMXNUM(R1),RO    ;Get mux index number
13         TST     DM$CSR(RO)      ;Does this DH11 have DM11 modem control?
14         BEQ     2$              ;Br if not
15         BICB   #MF$LIN,@DM$CSR(RO) ;Clear line # field in DM11 CSR
16         BISB   LMXLN(R1),@DM$CSR(RO);Select line of interest
17         MOV     #MF$LE,@DM$LSR(RO);Enable the line
18         ;
19         ; Finished
20         ;
21         2$:    RETURN

```

PIDVEN -- Make device table entry for PI device

```
1          .SBTTL  VHLPRM -- Set line parameter values for DHV11 line
2          ;-----
3          ; Set the line parameter values for a DHV11 line.
4          ;
5          ; Inputs:
6          ;   R1 = Physical line index number.
7          ;
8          VHLPRM:
9          ;
10         ; Enable the line
11         ;
12         MOV    LMXNUM(R1),RO    ;Get mux index number
13         BIC    #VF$LIN,@VH$CSR(RO) ;Clear line # field in mux CSR
14         BISS   LMXLN(R1),@VH$CSR(RO) ;Set our line #
15         MOV    #<VF$RE>,@VH$LCR(RO) ;Enable the line
16         ;
17         ; Finished
18         ;
19         RETURN
```

PIDVEN -- Make device table entry for PI device

```

1          .SBTTL  DZINIT -- Initialize a DZ11 multiplexer
2          ;-----
3          ; DZINIT is called to initialize a DZ11 multiplexer.
4          ;
5          ; Inputs:
6          ; R1 = Mux index number.
7          ;
8          DZINIT:
9          ;
10         ; See if this DZ11 is installed
11         ;
12         TST     MXCSR(R1)      ;Is this DZ-11 installed?
13         BEQ     4$             ;Br if not
14         ;
15         ; Set up interrupt vector connections for this MUX
16         ;
17         CALL    MUXVEC         ;Set up interrupt vectors for this DZ11
18         ;
19         ; Start up the mux operation
20         ;
21         BIS     #ZCLR,@MXCSR(R1);Do master clear on DZ-11
22         1$:    BIT     #ZCLR,@MXCSR(R1);Wait for clear to finish
23         BNE     1$
24         2$:    MOV     @MXRBUF(R1),R0 ;Clear silo
25         BMI     2$
26         CLRB   @MXDTR(R1)      ;Disable all data sets
27         ;
28         ; Finished
29         ;
30         4$:    RETURN

```

PIDVEN -- Make device table entry for PI device

```

1          .SBTTL  MUXVEC -- Set up interrupt vectors for a multiplexer
2          ;-----
3          ; MUXVEC is called to set up the interrupt vector connections for
4          ; a DZ11, DH11, or DHV11 multiplexer.
5          ;
6          ; Inputs:
7          ; R1 = Mux index number.
8          ;
9          MUXVEC: MOV     R2, -(SP)
10             MOV     R3, -(SP)
11             MOV     R5, -(SP)
12          ;
13          ; Set interrupt vector for mux
14          ;
15             MOV     MXVEC(R1), R5 ; Get address of input interrupt vector
16             MOV     #INMXV, R2    ; End of receiving vector
17             MOV     #<OTMXV-6>, R3 ; Output interrupt table
18             MOV     R1, R0        ; Get mux index number
19             ASL     R0             ; 4 bytes per line in input int table
20             SUB     R0, R2        ; Get address of input entry point
21             ADD     R1, R0        ; 6 bytes per mux in output entry point table
22             ADD     R0, R3        ; Get address of output int entry point
23             MOV     R2, (R5)+     ; Set PC for input interrupt
24             MOV     #340, (R5)+   ; Set PS for output interrupt
25             MOV     R3, (R5)+     ; Set PC for output interrupt
26             MOV     #340, (R5)    ; Set PS for output interrupt
27          ;
28          ; Now store an instruction sequence of the form:
29          ;
30             JSR     R5, @#interrupt_routine
31             .WORD  mux_index
32          ;
33          ; to catch mux output interrupts and vector them to the interrupt routine.
34          ;
35             MOV     #004537, (R3)+ ; JSR R5, @#x
36             MOV     #DZOINT, R0    ; Assume this is a DZ11
37             CMP     MXTYPE(R1), #CDX$DZ ; Is this a DZ11?
38             BEQ     1$            ; Br if yes
39             MOV     #VHOINT, R0    ; Assume this is a DHV11
40             CMP     MXTYPE(R1), #CDX$VH ; Is this a DHV11?
41             BEQ     1$            ; Br if yes
42             MOV     #DHOINT, R0    ; Get interrupt routine for DH11's
43          1$: MOV     R0, (R3)+     ; Store address of interrupt routine
44             MOV     R1, (R3)      ; Store mux index number
45          ;
46          ; Finished
47          ;
48          9$: MOV     (SP)+, R5
49             MOV     (SP)+, R3
50             MOV     (SP)+, R2
51             RETURN

```

```
1          .SBTTL  DHINIT -- Initialize a DH11 multiplexer
2          ;-----
3          ; DHINIT is called to initialize a DH11 multiplexer
4          ;
5          ; Inputs:
6          ; R1 = Mux index number
7          ;
8          DHINIT:
9          ;
10         ; See if this DH11 is installed
11         ;
12         TST     MH$SCR(R1)      ; Is this DH11 installed?
13         BEQ     9$              ; Br if not
14         ;
15         ; Connect interrupt vector to DH11
16         ;
17         CALL    MUXVEC          ; Set up interrupt vectors for DH11
18         ;
19         ; Clear the multiplexer
20         ;
21         MOV     #HF$MC,@MH$SCR(R1) ; Set the master-clear flag
22         1$:    BIT     #HF$MC,@MH$SCR(R1) ; Wait for the master clear to be completed
23         BNE     1$
24         ;
25         ; Clear the DM11 scanner
26         ;
27         MOV     DM$CSR(R1),RO    ; Is there an associated DM11?
28         BEQ     3$              ; Br if not
29         MOV     #MF$CS,(RO)      ; Clear the scanner
30         BIS     #MF$CM,(RO)      ; Clear the multiplexer
31         3$:
32         ;
33         ; Finished
34         ;
35         9$:
36         RETURN
```

PIDVEN -- Make device table entry for PI device

```
1          .SBTTL  VHINIT -- Initialize a DHV11 multiplexer
2          ;-----
3          ; Perform initialization for a DHV11 mux.
4          ;
5          ; Inputs:
6          ; R1 = Mux index number.
7          ;
8          VHINIT:
9          ;
10         ; See if this DHV11 is installed
11         ;
12         TST    VH$CSR(R1)    ;Is this DHV11 installed?
13         BEQ    9$           ;Br if not
14         ;
15         ; Connect interrupt vector to DHV11
16         ;
17         CALL   MUXVEC       ;Set up interrupt vectors
18         ;
19         ; Clear the multiplexer
20         ;
21         MOV    #VF$MR,@VH$CSR(R1) ;Reset the multiplexer
22         1$:   BIT    #VF$MR,@VH$CSR(R1) ;Wait for reset to finish
23         BNE    1$
24         ;
25         ; Clean out the FIFO buffer in the mux
26         ;
27         2$:   MOV    @MXRBUF(R1),R0 ;Get contents of receiver buffer register
28         BLT    2$           ;Loop until RBUF empty
29         ;
30         ; Finished
31         ;
32         9$:   RETURN
```

```

1
2 ; -----
3 ; End of code that can be omitted for Pro-only systems
4 ;
5 ; . IFF ; NE, <PROASM-1> ; Begin code for Pro only
6 ;
7 ; This code is assembled only for Pro systems.
8 ; T/S line init routines for Pro only.
9 ;
10 005374 LINCHK:
11 005374 DHLPRM:
12 005374 VHLPRM:
13 005374 DZINIT:
14 005374 MUXVEC:
15 005374 DHINIT:
16 005374 000207 VHINIT:
17 ; RETURN
18 ;
19 ; LININI routine for Pro systems
20 005376 010146 LININI: MOV R1, -(SP)
21 005400 012701 000000G MOV #LSTLIN, R1 ; Get # of last line
22 ;
23 ; Determine if this line is connected to hardware
24 ;
25 005404 004737 005454' 1$: CALL LINTYP ; Determine the type of this line
26 005410 032761 000000G 000000G BIT ##HARD, LSW3(R1) ; Is this line connected to hardware?
27 005416 001411 BEQ 2$ ; Br if not
28 ;
29 ; Call Pro line initialization routine
30 ;
31 005420 004737 000000G CALL PROLIN ; Initialize Pro line
32 ;
33 ; Do some special init for phone lines
34 ;
35 005424 032761 000000G 000000G BIT ##PHONE, ILSW2(R1) ; Is this a dialup line?
36 005432 001403 BEQ 2$ ; Br if not
37 005434 052761 000000G 000000G BIS ##NOIN, LSW3(R1) ; Ignore input till dial up occurs
38 ;
39 ; Check next line
40 ;
41 005442 162701 000002 2$: SUB #2, R1 ; Get index # of next line
42 005446 003356 BGT 1$ ; Loop if more lines to do
43 ;
44 ; Finished
45 ;
46 005450 012601 MOV (SP)+, R1
47 005452 000207 RETURN
48 ;
49 ; End of Pro-only code
50 ;
51 ; . ENDC ; NE, <PROASM-1>

```

LINTYP -- Determine the type of a line

```

1          .SBTTL  LINTYP -- Determine the type of a line
2          ;-----
3          ; LINTYP is called to determine if the current line is a time-sharing line
4          ; a CL line, or a non-hardware connected line.
5          ;
6          ; Inputs:
7          ; R1 = Line index number
8          ;
9 005454 020127 000000G LINTYP: CMP      R1,#LSTPL      ;Is this a time-sharing line?
10 005460 101422          BLOS     1$          ;Br if yes
11 005462 020127 000000G      CMP      R1,#FSTIOL     ;Is this a CL line?
12 005466 103004          BHIS     2$          ;Br if yes
13 005470 012761 177777 000000G      MOV      #-1,LCLUNT(R1) ;Say line not in use as a CL line
14 005476 000432          BR       9$
15          ;
16          ; This is a CL line
17          ;
18 005500 016100 000000G 2$:      MOV      LCLUNT(R1),RO ;Get the CL unit index number
19 005504 010160 000000G      MOV      R1,CL$LIX(RO) ;Say which line is assoc with this CL unit
20 005510 012761 000000G 000000G      MOV      #CLOTIR,LOUTIR(R1) ;Set terminal output interrupt routine
21 005516 012761 000000G 000000G      MOV      #CLINCP,LINIR(R1) ;Set terminal input interrupt routine
22 005524 000414          BR       8$
23          ;
24          ; This is a time-sharing line
25          ;
26 005526 012761 177777 000000G 1$:      MOV      #-1,LCLUNT(R1) ;Say line is not in use as a CL unit
27 005534 012761 177777 000000G      MOV      #-1,LXCL(R1) ;Line is not cross-connected to CL unit
28 005542 012761 000000G 000000G      MOV      #NEDCHR,LOUTIR(R1) ;Set terminal output interrupt routine
29 005550 012761 000000G 000000G      MOV      #TTINCP,LINIR(R1) ;Set terminal input interrupt routine
30 005556 052761 000000G 000000G 8$:      BIS      #$HARD,LSW3(R1) ;This line is connected to hardware
31          ;
32          ; Finished
33          ;
34 005564 000207          9$:      RETURN

```

\*\*\* Initialization done with RT-11 running \*\*\*

```

1          .SBTTL  *** Initialization done with RT-11 running ***
2          ;-----
3          ; Initialization at start of execution of TSX.
4          ;
5          ; The initialization done in this section uses the running RT-11 system
6          ; to perform functions for it.
7          ;
8 005566   INITGD:
9          ;
10         ; Save some RT-11 pointers in case we abort the initialization
11         ;
12 005566   013737   000004   000044'       MOV     @#4,RTTRP4       ;Save trap 4 vector
13 005574   013737   000000G 000046'       MOV     @#RMON,RTMNVG   ;RT-11 monitor pointer
14 005602   013737   000000G 000042'       MOV     @#CLKVEC,CLK100 ;Clock vector (defined in TSGEN at 100)
15         ;
16         ; Get the current time of day which we will use later to make sure
17         ; the line time clock is working.
18         ;
19 005610   .GTIM   #AREA,#SYTIMH   ;Get the current time of day
20         ;
21         ; Trap ^C for later test so we can restore clock vector
22         ;
23 005630   .SCCA   #AREA,#CCAFLE   ;Catch control-C
24         ;
25         ; Check for TSGEN size overflow
26         ;
27 005650   012700   000000G       MOV     #GENTOP,RO      ;Get top of TSGEN
28 005654   162700   037776       SUB     #<40000-2>,RO   ;Will TSKMON have problems?
29 005660   003422                                     BLE     15#            ;Continue if not
30 005662   010046       MOV     RO,-(SP)        ;Save overflow size
31 005664   .PRINT  #TSXHD          ;Print error message
32 005672   .PRINT  #TOOBIG        ;
33 005700   .PRINT  #REDUCE        ;
34 005706   012600       MOV     (SP)+,RO        ;Recover amount of overflow
35 005710   004737   025436'       CALL   PRTDEC          ;
36 005714   .PRINT  #BYTES          ;
37 005722   000137   004250'       JMP     INISTP         ;
38         ;
39         ; Initialize the system stack (below 1000)
40         ;
41 005726   012701   000000G   15#:   MOV     #SEND,R1      ;Point to bottom of stack
42 005732   012700   123456       MOV     #123456,RO     ;Get initialization value
43 005736   010021   13#:   MOV     RO,(R1)+     ;Initialize the stack
44 005740   020127   000000G       CMP     R1,#SS         ;Reached top of the stack area?
45 005744   103774       BLO    13#            ;Loop if not
46 005746   010106       MOV     R1,SP         ;Run on system stack
47         ;
48         ; Make sure we are not already running under TSX.
49         ;
50 005750   .SERR          ;DON'T DIE ON ERRORS
51 005756   012700   000242'       MOV     #GTLIN,RO     ;TSX EMT TO GET LINE NUMBER
52 005762   104375       EMT    375            ;TRY A TSX EMT
53 005764   103410       BCS    1#            ;BR IF NOT UNDER TSX
54 005766   .PRINT  #TSXHD          ;ALREADY UNDER TSX
55 005774   .PRINT  #TSXRUN        ;
56 006002   000137   004250'       JMP     INISTP         ;
57 006006   1#:   .HERR          ;REABLE FATAL ERRORS

```

\*\*\* Initialization done with RT-11 running \*\*\*

```

58 ;
59 ; Make sure this machine has memory management facilities.
60 ;
61 006014 ; .IRPSET #AREA,#NOXM ;CATCH TRAPS
62 006034 005737 000000G TST @#SROMMR ;TRY TO ACCESS MEMORY MANAGEMENT REGISTER
63 006040 ; .TRPSET #AREA,#0 ;Release trap control
64 ;
65 ; Request all available memory from RT-11.
66 ;
67 006056 ; .SETTOP #-2 ;REQUEST ALL AVAILABLE MEMORY
68 006064 010037 000132' MOV RO, TOPMEM ;REMEMBER WHERE TOP OF MEMORY IS
69 006070 020027 000000G CMP RO, #VPAR5 ;TSX CANNOT EXTEND ABOVE PAR5 BASE ADDRESS
70 006074 101402 BLOS 3# ;BR IF RT-11 IS BELOW THAT
71 006076 012700 000000G MOV #VPAR5, RO ;SET PAR5 BASE AS UPPER LIMIT ON TSX SIZE
72 006102 010037 000302' 3#: MOV RO, MEMLIM ;TSX MAY NOT EXCEED THIS UPPER LIMIT
73 ;
74 ; Lock USR in memory for speed
75 ; (Set USR to swap over TSEMT to get out of the way)
76 ;
77 006106 012705 177776' MOV #TSINIT-2, R5 ;GET THE BASE OF TSINIT
78 006112 ; .GVAL #AREA, #374 ;GET SIZE OF RT-11 USR MODULE
79 006132 160005 SUB RO, R5 ;ALLOCATE SPACE BELOW TSINIT FOR USR
80 006134 010537 000046 MOV R5, @#46 ;SET USR TO SWAP OVER TSEMT
81 006140 5#: .LOCK ;LOCK USR IN MEMORY
82 ;
83 ; Determine if we are to run system with the system debugger
84 ;
85 006142 032737 000000G 000000G BIT #CHAIN, @#JSWLOC ;WERE WE CHAINED TO?
86 006150 001406 BEQ 10# ;BR IF NOT
87 006152 023737 000510 000222' CMP @#510, R500DT ;SHOULD WE RUN UNDER ODT?
88 006160 001002 BNE 10# ;BR IF NOT
89 006162 005237 000034' INC ODTFLG ;SET FLAG SAYING DEBUGGER WANTED
90 ;
91 ; Call Pro TSX initialization only if assembling for the Pro
92 ; Jump to INISTP if checking fails.
93 ;
94 006166 10#:
95 ; .IF NE, PROCID ;** Do if assembling for pro only **
96 006166 004737 025562' CALL INSCHK ;PERFORM VERIFICATION AND DECRYPTION FOR PRO
97 ; .ENDC ;NE, PROCID
98 ;
99 ; Allocate non-initialized buffer space over TSINIT.
100 ;
101 006172 012705 000000' MOV #TSINIT, R5 ;Allocate buffer space over TSINIT
102 006176 004737 011652' CALL ALOCFB ;Do allocation
103 006202 004737 012316' CALL ALCSLO ;Allocate silo buffers for lines
104 006206 020527 005566' CMP R5, #INITGO ;Are we beyond code that takes over control?
105 006212 103002 BHS 12# ;Br if yes
106 006214 012705 005566' MOV #INITGO, R5 ;Advance up to initial code
107 ;
108 ; Allocate the interrupt stack over TSINIT
109 ; If we are running on a Pro, allocate buffer for the PI handler
110 ; initialization code over the interrupt stack area.
111 ;
112 001274 PIINSZ = 700. ;Space needed for PI init code
113 006220 010537 000000G 12#: MOV R5, INTSND ;Ptr to base of interrupt stack
114 006224 062705 000002 ADD #2, R5 ;Always leave last word of stack for flag val

```

\* \* \* Initialization done with RT-11 running \* \* \*

```

115 006230 013701 000000G      MOV     @#RMON,R1      ;Get pointer to RT-11 RMON base
116 006234 032761 000000G 000370  BIT     #CW$PRO,370(R1) ;Are we running on a PRO?
117 006242 001407          BEQ     11$           ;Br if not
118 006244 105237 000000G      INCB   PROFLG        ;Set flag saying this is a PRO-350
119 006250 010537 000150'      MOV     R5,PROBUF     ;Save pointer to buffer area
120 006254 062705 001274      ADD     #PIINSZ,R5    ;Allocate space for buffer
121 006260 000402          BR      14$           ;
122 006262 062705 000000G      11$:   ADD     #INTSSZ,R5 ;Allocate space for interrupt stack
123 006266 010537 000000G      14$:   MOV     R5,INTSTK  ;Address of top of interrupt stack
124                                     ;
125                                     ; Allocate space for those overlays that go over TSINIT
126                                     ;
127 006272 004737 021624'      CALL   OVLPOS        ;Determine how much space to alloc for overlay
128                                     ;
129                                     ; Note: from this point onward we are carrying the address of the
130                                     ; base of the free memory area in R5.
131                                     ;
132 006276 020527 025556'      CMP     R5,#INITOP    ;Have we allocated up to top of TSINIT?
133 006302 103002          BHS    4$            ;Br if yes
134 006304 012705 025556'      MOV     #INITOP,R5   ;Advance to top of TSINIT
135                                     ;
136                                     ; Allocate a 2048. byte work buffer
137                                     ;
138 006310 004737 007454'      4$:    CALL   ALCWRK     ;Allocate work buffer
139                                     ;
140                                     ; Allocate empty Region Control Blocks for use by handlers
141                                     ;
142 006314 004737 007510'      CALL   ALCHRB        ;
143                                     ;
144                                     ; If we were started in debug mode, load ODT.
145                                     ;
146                                     ; . IF EQ,PROCID      ;Don't allow ODT for production PRO version
147                                     ; TST ODTFLG        ;Are we to load system debugger?
148                                     ; BEQ 2$            ;Br if not
149                                     ; CALL GETODT      ;Load ODT and start it
150                                     ; . ENDC ;EQ,PROCID
151                                     ;
152                                     ; Initialize memory management registers for 1-to-1 mapping but
153                                     ; leave memory management turned off
154                                     ;
155 006320 004737 015356'      2$:    CALL   MEMINI     ;Initialize memory management
156                                     ;
157                                     ; Extract information from RT-11 configuration and sysgen words.
158                                     ;
159 006324 013701 000000G      MOV     @#RMON,R1    ;GET POINTER TO BASE OF RMON
160 006330 016102 000300      MOV     300(R1),R2   ;GET RT-11 CONFIGURATION WORD
161 006334 042702 000000C      BIC     #CW$GDH+CW$BTH+CW$LGS,R2 ;RESET A FEW FLAGS
162 006340 052702 000000C      BIS     #CW$FB+CW$FGJ+CW$USR+CW$XM,R2 ;SET A FEW FLAGS
163 006344 010237 000000G      MOV     R2,CONFIG    ;INITIALIZE OUR CONFIGURATION WORD
164                                     ; Now get extended configuration word.
165 006350 016137 000370 000000G  MOV     370(R1),CONF2 ;EXTENDED CONFIGURATION WORD
166 006356 052737 000000G 000000G  BIS     #CW$ESP,CONF2 ;SET EXIT NO SWAP FLAG
167 006364 123727 000000G 000000G  CMPB   VBUSTP,#QBUS  ;Is this a Q-bus machine?
168 006372 001003          BNE    25$          ;Br if not
169 006374 052737 000000G 000000G  BIS     #CW$QBS,CONF2 ;Set QBUS flag
170                                     ; And sysgen option word.
171 006402 016102 000372      25$:   MOV     372(R1),R2

```

\*\*\* Initialization done with RT-11 running \*\*\*

```
172 006406 042702 000000C      BIC      #SG$ELG+SG$PAR+SG$MTS,R2
173 006412 052702 000000C      BIS      #SG$MMU+SG$MTM+SG$IOT+SG$TSX,R2
174 006416 010237 000000G      MOV      R2,SYSGEN      ;INITIALIZE OUR SYSGEN WORD
175                               ; Get system version number
176 006422      .GVAL      #AREA,#276      ;GET RT-11 SYSTEM VERSION NUMBER
177 006442 010037 000000G      MOV      R0,SYsver      ;SET AS TSX-PLUS VERSION NUMBER
```

\* \* \* Initialization done with RT-11 running \* \* \*

```

1
2 ; Set up a few clock constants based on clock frequency.
3 ; See if we have a 50 or 60 Hz clock
4 ;
5 006446 032737 000000G 000000G INICLK: BIT #CW#50H, CONFIG ; 50 or 60 Hz clock?
6 006454 001017 BNE 2# ; Br if 50 Hz
7 ;
8 ; 60 Hz clock
9 ;
10 006456 012737 000074 000000G MOV #60., TK1SEC ; Clock ticks per 1 second
11 006464 012737 000036 000000G MOV #30., TK5VAL ; Clock ticks per 0.5 seconds
12 006472 012737 000264 000000G MOV #180., TK3SVL ; Clock ticks per 3 seconds
13 006500 012737 000006 000000G MOV #6., TK1VAL ; Clock ticks per 0.1 seconds
14 006506 012700 001130 MOV #600., R0 ; Get # clock ticks per 10 seconds
15 006512 000416 BR B#
16 ;
17 ; 50 Hz clock
18 ;
19 006514 012737 000062 000000G 2#: MOV #50., TK1SEC ; Clock ticks per 1 second
20 006522 012737 000031 000000G MOV #25., TK5VAL ; Clock ticks per 0.5 seconds
21 006530 012737 000226 000000G MOV #150., TK3SVL ; Clock ticks per 3 seconds
22 006536 012737 000005 000000G MOV #5., TK1VAL ; Clock ticks per 0.1 seconds
23 006544 012700 000764 MOV #500., R0 ; Get # clock ticks per 10 seconds
24 ;
25 ; Set number of clock ticks per day
26 ;
27 006550 012702 020700 B#: MOV #8640., R2 ; (# seconds per day) / 10.
28 006554 070200 MUL R0, R2 ; Get # clock ticks per day
29 006556 010237 000000G MOV R2, DATIMH ; High-order value
30 006562 010337 000000G MOV R3, DATIML ; Low-order value
31 ;
32 ; Do a fast check to make sure specified T/S line addresses are ok.
33 ;
34 006566 004737 005374' CKLIN: CALL LINCHK ; CHECK T/S LINE ADDRESSES
35 ;
36 ; Do PRO-350 system initialization
37 ;
38 ; IF NE, PROASM
39 006572 032737 000000G 000000G BIT #CW#PRO, CFG2 ; Are we running on a PRO-350?
40 006600 001405 BEQ INIDEV ; Br if not
41 006602 004737 000000G CALL PROINI ; Do PRO-350 initialization
42 006606 012737 000000G 000000G MOV #PIDRIV, PIDPTR ; Set up pointer to clock driven PI routine
43 ; ENDC ; NE, PROASM
44 ;
45 ; Make entry in device handler table for TT device.
46 ;
47 006614 013737 000172' 000000G INIDEV: MOV R50TT, PNAME ; PERMANENT NAME "TT"
48 006622 012737 000000G 000000G MOV #DI#TT, DVSTAT ; SET DEVICE STATUS FLAGS FOR TT
49 006630 005037 000000G CLR DVFLAG
50 006634 005037 000000G CLR DEVSIZ
51 006640 012737 000002 000000G MOV #2, HANENT ; SET UP HANENT SO HANDLER LOOKS RESIDENT
52 006646 005037 000000G CLR NUMDEV ; IT IS DEVICE # 0
53 ;
54 ; Make device table entry for LD (logical disk) device
55 ;
56 006652 012737 177777 000000G MOV #-1, LDDEVX ; ASSUME LD SUPPORT NOT WANTED
57 006660 105737 000000G TSTB VLDSYS ; IS LD SUPPORT GENNED IN?

```

\* \* \* Initialization done with RT-11 running \* \* \*

```

58 006664 001427          BEQ      6$          ;BR IF NOT
59 006666 062737 000002 000000G  ADD      #2,NUMDEV    ;ONE MORE DEVICE
60 006674 013701 000000G  MOV      NUMDEV,R1    ;GET DEVICE TABLE INDEX
61 006700 010137 000000G  MOV      R1,LDDEVX    ;REMEMBER INDEX NUMBER FOR LD DEVICE
62 006704 013761 000176' 000000G  MOV      R5OLD,PNAME(R1) ;SET DEVICE NAME ("LD")
63 006712 012761 000000C 000000G  MOV      #<DS$DIR+DS$SFN+DS$VSZ+DI$LD>,DVSTAT(R1);SET DEV STATUS FLAGS
64 006720 012761 000000G 000000G  MOV      #DX$EBA,DVFLAG(R1);Say buffers must be on even byte boundaries
65 006726 005061 000000G  CLR      DEVSIZ(R1)
66 006732 012761 000002 000000G  MOV      #2,HANENT(R1) ;SAY HANDLER IS RESIDENT
67 006740 004737 014156'  CALL     LDINIT      ;DETERMINE LD TRANSLATION TABLE FORMAT
68
69 ; Make device table entry for CL (communications line) device
70
71 006744 005727 000000G  6$:     TST      #CLTOTL ;Are there any communications lines?
72 006750 001402          BEQ      8$          ;Br if not
73 006752 004737 013436'  CALL     CLINIT      ;Initialize CL handler
74
75 ; Disable clock interrupts.
76
77 006756 012737 000002 000000 8$:     MOV      #2,@#0    ;LOAD RTI IN LOCATION 0
78 006764 005037 000000G  CLR      @#CLKVEC    ;ATTACH CLOCK INTERRUPT TO 0
79 006770 032737 000000G 000000G  BIT      #CW$PRO,CONF02 ;ARE WE RUNNING ON A PRO?
80 006776 001402          BEQ      1$          ;BR IF NOT
81 007000 005037 000230          CLR      @#230      ;380 CLOCK INTERRUPT VECTOR
82
83 ; Set up memory parity control
84
85 007004 004737 016426'  1$:     CALL     PARSET    ;SET UP MEMORY PARITY CONTROL
86
87 ; Determine how much memory is installed on machine
88
89 007010 004737 015462'  CALL     MEMTST      ;FIND OUT HOW MUCH PHYSICAL MEMORY THERE IS
90
91 ; Set up information about the size of the job context area
92
93 007014 004737 015756'  CALL     CXTALC      ;Determine size of job context area
94
95 ; Load TSX-Plus device handlers that go in low memory
96
97 007020 004737 016430'  CALL     GETHNL      ;Load low memory handlers
98
99 ; Reserve space for interrupt vector intercept routines for mapped handlers
100
101 007024 010537 000122'  MOV      R5,XMVBAS   ;Save address of base of area for XM vectors
102 007030 013701 000124'  MOV      NMXHAN,R1   ;Get # mapped handlers
103 007034 006301          ASL      R1          ;Reserve room for 2 interrupts per handler
104 007036 062701 000000G  ADD      #NXIVMH,R1  ;Add # requested extra interrupt vectors
105 007042 070127 000032          MUL      #MPIVSZ,R1 ;Calc space needed for interrupt entry code
106 007046 060105          ADD      R1,R5      ;Advance the address of free memory
107
108 ; Set up device index number and unit number for "SY:" device.
109
110 007050 004737 025006'  CALL     SETSY       ;SET UP INFO ABOUT SY DEVICE
111
112 ; Open channel to TSKMON and set up information about it.
113
114 007054 004737 013134'  CALL     OPNKMN      ;OPEN CHANNEL TO TSKMON

```

\* \* \* Initialization done with RT-11 running \* \* \*

```

115 ;
116 ; Set up information about the IND program
117 ;
118 007060 004737 014236' CALL INDINI ; INITIALIZE FOR IND PROGRAM
119 ;
120 ; Initialize the TSXUCL data file
121 ;
122 007064 004737 015000' CALL UCLINI
123 ;
124 ; Set name of device that UCL program is to be run from
125 ;
126 007070 013737 000000G 000000G MOV SYNAME,UCLNAM ; SET DEVICE NAME FOR UCL PROGRAM
127 ;
128 ; Initialize spooling system
129 ;
130 007076 004737 010522' CALL SPLINI ; INITIALIZE SPOOLING SYSTEM
131 ;
132 ; Open system swap file
133 ;
134 007102 105737 000000G TSTB VSWPFL ; IS JOB SWAPPING ALLOWED?
135 007106 001402 BEQ 3# ; BR IF NOT
136 007110 004737 007564' CALL OPNSWP ; OPEN THE SYSTEM SWAP FILE
137 ;
138 ; Open swap file used for PLAS regions
139 ;
140 007114 004737 010216' 3#: CALL OPNRSF ; Open PLAS region swap file
141 ;
142 ; Set up information about which devices need to have their I/O mapped
143 ;
144 007120 004737 021622' CALL SETMIO ; Set up information about mapped I/O
145 ;
146 ; We are finished allocating low-memory buffer space.
147 ;
148 007124 010500 MOV R5,R0 ; ENSURE WE DON'T OVERFLOW 40KB
149 007126 004737 025312' CALL CHKMEM ; ABORT IF > 40KB OR INTO RT-11
150 ;
151 ; From this point on carry the free memory address in R5
152 ; as a 64-byte block # in physical memory.
153 ;
154 007132 010537 000000G MOV R5,UMSYTP ; SAVE ADDRESS OF NON-EXTENDED SYSTEM TOP
155 007136 062705 000077 ADD #77,R5 ; BOUND UP TO 64-BYTE BOUNDARY
156 007142 072527 177772 ASH #-6,R5 ; CONVERT TO 64-BYTE BLOCK #
157 007146 042705 176000 BIC #176000,R5 ; KILL SIGN EXTENSION
158 ;
159 ; Allocate buffer space that is not constrained to 40Kb TSX-Plus region.
160 ;
161 007152 004737 012536' CALL ALBFX ; ALLOCATE EXTENDED BUFFERS
162 ;
163 ; We will now do some allocation from the top of physical memory downward.
164 ; Save the base of free memory in R4 and get the top of free memory to R5.
165 ;
166 007156 010504 MOV R5,R4 ; Save the base of free memory in R4
167 007160 010437 000136' MOV R4,FMEMLO ; Save pointer above top of alloc low memory
168 007164 013705 000134' MOV FMEMHI,R5 ; Get 64-byte block # of free high memory
169 ;
170 ; Load any mapped system code
171 ;

```

\* \* \* Initialization done with RT-11 running \* \* \*

```

172 007170 004737 022234'          CALL   GETMAP          ;LOAD USR, EMT, MSG, LOCK, SPOOL, etc.
173                               ;
174                               ; Load any shared run-time systems
175                               ;
176 007174 005727 000000G          TST     #NUMRDB          ;Do we need to load any shared run-times?
177 007200 001415                   BEQ     4$                ;Br if not
178 007202 012701 000000G          MOV     #RDB,R1         ;Point to 1st run-time descriptor block
179 007206 010502                   MOV     R5,R2          ;Save initial memory pointer
180 007210 004737 023700'          5$:   CALL   GETSRT          ;Load a shared run-time system
181 007214 062701 000000G          ADD     #RT##SZ,R1     ;Point to next shared run-time descriptor
182 007220 020127 000000G          CMP     R1,#RDBEND     ;Are there more to load?
183 007224 103771                   BLD     5$              ;Br if yes
184 007226 160502                   SUB     R5,R2          ;Compute amt of space used by run-times
185 007230 010237 000000G          MOV     R2,SRTSIZ     ;Save total run-time size
186 007234                          4$:
187                               .IF     NE,PROASM
188                               ;
189                               ; If we are running on a Pro. load the PI handler like a shared run-time
190                               ;
191 007234 105737 000000G          TSTB   PROFLG          ;Are we running on a Pro?
192 007240 001410                   BEQ     10$            ;Br if not
193 007242 012701 000310'          MOV     #PISRT,R1     ;Point to dummy shared run-time block for PI
194 007246 010502                   MOV     R5,R2          ;Save current memory pointer
195 007250 004737 023700'          CALL   GETSRT          ;Load PI handler like a shared run-time
196 007254 160502                   SUB     R5,R2          ;Calculate amt of space used by PI handler
197 007256 060237 000000G          ADD     R2,MHNSIZ     ;Count in mapped-handler size
198                               .ENDC   ;NE,PROASM
199                               ;
200                               ; Load any mapped handlers
201                               ;
202 007262 004737 017736'          10$:  CALL   GETHND          ;Load mapped handlers
203                               ;
204                               ; Allocate space for data cache buffers and control tables
205                               ;
206 007266 004737 024312'          CALL   CSHBUF          ;Allocate space for data cache
207                               ;
208                               ; We have finished allocating all of the memory used by the system.
209                               ; Allocate and initialize a memory map table that will be used to
210                               ; show which pages are available for user jobs.
211                               ;
212 007272 004737 016116'          CALL   MAPALC          ;Allocate memory map table
213                               ;
214                               ; Set up info about maximum memory space available to jobs
215                               ;
216 007276 004737 016302'          CALL   SETJSZ          ;SET JOB SIZE INFO
217                               ;
218                               ; Set up date and time
219                               ;
220 007302 013702 000000G          MOV     SYTIML,R2     ;Save time we got at start of init
221 007306                   .GTIM  #AREA,#SYTIMH   ;SET TIME OF DAY
222 007326                   .DATE                   ;GET DATE
223 007334 010037 000000G          MOV     R0,SYSDAT     ;SET SYSTEM DATE
224 007340 020237 000000G          CMP     R2,SYTIML     ;Make sure some time has elapsed
225 007344 001010                   BNE     11$            ;Br if clock is running
226 007346                   .PRINT #TSXHD          ;Print error message heading
227 007354                   .PRINT #NOCLK          ;Print clock-not-working message
228 007362 000137 004250'          JMP     INISTP         ;Abort initialization

```

\* \* \* Initialization done with RT-11 running \* \* \*

```
229 ;
230 ; Unlock the USR so that TSEMT will be swapped back in.
231 ;
232 007366 11$: .UNLOCK ;RELEASE USR
233 ;
234 ; Read back into memory that part of the resident portion of TSX
235 ; that we overlayed with our work buffer.
236 ;
237 007370 013702 000154' MOV WRKSIZ,R2 ;Get size of work buffer
238 007374 006202 ASR R2 ;Convert to # words
239 007376 013703 000152' MOV WRKBUF,R3 ;Get address of work buffer area
240 007402 000241 CLC ;Convert to block # in TSX.SAV file
241 007404 006003 ROR R3
242 007406 000303 SWAB R3
243 007410 .READW #AREA,#17,WRKBUF,R2,R3 ;Read back TSX over work buffer
244 ;
245 ; See if user requested control-C abort
246 ;
247 007444 004737 025352' CALL CCATST ;JUMP TO INISTP IF ^C^C BEFORE THIS POINT
248 ;
249 ; Jump to code at end of TSINIT which takes over control from RT-11
250 ;
251 007450 000137 003534' JMP TAKOVR
```

\*\*\* Subroutines \*\*\*

```

1          .SBTTL *** Subroutines ***
2          .SBTTL ALCWRK -- Allocate a work buffer
3          ;-----
4          ; Allocate a 2048. byte work buffer over a resident portion of TSX.
5          ; This area will be restored from the TSX.SAV disk file after we
6          ; are finished using the work area.
7          ;
8          ; Outputs:
9          ;   WRKBUF = Address of base of work buffer.
10         ;   WRKSIZ = Size of work buffer (2048).
11         ;
12 007454 010246 ALCWRK: MOV      R2,-(SP)
13         ;
14         ; Get address of start of area where buffer can go and then bound
15         ; up to a block boundary.
16         ;
17 007456 012702 000000G      MOV      #EXCBUF,R2      ;Get address of base of buffer area
18 007462 062702 000777      ADD      #777,R2      ;Bound up to block boundary
19 007466 042702 000777      BIC      #777,R2      ;Set to block boundary
20 007472 010237 000152'     MOV      R2,WRKBUF
21 007476 012737 004000 000154'  MOV      #2048.,WRKSIZ
22         ;
23         ; Finished
24         ;
25 007504 012602      MOV      (SP)+,R2
26 007506 000207      RETURN

```

```

1          .SBTTL  ALCHRB -- Allocate Region Control Blocks for handlers
2          ;-----
3          ; This routine allocates and initializes empty Region Control Blocks for
4          ; use by device handlers.
5          ;
6          ; Inputs:
7          ;   R5 = Pointer to start of memory area where RCB's are to be built.
8          ;
9          ; Outputs:
10         ;   R5 = Pointer past end of RCB area.
11         ;
12 007510  010246  ALCHRB: MOV     R2, -(SP)
13         ;
14         ; Get count of # RCB's to build
15         ;
16 007512  013700  000000G      MOV     NDVRCB, R0      ; Get # RCB's to build for handlers
17         ;
18         ; Store pointer to start of RCB area and store -1 at beginning of area
19         ;
20 007516  010537  000000G      MOV     R5, HANRCB      ; Start of RCB area
21 007522  010537  000000G      MOV     R5, HANRCO      ; Store offset relative to MONVEC
22 007526  162737  000000G 000000G  SUB     #MONVEC, HANRCO ; Convert address to offset
23 007534  012725  177777      MOV     #-1, (R5)+      ; Store -1 at start of area
24         ;
25         ; Allocate and initialize to zero the RCB's
26         ;
27 007540  012702  000005      1$:    MOV     #5, R2      ; Each RCB has 5 words
28 007544  005025      2$:    CLR     (R5)+      ; Zero the RCB
29 007546  077202      SOB     R2, 2$
30         ;
31         ; See if there are more RCB's to build
32         ;
33 007550  005300      DEC     R0      ; More RCB's to initialize?
34 007552  003372      BGT     1$      ; Loop if yes
35         ;
36         ; Store -1 at end of RCB area
37         ;
38 007554  012725  177777      MOV     #-1, (R5)+      ; Mark end of RCB list
39         ;
40         ; Finished
41         ;
42 007560  012602      MOV     (SP)+, R2
43 007562  000207      RETURN

```

```

1          . IF      NE,<PROASM-1> ; If not assembling for Pro only
2          . SBTTL   LINCHK -- Check validity of T/S line
3          ;-----
4          ; LINCHK is called to check the validity of specified T/S line
5          ; vector and status register addresses.
6          ; If an uninstalled line is detected this routine aborts if
7          ; INIABT=1 or sets the $DEAD flag for the line if INIABT=0.
8          ;
9          LINCHK: MOV     R1,-(SP)
10         MOV     R2,-(SP)
11         MOV     R3,-(SP)
12         MOV     R4,-(SP)
13         MOV     @#4,-(SP) ; SAVE ORIGINAL TRAP VECTOR
14         ;
15         ; Take over trap control
16         ;
17         MOV     #6@,#4 ; CATCH TRAPS
18         ;
19         ; Loop through the test for each line.
20         ;
21         MOV     #LSTLIN,R1 ; NUMBER OF LAST LINE
22         ;
23         ; Determine if this is a primary line or an I/O line and set the
24         ; addresses of the interrupt service routines.
25         ;
26         1$: CALL    LINTYP ; Determine the type of this line
27         BIT     #$HARD,LSW3(R1) ; Is this line connected to hardware?
28         BEQ    31$ ; Br if not
29         MOV     LMXNUM(R1),R3 ; IS THIS A DL-11 OR MULTIPLEXER LINE?
30         BEQ    2$ ; BR IF DL-11
31         MOV     MXCSR(R3),R2 ; GET DZ11 OR DH11 STATUS REGISTER ADDRESS
32         BEQ    11$ ; BR IF ALREADY MARKED AS DEAD
33         MOV     MXVEC(R3),R4 ; GET MUX INTERRUPT VECTOR ADDRESS
34         BR     3$
35         11$: CALL   4$ ; MARK LINE AS DEAD
36         BR     31$ ; CONTINUE CHECKING TERMINALS
37         2$: MOV    RSR(R1),R2 ; GET DL-11 STATUS REGISTER ADDRESS
38         MOV    INVEC(R1),R4 ; GET DL-11 INTERRUPT VECTOR ADDRESS
39         ; Check validity of status register address.
40         3$: CMP    R2,#160000 ; IS IT IN I/O PAGE?
41         BLO    LINTRP ; ERROR IF NOT
42         BIT    #7,R2 ; IS IT ON 8-BYTE BOUNDARY?
43         BNE    LINTRP ; ERROR IF NOT
44         TST    @R2 ; TRY TO ACCESS IT AND SEE IF WE TRAP
45         ; Check validty of interrupt vector address.
46         CMP    R4,#60 ; CAN'T BE BELOW 60
47         BLO    BADVEC
48         CMP    R4,#500 ; OR ABOVE 500
49         BHS    BADVEC
50         BIT    #7,R4 ; MUST BE ON 8-BYTE BOUNDARY
51         BNE    BADVEC
52         ; This line looks good. Check next.
53         31$: SUB   #2,R1 ; MORE TO CHECK?
54         BGT    1$ ; BR IF YES
55         ;
56         ; Finished -- all lines look ok.
57         ;

```

ALCHRB -- Allocate Region Control Blocks for handlers

```

58          MOV      (SP)+, @#4          ; RESTORE TRAP VECTOR
59          MOV      (SP)+, R4
60          MOV      (SP)+, R3
61          MOV      (SP)+, R2
62          MOV      (SP)+, R1
63          RETURN
64          ;
65          ; See if we should abort or just mark the line as dead.
66          ;
67          4$:      TSTB     VINABT          ; DOES HE WANT TO ABORT?
68                  BNE     LINTRP          ; YES
69                  BIS     ##DEAD, LSW3(R1) ; MARK LINE AS DEAD
70                  TST     R3              ; IS THIS A DL11 OR A MUX LINE?
71                  BEQ     5$              ; BR IF DL11
72                  CLR     MXCSR(R3)       ; MARK DZ OR DH AS DEAD
73          5$:      RETURN
74          ;
75          ; Trap occured while trying to access status register.
76          ;
77          ;
78          6$:      CALL     4$              ; REPORT ERROR OR MARK AS DEAD LINE
79                  RTI                    ; RETURN TO LINE CHECKING
80          ;
81          ; Error: Invalid status register address.
82          ; R1 = Line number, R2 = status register address
83          ;
84          LINTRP: .PRINT  #TSXHD          ; PRINT ERROR MESSAGE
85                  .PRINT  #BADLIN
86                  BR      ERP
87          ;
88          ; Error: Invalid interrupt vector address.
89          ; R1 = Line number, R4 = interrupt vector address
90          ;
91          BADVEC: .PRINT  #TSXHD          ; PRINT ERROR MESSAGE
92                  .PRINT  #BDVMSG
93                  MOV     R4, R2          ; GET VECTOR ADDRESS TO R2
94          ERP:    MOV     R2, R0          ; GET ADDRESS TO R0
95                  CALL    PRTOCT         ; PRINT OCTAL VALUE
96                  .PRINT #CRLF
97                  .PRINT #BDLMSG        ; LINE # =
98                  MOV     R1, R0          ; GET LINE NUMBER
99                  ASR     R0              ; # 1
100                 CALL    PRTDEC         ; PRINT LINE NUMBER
101                 .PRINT #CRLF
102                 JMP     INISTP         ; ABORT INITIALIZATION
103                 .ENDC                ; NE, <PROASM-1>

```

OPNSWP -- Open system swap file

```

1          .SBTTL  OPNSWP  -- Open system swap file
2          ;-----
3          ; OPNSWP is called to open the TSX job swap file.
4          ; It also assigns swap file slots for each line.
5          ;
6          ; Inputs:
7          ; R5 = Address of base of free memory region
8          ;
9          ; Outputs:
10         ; SWPCHN = Set up for access to swap file.
11         ; LSWPBK(i) = Starting block number in swap file for swap area for line.
12         ;
13 007564 010146 OPNSWP: MOV     R1,-(SP)
14 007566 010246      MOV     R2,-(SP)
15 007570 010346      MOV     R3,-(SP)
16         ;
17         ; Load RT-11 handler for swap device.
18         ;
19 007572 013700 000000G      MOV     SWDBLK,R0      ;Get name of device
20 007576 004737 025176'     CALL    RTFTCH      ;Fetch the RT-11 handler
21 007602 103546      BCS     11$          ;Br if invalid device
22         ;
23         ; Compute the maximum number of slots in swap file that we could need
24         ;
25 007604 012703 000000G      MOV     #NSL+NDL,R3      ;Get # virtual lines and detached jobs
26 007610 012701 000000G      MOV     #LSTPL,R1      ;Get index to last primary line
27 007614 032761 000000G 000000G 1$: BIT     ##DEAD,LSW3(R1) ;Is this line installed?
28 007622 001001      BNE     5$          ;Br if not
29 007624 005203      INC     R3          ;Count another primary line
30 007626 162701 000002      SUB     #2,R1          ;Get next line index
31 007632 003370      BGT     1$          ;Loop if more lines to check
32 007634 020337 000000G      CMP     R3,VSWPSL      ;Compare with # slots specified
33 007640 002002      BGE     6$          ;Br if VSWPSL value is ok
34 007642 010337 000000G      MOV     R3,VSWPSL      ;Reduce number of slots in swap file
35         ;
36         ; Determine how many blocks are needed for each slot in swap file.
37         ;
38 007646 013703 000000G 6$:  MOV     VHIMEM,R3      ;GET # BLOCKS NEEDED FOR LARGEST JOB SIZE
39 007652 006303      ASL     R3
40 007654 063703 000000G      ADD     CXTPAG,R3      ;ADD # BLOCKS NEEDED FOR JOB CONTEXT AREA
41 007660 010337 000000G      MOV     R3,SLTSIZ      ;Save size of swap file slot
42         ;
43         ; Compute the total number of blocks needed for the swap file.
44         ;
45 007664 070337 000000G      MUL     VSWPSL,R3      ;Multiply by # slots in swap file
46         ;
47         ; R3 now contains total number of blocks needed in swap file.
48         ; See if swap file already exists on disk.
49         ;
50 007670 4$:  .LOOKUP #AREA,#1,#SWDBLK; DOES SWAP FILE EXIST NOW?
51 007710 103415      BCS     2$          ;BR IF NOT
52         ; Swap file exists. See if it is the right size.
53 007712 020003      CMP     R0,R3          ;IS SWAP FILE THE RIGHT SIZE?
54 007714 001453      BEQ     3$          ;BR IF YES
55         ; Old swap file is not of correct size.
56         ; Delete it and open a new swap file.
57 007716      .CLOSE #1

```

```

58 007724          .DELETE #AREA,#1,#SWDBLK;DELETE THE OLD SWAP FILE
59                ;
60                ; Create a new swap file.
61                ;
62 007744          2#: .ENTER #AREA,#1,#SWDBLK,R3 ;CREATE A NEW SWAP FILE
63 007770 103443   BCS      9#          ;BR IF SOME ERROR ON OPEN
64                ;
65                ; Swap file has been created.
66                ; Write to last block to reserve full space in file then close
67                ; and reopen the channel using a .lookup.
68                ;
69 007772 005303   DEC      R3          ;GET # OF LAST BLOCK IN FILE
70 007774          .WRITW #AREA,#1,#TSINIT,#256.,R3 ;WRITE TO LAST BLOCK IN FILE
71 010032 005203   INC      R3          ;GET BACK # BLOCKS IN FILE
72 010034          .CLOSE #1          ;CLOSE FILE WE CREATED
73 010042 000712   BR      4#          ;NOW GO REOPEN USING A .LOOKUP
74                ;
75                ; Swap file has been successfully opened using a .lookup.
76                ; Now copy channel status to TSX swap channel.
77                ;
78 010044 012700 000000G 3#:  MOV      #SWPCHN,R0      ;POINT TO SWAP CHANNEL BLOCK
79 010050 013702 000000G   MOV      SWDBLK,R2      ;GET DEVICE NAME
80 010054 004737 024616'   CALL     SETCHN        ;SET UP SWAP CHANNEL INFO
81                ;
82                ; Release the RT-11 device handler
83                ;
84 010060          .RELEAS #SWDBLK      ;Release RT-11 device handler
85                ;
86                ; Finished
87                ;
88 010070 012603   MOV      (SP)+,R3
89 010072 012602   MOV      (SP)+,R2
90 010074 012601   MOV      (SP)+,R1
91 010076 000207   RETURN
92                ;
93                ; Error: Cannot open swap file
94                ;
95 010100          9#: .PRINT #TSXHD      ;PRINT ERROR MESSAGE
96 010106          .PRINT #BADOPN
97 010114 004737 010142'   CALL     SPNEED        ;Print info about number of blocks needed
98                ;
99                ; Error: Invalid device specification.
100               ;
101 010120 010001   11#:  MOV      R0,R1      ;Save device name
102 010122          .PRINT #TSXHD      ;Print error message
103 010130          .PRINT #BADOPN
104 010136 004737 010170'   CALL     BADDEV        ;Print invalid device specification
105                ;
106                ; Error: Number of contiguous blocks required.
107                ;
108 010142          SPNEED: .PRINT #CONSPC ;Print contiguous blocks needed
109 010150 010300   MOV      R3,R0      ;GET # BLOCKS NEEDED FOR FILE
110 010152 004737 025436'   CALL     PRTDEC        ;DISPLAY # BLOCKS NEEDED
111 010156          .PRINT #CRLF
112 010164 000137 004250'   JMP      INISTP        ;ABORT INITIALIZATION
113                ;
114                ; Bad file specification.

```

```
115  
116 010170          ;  
117 010176 010100  BADDEV: .PRINT #CFHMSG      ;Print invalid device specification  
118 010200 004737 025502'  MOV      R1,R0      ;Get the rad50 device name  
119 010204          CALL    PRTR50      ;Print rad50 device name  
120 010212 000137 004250'  .PRINT #CRLF      ;Print carriage return/line feed  
                          JMP      INISTP      ;Abort initialization
```

```

1          .SBTTL  OPNRSF -- Open PLAS region swap file
2          ;-----
3          ; OPNRSF is called to open the swap file used for PLAS regions.
4          ;
5          ; Inputs:
6          ;   R5 = Address of base of free memory area.
7          ;
8          ; Outputs:
9          ;   SEGCHN = Set up to access swap file.
10         ;
11 010216 010346 OPNRSF: MOV      R3, -(SP)
12         ;
13         ; Return if this is a non-swapping system or if region swap file is
14         ; not wanted.
15         ;
16 010220 105737 000000G      TSTB   VSWPFL      ; Is this a non-swapping system?
17 010224 001513      BEQ     9$          ; Br if yes
18 010226 005737 000000G      TST    VPLAS       ; Is a PLAS swap file wanted?
19 010232 001510      BEQ     9$          ; Br if not
20         ;
21         ; Load RT-11 device handler for swap device
22         ;
23 010234 013700 000000G      MOV     RSFBLK, R0      ; Get name of device
24 010240 004737 025176'      CALL   RTFTCH       ; Try to fetch the RT-11 device handler
25 010244 103515      BCS     11$         ; Br if error on handler fetch
26 010246 013703 000000G      MOV     VPLAS, R3      ; Get # blocks in PLAS swap file
27         ;
28         ; See if PLAS swap file already exists on disk
29         ;
30 010252 4$: .LOOKUP #AREA, #1, #RSFBLK ; Try to find existing PLAS swap file
31 010272 103416      BCS     2$          ; Br if file does not now exist
32         ;
33         ; PLAS swap file exists.
34         ; See if it is the correct size.
35         ;
36 010274 020037 000000G      CMP     R0, VPLAS     ; Is swap file of the correct size?
37 010300 001453      BEQ     3$          ; Br if yes
38         ;
39         ; Old PLAS swap file is not of correct size.
40         ; Delete it and open a new swap file.
41         ;
42 010302      .CLOSE  #1          ; Close and delete the old file
43 010310      .DELETE #AREA, #1, #RSFBLK; Delete the old file
44         ;
45         ; Create new swap file
46         ;
47 010330 2$: .ENTER  #AREA, #1, #RSFBLK, VPLAS ; Create a new PLAS swap file
48 010356 103440      BCS     10$         ; Br if cannot create new file
49         ;
50         ; New swap file has been created.
51         ; Write to last block to reserve full file size
52         ; and then close and reopen with a lookup.
53         ;
54 010360 005303      DEC     R3          ; Get # of last block in file
55 010362      .WRITW #AREA, #1, #TSINIT, #256., R3
56 010420      .CLOSE  #1
57 010426 000711      BR      4$          ; Go back and lookup file

```

```
58 ;  
59 ; Swap file has been successfully opened using lookup.  
60 ; Copy channel status to TSX channel block.  
61 ;  
62 010430 012700 0000000 3#: MOV #SEGCHN,R0 ;Point to TSX PLAS swap channel  
63 010434 013703 0000000 MOV RSFBLK,R3 ;Get device name  
64 010440 004737 024616' CALL SETCHN ;Set up TSX channel block  
65 ;  
66 ; Release the RT-11 device handler  
67 ;  
68 010444 .RELEAS #RSFBLK ;Release RT-11 device handler  
69 ;  
70 ; Finished  
71 ;  
72 010454 012603 9#: MOV (SP)+,R3  
73 010456 000207 RETURN  
74 ;  
75 ; Error -- Cannot open PLAS swap file  
76 ;  
77 010460 10#: .PRINT #TSXHD ;Print error prefix  
78 010466 .PRINT #RSFERR ;Print error message  
79 010474 004737 010142' CALL SPNEED ;Print information about amt of space needed  
80 ;  
81 ; Error: Invalid device specification.  
82 ;  
83 010500 010001 11#: MOV R0,R1 ;Save device name  
84 010502 .PRINT #TSXHD ;Print error message  
85 010510 .PRINT #RSFERR  
86 010516 004737 010170' CALL BADDEV ;Print invalid device specification
```

SPLINI -- Initialize spooling system

```

1          .SBTTL  SPLINI -- Initialize spooling system
2          ;-----
3          ; SPLINI performs the initialization of the spooling system.
4          ; Inputs:
5          ; R5 = Current base of free memory area.
6          ;
7 010522 005727 000000G SPLINI: TST      #SPLND      ; Are there any spooled devices?
8 010526 001401          BEQ      13$          ; Br if not
9 010530 000401          BR       10$          ; Initialize the spooled devices
10 010532 000207          13$:   RETURN
11          ;
12          ; There are some spooled devices
13          ;
14 010534 010146          10$:   MOV      R1,-(SP)
15 010536 010246          MOV      R2,-(SP)
16 010540 010346          MOV      R3,-(SP)
17 010542 010446          MOV      R4,-(SP)
18 010544 010546          MOV      R5,-(SP)
19          ;
20          ; Open each spooled device
21          ;
22 010546 105037 000000G          CLRB     NSPLDV      ; INIT COUNT OF # ACTUAL SPOOLED DEVICES
23 010552 012701 000000G          MOV      #SDCB,R1      ; POINT TO 1ST SDCB
24 010556 012703 000000G          MOV      #SPLDEV,R3     ; POINT TO TABLE OF RAD50 DEV NAMES
25 010562 012704 000000G          MOV      #SPLANM,R4    ; POINT TO TABLE OF ASCII DEV NAMES
26 010566 004737 011574'          2$:   CALL     FORCEO      ; FORCE UNIDENTIFIED UNIT #S TO 0
27 010572 011302          MOV      (R3),R2      ; GET RAD50 NAME OF SPOOLED DEVICE
28 010574 010261 000000G          MOV      R2,SDNAME(R1) ; SET NAME IN SDCB
29 010600 010100          MOV      R1,RO        ; GET ADDRESS OF SDCB
30 010602 062700 000000G          ADD      #SDANAM,RO    ; POINT TO CELL FOR ASCII NAME
31 010606 112420          MOVB    (R4)+,(RO)+    ; MOVE IN ASCII DEVICE NAME
32 010610 112420          MOVB    (R4)+,(RO)+
33 010612 112420          MOVB    (R4)+,(RO)+
34 010614 020227 000000G          CMP      R2,#DMYDEV    ; Is this a dummy entry for later patching?
35 010620 001451          BEQ      1$          ; Br if yes -- Ignore it
36 010622 010200          MOV      R2,RO        ; Get name to RO
37 010624 004737 011476'          CALL     CVTDVU        ; Convert name to device # and unit #
38 010630 010061 000000G          MOV      RO,SDDVU(R1)  ; Store device # and unit # in SDCB
39 010634 010200          MOV      R2,RO        ; Get device name
40 010636 004737 011372'          CALL     CHKCLD        ; See if this is a CL device?
41 010642 103406          BCS     14$          ; Br if not
42 010644 004737 011306'          CALL     SPLCLD        ; Set up for spooling to CL device
43 010650 103414          BCS     3$          ; Br if invalid unit
44 010652 105237 000000G          INCB    NSPLDV        ; Count # of actual spooled devices
45 010656 000432          BR      1$          ; Process next device
46 010660 010100          14$:   MOV      R1,RO        ; Get address of SDCB
47 010662 062700 000000G          ADD      #SDCHAN,RO    ; Point to channel block within SDCB
48 010666 004737 024530'          CALL     OPNCHN        ; Set TSX-Plus channel block open to device
49 010672 103403          BCS     3$          ; Br if did not recognize device
50 010674 105237 000000G          INCB    NSPLDV        ; Count # actual spooled devices
51 010700 000421          BR      1$          ; GO PROCESS NEXT DEVICE
52          ;
53          ; Error on opening spooled device
54          ; Determine if we should print an error message or simply
55          ; mark the spooled device as unavailable.
56          ;
57 010702 012761 000000G 000000G 3$:   MOV      #DMYDEV,SDNAME(R1); SAY THIS DEVICE IS NOT SPOOLED

```

SPLINI -- Initialize spooling system

```

58 010710 105737 000000G          TSTB   VINABT          ;ABORT OR CONTINUE ON ERRORS?
59 010714 001413                   BEQ    1$              ;BR TO IGNORE DEVICE AND CONTINUE INIT
60 010716                   .PRINT #TSXHD          ;PRINT ERROR MESSAGE
61 010724                   .PRINT #BDSPOP         ;
62 010732 010200                   MOV    R2,R0          ;GET RAD50 DEVICE NAME
63 010734 004737 025502'          CALL   PRTR50         ;PRINT DEVICE NAME
64 010740 000137 004250'          JMP    INISTP         ;ABORT INITIALIZATION
65                               ;
66                               ; Process next spooled device
67                               ;
68 010744 062701 000000G          1$:   ADD    #SDCBSZ,R1  ;POINT TO NEXT SDCB
69 010750 005723                   TST    (R3)+          ;POINT TO NEXT DEVICE NAME
70 010752 020327 000000G          CMP    R3,#SPLDVN    ;OPENED ALL SPOOLED DEVICES?
71 010756 103703                   BLO   2$              ;BR IF MORE TO DO
72                               ;
73                               ; Open the spool file
74                               ;
75 010760 105737 000000G          TSTB   NSPLDV         ;ARE THERE ANY ACTUAL SPOOLED DEVICES?
76 010764 001521                   BEQ    12$            ;BR IF THERE ARE NO ACTUAL SPOOLED DEVICES
77 010766 013700 000000G          MOV    SPLBLK,R0     ;Get name of device for spool file
78 010772 004737 025176'          CALL   RTFTCH        ;Fetch the RT-11 device handler
79 010776 103532                   BCS   11$            ;Br if cannot fetch handler
80 011000 013702 000000G          MOV    NSPLBL,R2     ;GET # BLOCKS TO ALLOCATE FOR FILE
81 011004 005202                   INC    R2             ;Add 1 extra block
82                               ;
83                               ; See if spool file already exists
84                               ;
85 011006                   5$:   .LOOKUP #AREA,#1,#SPLBLK;SEE IF SPOOL FILE ALREADY EXISTS
86 011026 103415                   BCS   6$              ;BR IF IT DOES NOT EXIST
87 011030 020002                   CMP    R0,R2         ;IS IT THE RIGHT SIZE?
88 011032 001453                   BEQ   7$              ;BR IF YES
89 011034                   .CLOSE #1             ;IT EXISTS BUT IS OF WRONG SIZE
90 011042                   .DELETE #AREA,#1,#SPLBLK;DELETE CURRENT FILE AND OPEN NEW ONE
91                               ;
92                               ; Open new spool file
93                               ;
94 011062                   6$:   .ENTER #AREA,#1,#SPLBLK,R2;CREATE A NEW SPOOL FILE
95 011106 103456                   BCS   8$              ;BR IF ERROR ON ENTER
96                               ; Write to last block in file to reserve full file space
97 011110 010203                   MOV    R2,R3         ;Get # of blocks in file
98 011112 005303                   DEC    R3            ;Get # of last block in file
99 011114                   .WRITW #AREA,#1,#TSINIT,#256.,R3
100                              ; Now close and reopen using a lookup
101 011152                   .CLOSE #1            ;CLOSE SPOOL FILE
102 011160 000712                   BR    5$             ;GO BACK AND REOPEN USING LOOKUP
103                              ;
104                              ; Spool file has been successfully opened with a lookup.
105                              ; Save the channel status.
106                              ;
107 011162 012700 000000G          7$:   MOV    #SPLCHN,R0  ;SAVE CHANNEL STATUS HERE
108 011166 013702 000000G          MOV    SPLBLK,R2     ;GET DEVICE NAME
109 011172 004737 024616'          CALL   SETCHN        ;SAVE CHANNEL STATUS
110 011176                   .RELEASE #SPLBLK     ;Release the RT-11 device handler
111                              ;
112                              ; Set number of free public blocks in spool file
113                              ;
114 011206 113703 000000G          MOVB   NSPLDV,R3     ;Get # spooled devices

```

SPLINI -- Initialize spooling system

```

115 011212 070327 0000000      MUL      #PVSPBL,R3      ;Times number of private blocks per dev
116 011216 005403              NEG      R3
117 011220 063703 0000000      ADD      NSPLBL,R3      ;Get # public spool blocks
118 011224 010337 0000000      MOV      R3,NFRESB      ;This is number of public free spool blocks
119                               ;
120                               ; Finished
121                               ;
122 011230 012605      12$:    MOV      (SP)+,R5
123 011232 012604              MOV      (SP)+,R4
124 011234 012603              MOV      (SP)+,R3
125 011236 012602              MOV      (SP)+,R2
126 011240 012601              MOV      (SP)+,R1
127 011242 000207      9$:    RETURN
128                               ;
129                               ; Error: Cannot open spool file.
130                               ;
131 011244              8$:    .PRINT  #TSXHD      ;PRINT ERROR MESSAGE
132 011252              .PRINT  #BOSF
133 011260 000137 004250'      JMP      INISTP          ;ABORT INITIALIZATION
134                               ;
135                               ; Error: Invalid device specification.
136                               ;
137 011264 010001      11$:    MOV      R0,R1      ;Save device name
138 011266              .PRINT  #TSXHD      ;Print error message
139 011274              .PRINT  #BOSF
140 011302 004737 010170'      CALL    BADDEV          ;Print invalid device specification

```

SPLCLD -- Set up spooling to a CL device

```

1          .SBTTL  SPLCLD -- Set up spooling to a CL device
2          ;-----
3          ; SPLCLD is called to set up a spool device control block when
4          ; spooling is being directed to a Communication Line (CL) device.
5          ;
6          ; Inputs:
7          ;   RO = CL unit number
8          ;   R1 = Address of SDCB
9          ;
10         ; Outputs:
11         ;   C-flag set ==> Invalid CL unit
12         ;
13 011306 010546 SPLCLD: MOV      R5, -(SP)
14         ;
15         ; Make sure CL unit number is valie
16         ;
17 011310 010005         MOV      RO, R5          ; Get CL unit number
18 011312 020527 0000006 CMP      R5, #CLTOTL      ; Is this a valid unit #
19 011316 103022         BHIS     B#,          ; Br if invalid
20         ;
21         ; Set up channel control block in SDCB
22         ;
23 011320 005061 000000C CLR      SDCHAN+C. SBLK(R1); Starting block # = 0
24 011324 020527 0000007 CMP      R5, #7          ; Is this a CL or C1 unit?
25 011330 101405         BLOS     1#,          ; Br if CL unit
26 011332 162705 000010 SUB      #B., R5          ; Remove C1 unit # bias
27 011336 013700 0000006 MOV      C1DEVX, RO       ; Get C1 device index number
28 011342 000402         BR       2#,          ;
29 011344 013700 0000006 1#: MOV      CLDEVX, RO       ; Get CL device index number
30 011350 010061 000000C 2#: MOV      RO, SDCHAN+C. CSW(R1); Set device index number
31 011354 110561 000000C         MOVB   R5, SDCHAN+C. DEVQ(R1); Set unit #
32         ;
33         ; We successfully set up a CL unit
34         ;
35 011360 000241         CLC          ; Signal success on error
36 011362 000401         BR       9#,          ;
37         ;
38         ; We cannot open this CL unit
39         ;
40 011364 000261 8#: SEC          ; Signal error
41         ;
42         ; Finished
43         ;
44 011366 012605 9#: MOV      (SP)+, R5
45 011370 000207         RETURN

```

```

1          .SBTTL  CHKCLD -- See if a device name is a CL or C1 unit
2          ;-----
3          ; Determine if a rad50 device and unit name is a CL or C1 device.
4          ;
5          ; Inputs:
6          ;   RO = Rad50 device spec
7          ;
8          ; Outputs:
9          ;   C-flag set      ==> Not a CL or C1 unit
10         ;   C-flag cleared ==> This is a CL or C1 unit
11         ;   RO = CL unit number (0-15)
12         ;
13 011372  CHKCLD:
14         ;
15         ; See if this is a CL unit
16         ;
17 011372  020037  000202'      CMP      RO,R50CL      ; Is name "CL"?
18 011376  001411              BEQ      1$                ; Br if yes
19 011400  020037  000204'      CMP      RO,R50CLO     ; Is name in the range CLO to CL7?
20 011404  103432              BLO      8$                ; Br if not
21 011406  020037  000206'      CMP      RO,R50CL7
22 011412  101005              BHI      2$
23 011414  163700  000204'      SUB      R50CLO,RO     ; Get CL unit number
24 011420  000422              BR       7$
25 011422  005000      1$:    CLR      RO                ; CL = CLO
26 011424  000420              BR       7$
27         ;
28         ; See if this is a C1 unit
29         ;
30 011426  020037  000210'      2$:    CMP      RO,R50C1      ; Is name "C1"?
31 011432  001413              BEQ      3$                ; Br if yes
32 011434  020037  000212'      CMP      RO,R50C10     ; Is name in the range C10 to C17?
33 011440  103414              BLO      8$                ; Br if not
34 011442  020037  000214'      CMP      RO,R50C17
35 011446  101011              BHI      8$
36 011450  163700  000212'      SUB      R50C10,RO     ; Get unit number
37 011454  062700  000010      ADD      #8.,RO        ; Bias by 8 for C1 units
38 011460  000402              BR       7$
39 011462  012700  000010      3$:    MOV      #8.,RO        ; C1 = CLB
40         ;
41         ; This is a CL or C1 unit
42         ;
43 011466  000241      7$:    CLC
44 011470  000401              BR       9$                ; Signal success on return
45         ;
46         ; This is not a CL or C1 unit
47         ;
48 011472  000261      8$:    SEC
49         ;                               ; Signal failure on return
50         ; Finished
51         ;
52 011474  000207      9$:    RETURN

```

CVTDVU -- Convert device name to dev index and unit #

```

1          .SBTTL  CVTDVU -- Convert device name to dev index and unit #
2          ;-----
3          ; CVTDVU is called to convert a RAD50 device name into the corresponding
4          ; device index number and unit number.
5          ;
6          ; Inputs:
7          ;   RO = RAD50 device name.
8          ;
9          ; Outputs:
10         ;   C-flag cleared ==> Conversion successful.
11         ;   C-flag set    ==> Unable to find device name in tables.
12         ;   RO = Device index number (low byte), device unit number (high byte).
13         ;
14 011476 010246 CVTDVU: MOV     R2,-(SP)
15 011500 010346         MOV     R3,-(SP)
16         ;
17         ; Split the unit number off of the full device name
18         ;
19 011502 010003         MOV     R0,R3           ;Get full device name
20 011504 005002         CLR     R2           ;Set up for divide
21 011506 071227 000050  DIV     #50,R2       ;Split name and unit (R0=name, R1=unit)
22 011512 005703         TST     R3           ;Was a unit number specified?
23 011514 001402         BEQ     1$           ;Br if not
24 011516 162703 000036  SUB     #36,R3       ;Convert unit number to binary value
25 011522 010300 1$:    MOV     R3,R0           ;Get unit number
26 011524 000300         SWAB    R0           ;Position to high-order byte
27         ;
28         ; Look up the device name to get the device index
29         ;
30 011526 070227 000050  MUL     #50,R2       ;Now get the device name without unit number
31 011532 013702 000000G  MOV     NUMDEV,R2    ;Get index number of last device
32 011536 020362 000000G  2$:    CMP     R3,PNAME(R2) ;Search for device in name table
33 011542 001407         BEQ     3$           ;Br if found it
34 011544 162702 000002  SUB     #2,R2       ;Try next device
35 011550 002372         BGE     2$           ;Loop if more to check
36         ;
37         ; Error, cannot find device name in tables
38         ;
39 011552 012700 177777  MOV     #-1,R0       ;Set device # = unit # = -1
40 011556 000261         SEC           ;Signal error on return
41 011560 000402         BR     9$
42         ;
43         ; Found the device in the tables
44         ;
45 011562 050200 3$:    BIS     R2,R0           ;Combine device # and unit #
46 011564 000241         CLC           ;Signal success on return
47         ;
48         ; Finished
49         ;
50 011566 012603 9$:    MOV     (SP)+,R3
51 011570 012602         MOV     (SP)+,R2
52 011572 000207         RETURN

```

FORCE0 -- Force a 2-char dev name to unit 0

```

1          .SBTTL  FORCE0 -- Force a 2-char dev name to unit 0
2          ;-----
3          ; Inputs: R3 points to a RAD50 device name
4          ;
5          ; Outputs: If the 3rd char of the device name pointed to by R3 is
6          ;          blank, then it is changed to 0
7          ;
8 011574 010346  FORCE0: MOV      R3,-(SP)
9 011576 010446          MOV      R4,-(SP)
10 011600 010546          MOV      R5,-(SP)
11 011602 011305          MOV      (R3),R5      ; MOVE CURRENT DEV NAME TO R5
12 011604 005004          CLR      R4          ; SET UP FOR DIVIDE
13 011606 071427 000050  DIV      #50,R4      ; SEPARATE INTO NAME AND UNIT
14 011612 005705          TST      R5          ; WAS 3RD CHAR BLANK?
15 011614 001012          BNE     9$          ; RETURN IF NOT
16 011616 010405          MOV      R4,R5      ; GET HIGH 2 CHARS
17 011620 005004          CLR      R4          ; SET UP FOR ANOTHER DIVIDE
18 011622 071427 000050  DIV      #50,R4      ; SEPARATE 1 & 2 CHARS
19 011626 005704          TST      R4          ; WAS CHAR 1 BLANK?
20 011630 001404          BEQ     9$          ; EMPTY OR INVALID DEV NAME!
21 011632 005705          TST      R5          ; WAS CHAR 2 BLANK?
22 011634 001402          BEQ     9$          ; 1-CHAR DEV NAME SHOULD BE INVALID???
23 011636 062713 000036  ADD      #^R 0,(R3)   ; FORCE TO UNIT 0
24 011642 012605          9$:  MOV      (SP)+,R5
25 011644 012604          MOV      (SP)+,R4
26 011646 012603          MOV      (SP)+,R3
27 011650 000207          RETURN

```

```

1          .SBTTL  ALOCBF -- Allocate buffer space
2          ;-----
3          ; ALOCBF is called to allocate space for buffers.  The allocated space
4          ; is not initialized but simply reserved.
5          ;
6          ; Inputs:
7          ; R5 = Start of area to allocate buffer space in.
8          ;
9          ; Outputs:
10         ; R5 = Address beyond end of buffer area.
11         ; CHNBAS = Address of base of I/O channel space.
12         ; CHNEND = Address past end of I/O channel space.
13         ;
14 011652 010146 ALOCBF: MOV      R1,-(SP)
15         ;
16         ; Assign space for I/O queue elements.
17         ;
18 011654 010537 000000G      MOV      R5,FREIDQ      ; START OF I/O QUEUE SPACE
19 011660 062705 000000C      ADD      #IOQSIZ*NUMIOQ,R5; RESERVE SPACE FOR I/O QUEUE ELEMENTS
20         ;
21         ; Assign space for shared PLAS region control blocks
22         ;
23 011664 010537 000000G      MOV      R5,SHRRCB      ; Start of area for RCB's
24 011670 013701 000000G      MOV      VNGR,R1        ; Get number of RCB's wanted
25 011674 020137 000000G      CMP      R1,VMXWIN      ; Must have one for each display window
26 011700 103002                BHS     13$             ; Br if ok
27 011702 013701 000000G      MOV      VMXWIN,R1      ; Force one for each window
28 011706 070127 000000G  13$:   MUL      #RC##SZ,R1    ; Multiply by size of each block
29 011712 060105                ADD      R1,R5          ; Allocate space for RCB's
30 011714 010537 000000G      MOV      R5,SHRRCN      ; Address of end of region
31         ;
32         ; Assign space for fork blocks
33         ;
34 011720 012700 000000C      MOV      #<NUMFRK-FRKGEN>,R0 ; Get # fork blocks to allocate
35 011724 003404                BLE     11$             ; Br if none to allocate
36 011726 010537 000000G      MOV      R5,FRKINI      ; Set pointer to start of area
37 011732 062705 000000C      ADD      #<<NUMFRK-FRKGEN>*FQ##SZ>,R5 ; Reserve space for fork blocks
38         ;
39         ; Assign space for job monitoring control blocks
40         ;
41 011736 013701 000000G  11$:   MOV      VMXMON,R1    ; Any job monitoring blocks wanted?
42 011742 001405                BEQ     10$             ; Br if not
43 011744 010537 000000G      MOV      R5,MONFQH      ; Start of job monitoring control blocks
44 011750 070127 000000G      MUL      #JM##SZ,R1    ; Compute space needed for control blocks
45 011754 060105                ADD      R1,R5          ; Allocate the space
46         ;
47         ; Allocate space for system message buffers.
48         ;
49 011756 010537 000000G  10$:   MOV      R5,SNMSHD      ; HEAD OF SYSTEM MESSAGE BUFFER AREA
50 011762 062705 000000C      ADD      #<NMSNMB*SB##SZ>,R5; RESERVE ROOM FOR MESSAGE BUFFERS
51         ;
52         ; Allocate space for INSTALLED program table
53         ;
54 011766 010537 000000G      MOV      R5,INSTBL      ; Base of table
55 011772 013701 000000G      MOV      VNUIP,R1       ; # slots for user installed programs
56 011776 062701 000000G      ADD      #NSIP,R1       ; Add # slots for system programs
57 012002 070127 000000G      MUL      #II##SZ,R1    ; Multiply by size of each slot

```

ALOCBF -- Allocate buffer space

```

58 012006 060105          ADD    R1,R5          ;Allocate space for table
59 012010 010537 000000G  MOV    R5,INSTBN       ;Pointer past end of INSTALL table
60                                     ;
61                                     ; Allocate space for device mount entries
62                                     ;
63 012014 010537 000000G  MOV    R5,CSHDEV       ;Point to start of area
64 012020 012701 000000G  MOV    #CD##SZ,R1      ;Get size of each entry
65 012024 070137 000000G  MUL    VMXCSH,R1       ;Multiply by number of entries
66 012030 060105          ADD    R1,R5          ;Reserve space for table
67 012032 010537 000000G  MOV    R5,CSHDVN       ;Save pointer past end of table
68                                     ;
69                                     ; Allocate space for data cache control blocks
70                                     ;
71 012036 005737 000000G  TST    CSHALC          ;Is data caching wanted?
72 012042 001404          BEQ    12$              ;Br if not
73 012044 010537 000000G  MOV    R5,CCBHD        ;Head of free list area
74 012050 062705 000000C  ADD    #NUMCCB*CC##SZ,R5 ;Allocate space for control blocks
75                                     ;
76                                     ; Allocate space for spool file control blocks
77                                     ;
78 012054 013701 000000G  12$:  MOV    NSPLFL,R1    ;Get # spool file control blocks needed
79 012060 001407          BEQ    1$              ;Br if none needed
80 012062 070127 000000G  MUL    #SFCBSZ,R1      ;Compute space needed by control blocks
81 012066 010537 000000G  MOV    R5,SFCB         ;Base of control block area
82 012072 060105          ADD    R1,R5          ;Allocate space for control blocks
83 012074 010537 000000G  MOV    R5,SFCBND       ;End of control block area
84                                     ;
85                                     ; Allocate space for a 16 byte vector for each multiplexer.
86                                     ; This vector is used to map from the mux line number to the
87                                     ; TSX-Plus logical line number.
88                                     ;
89 012100 012701 000002   1$:  MOV    #2,R1          ;START WITH FIRST MUX
90 012104 020127 000000G  2$:  CMP    R1,#LSTMX     ;HAVE WE DONE ALL MUX'S?
91 012110 101007          BHI    5$              ;BR IF YES
92 012112 010561 000000G  MOV    R5,MXLNT(R1)    ;SET ADDRESS OF START OF VECTOR
93 012116 062705 000020   ADD    #16.,R5         ;RESERVE SPACE FOR VECTOR
94 012122 062701 000002   ADD    #2,R1          ;ADVANCE TO NEXT MUX
95 012126 000766          BR     2$              ;
96                                     ;
97                                     ; Allocate buffers to hold characters for DMA transfers to DH11 multiplexers
98                                     ;
99 012130 012701 000002   5$:  MOV    #2,R1          ;Start with first line
100 012134 020127 000000G  6$:  CMP    R1,#LSTPL     ;Is this a primary time-sharing line?
101 012140 101403          BLOS   3$              ;Br if yes
102 012142 020127 000000G  CMP    R1,#FSTIOL      ;Is this a CL line?
103 012146 103422          BLO    7$              ;Br if not
104 012150 026127 000000G 000000G  3$:  CMP    LCDTYP(R1),#CDX$DH ;Is this line connected to a DH11?
105 012156 001404          BEQ    8$              ;Br if yes
106 012160 026127 000000G 000000G  CMP    LCDTYP(R1),#CDX$VH ;Is this line connected to a DHV11?
107 012166 001012          BNE    7$              ;Br if not
108 012170 010561 000000G  8$:  MOV    R5,LDHB1B(R1)   ;Set address of start of buffer 1
109 012174 010561 000000G  MOV    R5,LDHB1P(R1)   ;Initialize pointer into buffer 1
110 012200 062705 000000G  ADD    #DHBFSZ,R5      ;Reserve space for buffer
111 012204 010561 000000G  MOV    R5,LDHB2B(R1)   ;Set address of start of buffer 2
112 012210 062705 000000G  ADD    #DHBFSZ,R5      ;Reserve space for buffer
113 012214 062701 000002   7$:  ADD    #2,R1          ;Get # of next line
114 012220 020127 000000G  CMP    R1,#LSTHL       ;Have we checked all lines?

```

ALOCBF -- Allocate buffer space

```

115 012224 101743          BLOS  6#           ;Br if not
116 012226 005205          INC   R5           ;Bound up to next word
117 012230 042705 000001   BIC   #1,R5
118
119           ; Allocate space for tables that keep track of space in swap file
120
121 012234 105737 000000G   TSTB  VSWPFL       ;Is this a swapping system?
122 012240 001415          BEQ   14#          ;Br if not
123 012242 013700 000000G   MOV   VSWPSL,R0   ;Get # slots in swap file
124 012246 006300          ASL   R0           ;Allocate 2 bytes per slot
125 012250 010537 000000G   MOV   R5,SWPPOS   ;Start of table with starting block #'s
126 012254 060005          ADD   R0,R5       ;Allocate space
127 012256 010537 000000G   MOV   R5,SWPJOB   ;Start of table with job #'s
128 012262 060005          ADD   R0,R5       ;Allocate space
129 012264 010537 000000G   MOV   R5,SCPFHD   ;Pointer to area with command packets
130 012270 062705 000000C   ADD   #NSCP*SP##SZ,R5 ;Allocate space for swap command packets
131
132           ; Allocate a 512-byte buffer to use to access job context blocks
133
134 012274 010537 000000G   14#: MOV   R5,CXTBUF ;Set address of buffer
135 012300 062705 001400   ADD   #1400,R5    ;Reserve space for the buffer
136
137           ; Make sure TSX is not too big.
138
139 012304 010500          MOV   R5,R0       ;GET CURRENT MEMORY ADDRESS
140 012306 004737 025312'   CALL  CHKMEM      ;CHECK FOR SPACE OVERFLOW
141
142           ; Finished
143
144 012312 012601          MOV   (SP)+,R1
145 012314 000207          RETURN

```

ALCSLO -- Allocate silo buffers for lines

```

1          .SBTTL  ALCSLO -- Allocate silo buffers for lines
2          ;-----
3          ; Allocate the silo buffers that are used to hold characters as they
4          ; are received from serial lines.
5          ;
6          ; Inputs:
7          ;   R5 = Current pointer to start of free memory.
8          ;
9          ; Outputs:
10         ;   R5 = New pointer to start of free memory.
11         ;
12 012316 010146 ALCSLO: MOV     R1,-(SP)
13 012320 010246         MOV     R2,-(SP)
14         ;
15         ; Begin loop to check each line
16         ;
17 012322 012701 000000G         MOV     #LSTHL,R1         ;Get index to last hardware line
18         ;
19         ; Only allocate silo buffers for real lines
20         ;
21 012326 012702 000040 1#:     MOV     #32.,R2         ;Set minimum size for time-sharing lines
22 012332 020127 000000G         CMP     R1,#LSTPL         ;Is this a primary line?
23 012336 101405         BLOS    2$         ;Br if yes
24 012340 020127 000000G         CMP     R1,#FSTIOL        ;Is this a CL line?
25 012344 103463         BLO    9$         ;Br if not
26 012346 012702 000020         MOV     #16.,R2         ;Set minimum size for CL lines
27         ;
28         ; Determine how much space to allocate
29         ;
30 012352 016100 000000G 2#:     MOV     LHIRBA(R1),R0        ;Get requested size
31 012356 001002         BNE     8$         ;Br if a size was specified
32 012360 013700 000000G         MOV     VNCSLO,R0         ;Use default value
33 012364 020027 000000G 8#:     CMP     R0,#MAXSLO        ;Constrain size to 255 bytes
34 012370 101402         BLOS    3$         ;Br if ok
35 012372 012700 000000G         MOV     #MAXSLO,R0        ;Reduce size
36 012376 020002 3#:     CMP     R0,R2         ;Compare with acceptable minimum
37 012400 103001         BHS    4$         ;Br if ok
38 012402 010200         MOV     R2,R0         ;Use minimum size allowed
39 012404 010061 000000G 4#:     MOV     R0,LHIRBA(R1)        ;Set # bytes to be allocated for silo
40         ;
41         ; Allocate the space
42         ;
43 012410 010561 000000G         MOV     R5,LHIRBB(R1)        ;Set base address of buffer
44 012414 010561 000000G         MOV     R5,LHIRBP(R1)        ;Init pointer where to store next char
45 012420 010561 000000G         MOV     R5,LHIRBG(R1)        ;Init pointer where to get next char
46 012424 010061 000000G         MOV     R0,LHIRBS(R1)        ;Set # free bytes in buffer
47 012430 060005         ADD     R0,R5         ;Allocate space for buffer
48 012432 010561 000000G         MOV     R5,LHIRBE(R1)        ;Set address beyond end of buffer
49         ;
50         ; Set up control information about when to send XON and XOFF
51         ;
52 012436 006200         ASR     R0         ;Get 1/2 of buffer size
53 012440 162700 000002         SUB     #2,R0         ;Minus two characters
54 012444 116102 000000G         MOVB   LHIRBC(R1),R2        ;Get specified size for XOFF point
55 012450 001002         BNE     5$         ;Br if value specified
56 012452 113702 000000G         MOVB   VNCXOF,R2         ;Try default
57 012456 020200 5#:     CMP     R2,R0         ;Is specified value ok?

```

ALCSLO -- Allocate silo buffers for lines

```

58 012460 101401          BLOS 6$          ;Br if yes
59 012462 010002          MOV  R0,R2          ;No, use size/2-2
60 012464 110261 000000G  6$:  MOVB R2,LHIRBC(R1) ;Set # of chars when XOFF sent
61 012470 116102 000001G  MOVB LHIRBC+1(R1),R2 ;Get specified size for XON point
62 012474 001002          BNE  7$          ;Br if a value was specified
63 012476 113702 000000G  MOVB VNCXON,R2      ;Try default
64 012502 020200          7$:  CMP  R2,R0          ;Is specified value ok?
65 012504 101401          BLOS 10$         ;Br if ok
66 012506 010002          MOV  R0,R2          ;No, use size/2-2
67 012510 110261 000001G  10$: MOVB R2,LHIRBC+1(R1) ;Set # of chars when XON sent
68
69          ; Do the next line
70
71 012514 162701 000002  9$:  SUB  #2,R1          ;Get next line index number
72 012520 003302          BGT  1$          ;Loop if more to do
73
74          ; Finished
75
76 012522 005205          INC  R5          ;Force R5 to be even
77 012524 042705 000001  BIC  #1,R5
78 012530 012602          MOV  (SP)+,R2
79 012532 012601          MOV  (SP)+,R1
80 012534 000207          RETURN

```

ALBFX -- Allocate buffers in extended memory region

```

1          .SBTTL  ALBFX  -- Allocate buffers in extended memory region
2          ;-----
3          ; ALBFX is called to allocate space for buffers that are not constrained
4          ; to fit in the 40Kb region that TSX-Plus occupies.
5          ;
6          ; Inputs:
7          ; R5 = 64-Byte address of base of free memory region.
8          ;
9          ; Outputs:
10         ; R5 = Address above top of buffers allocated.
11         ;
12 012536 010146 ALBFX:  MOV     R1,-(SP)
13 012540 010246      MOV     R2,-(SP)
14 012542 010346      MOV     R3,-(SP)
15         ;
16         ; Allocate character buffers for all lines
17         ; Note: Character buffer space will be accessed by mapping through PAR 6.
18         ;
19 012544 012701 000002      MOV     #2,R1          ;GET 1ST JOB INDEX NUMBER
20 012550 032761 000000G 000000G 3#:  BIT     ##DEAD,LSW3(R1) ;IS THIS LINE INSTALLED?
21 012556 001047          BNE     2$          ;BR IF NOT -- DON'T ALLOCATE ANY BUFFER SPACE
22 012560 020127 000000G      CMP     R1,#FSTD L      ;IS THIS A DETACHED JOB LINE?
23 012564 103403          BLO     1$          ;BR IF NOT
24 012566 020127 000000G      CMP     R1,#LSTD L      ;DETACHED JOB LINE?
25 012572 101441          BLOS   2$          ;BR IF DETACHED JOB -- DON'T ALLOCATE BUFFERS
26 012574 010561 000000G 1#:  MOV     R5,LTTPAR(R1) ;SET PHYSICAL MEMORY PAR OFFSET FOR BUFFER
27 012600 012702 000000G      MOV     #VPAR6,R2      ;GET VIRTUAL MEMORY ADDRESS FOR BASE OF PAR6
28 012604 010261 000000G      MOV     R2,LINBUF(R1) ;INPUT BUFFER STARTS AT BASE OF PAR6 REGION
29 012610 016100 000000G      MOV     LINSIZ(R1),R0 ;GET # BYTES FOR INPUT BUFFER
30 012614 010061 000000G      MOV     R0,LINSPC(R1) ;SET # FREE BYTES IN INPUT BUFFER
31 012620 060002          ADD     R0,R2          ;ADVANCE VIRTUAL ADDRESS
32 012622 010261 000000G      MOV     R2,LINEND(R1) ;POINTS PAST END OF INPUT BUFFER
33 012626 010003          MOV     R0,R3          ;Get # bytes in input buffer
34 012630 062703 000007      ADD     #7,R3          ;Bound up to multiple of 8
35 012634 072327 177775      ASH     #-3,R3         ;Get # bytes needed in activation-flag buffer
36 012640 060302          ADD     R3,R2          ;Reserve space for activation-flag buffer
37 012642 060300          ADD     R3,R0          ;Accumulate total buffer space
38 012644 010261 000000G      MOV     R2,LOTBUF(R1) ;POINTS TO START OF OUTPUT BUFFER AREA
39 012650 066100 000000G      ADD     LOTSIZ(R1),R0 ;ACCUMULATE # BYTES IN BOTH BUFFERS
40 012654 066102 000000G      ADD     LOTSIZ(R1),R2 ;ADVANCE VIRTUAL ADDRESS COUNTER
41 012660 010261 000000G      MOV     R2,LOTEND(R1) ;SAVE ADDRESS OF END OF OUTPUT BUFFER
42 012664 062700 000077      ADD     #77,R0         ;BOUND UP TO MULTIPLE OF 64 BYTES
43 012670 072027 177772      ASH     #-6,R0         ;CONVERT TO # 64-BYTE BLOCKS ALLOCATED
44 012674 060005          ADD     R0,R5          ;ADVANCE PHYSICAL MEMORY PAR ADDRESS
45 012676 062701 000002 2#:  ADD     #2,R1          ;ADVANCE LINE NUMBER
46 012702 020127 000000G      CMP     R1,#LSTSL     ;HAVE WE DONE ALL LINES YET?
47 012706 101720          BLOS   3$          ;BR IF MORE TO DO
48         ;
49         ; Allocate space for shared file record locking data structures
50         ;
51 012710 005737 000000G      TST     VMXSF          ;Shared file support wanted?
52 012714 001451          BEQ     5$          ;Br if not
53 012716 013737 000000G 000000G  MOV     VNUMDC,NUMDCD ;Set number of data cache blocks
54 012724 013737 000000G 000000G  MOV     VMXFC,NUMCDB  ;Set number of free CDB's
55 012732 010537 000000G      MOV     R5,LOKMEM     ;Set phys address of base of area
56 012736 012701 000000G      MOV     #FF#$SZ,R1   ;Size of an FDB
57 012742 070137 000000G      MUL     VMXSF,R1     ;Times number of FDB's

```

ALBFX -- Allocate buffers in extended memory region

```

58 012746 062701 000000C      ADD      #<NLINES*FW##SZ>,R1 ;Space needed for wait blocks
59 012752 013703 000000G      MOV      VMLBLK,R3          ;Max blocks a CDB may hold locked
60 012756 006303              ASL      R3                  ;Two bytes per entry
61 012760 062703 000000G      ADD      #FC#LBN,R3        ;Add base size of a CDB
62 012764 070337 000000G      MUL      VMXSFC,R3         ;Times number of shared file channels
63 012770 060301              ADD      R3,R1              ;Accumulate space needed
64 012772 012703 000000G      MOV      #DC##SZ,R3       ;Size of a data cache descriptor
65 012776 070337 000000G      MUL      VNUMDC,R3        ;Times number of data cache entries
66 013002 060301              ADD      R3,R1              ;Reserve space for data cache descriptors
67 013004 062701 000100      ADD      #64.,R1           ;Bound up to 64 byte unit
68 013010 072127 177772      ASH      #-6,R1            ;Convert to # 64 byte units
69 013014 042701 176000      BIC      #176000,R1        ;Clear sign extension
70 013020 060105              ADD      R1,R5              ;Reserve space for data structures
71 013022 010537 000000G      MOV      R5,LOKCSH        ;Save pointer to start of cache buffer area
72 013026 013701 000000G      MOV      VNUMDC,R1        ;# shared-file data cache blocks wanted
73 013032 070127 000010      MUL      #8.,R1           ;8 64-byte blocks each (512 bytes each)
74 013036 060105              ADD      R1,R5              ;Reserve space for data cache buffers
75                               ;
76                               ; Allocate space for mapped I/O buffers
77                               ;
78 013040 105737 000000G      5$:      TSTB     MIOFLG      ;Are any mapped I/O buffers needed?
79 013044 001415              BEQ      7$                ;Br if not
80 013046 013701 000000G      MOV      MIOBHD,R1        ;Point to 1st mapped I/O control block
81 013052 001412              BEQ      7$                ;Br if no more buffers needed
82 013054 010561 000000G      6$:      MOV      R5,MI$SBP(R1) ;Set address of base of buffer
83 013060 113703 000000G      MOV      VMIOSZ,R3        ;Get # blocks for buffer
84 013064 072327 000003      ASH      #3,R3            ;Convert to # 64-byte pages
85 013070 060305              ADD      R3,R5              ;Allocate space for buffer
86 013072 016101 000000G      MOV      MI$LNK(R1),R1    ;Get address of next control block
87 013076 001366              BNE      6$                ;Loop if more to allocate
88                               ;
89                               ; Allocate space for performance monitor data buffer if it is wanted.
90                               ;
91 013100 012701 000000G      7$:      MOV      #PMSIZE,R1     ;DID USER GEN IN PERFORMANCE MONITOR FEATURE?
92 013104 001407              BEQ      9$                ;BR IF NOT
93 013106 010537 000000G      MOV      R5,PMPAR        ;SET BASE ADDRESS OF PM BUFFER
94 013112 062701 000077      ADD      #77,R1           ;BOUND SIZE UP TO 64-BYTE MULTIPLE
95 013116 072127 177772      ASH      #-6,R1           ;CONVERT TO # 64-BYTE BLOCKS
96 013122 060105              ADD      R1,R5              ;ADVANCE FREE MEMORY POINTER
97                               ;
98                               ; Finished
99                               ;
100 013124 012603             9$:      MOV      (SP)+,R3
101 013126 012602             MOV      (SP)+,R2
102 013130 012601             MOV      (SP)+,R1
103 013132 000207             RETURN

```

OPNKMN -- Open channel to TSKMON

```

1          .SBTTL  OPNKMN -- Open channel to TSKMON
2          ;-----
3          ; OPNKMN is called to open an I/O channel to TSKMON SAV file and to
4          ; set up information about TSKMON.
5          ;
6          ; Inputs:
7          ; R5 = Address of base of free memory area
8          ;
9          ; Outputs:
10         ; KMNCHN = Saved status of channel to use to access TSKMON SAV file.
11         ; KMNTOP = Top of memory address for TSKMON.
12         ; KMNHI  = Top address of TSKMON - KMNBAS.
13         ; KMNPGS = Number of 256-word memory pages needed for TSKMON & context area
14         ; KMNSTK = Address of stack to use while TSKMON running.
15         ; KMNSTR = Starting address of TSKMON.
16         ;
17 013134 010246 OPNKMN: MOV      R2,-(SP)
18         ;
19         ; Lookup TSKMON file.
20         ;
21 013136         . LOOKUP #AREA,#1,#KMNNAM ;TRY TO FILE KMON SAV FILE
22 013156 103517 BCS      9#          ;BR IF NOT THERE
23         ;
24         ; Read block 0 of save file and extract some information.
25         ;
26 013160 013702 000152' MOV      WRKBUF,R2      ;Point to work buffer
27 013164         . READW #AREA,#1,R2,#256.,#0 ;READ BLOCK 0 OF SAV FILE
28         ; Determine size of kmon
29 013220 016200 000050 MOV      50(R2),R0      ;GET TOP ADDRESS OF KMON
30 013224 062700 000003 ADD      #3,R0         ;BOUND UP TO NEXT WORD
31 013230 042700 000001 BIC      #1,R0         ;FORCE EVEN
32 013234 010037 000000 MOV      R0,KMNTOP
33         ; Determine number of 256-word memory pages needed while kmon running.
34 013240 162700 000000 SUB      #KMNBAS,R0    ;BASE ADDRESS OF KMON
35 013244 010037 000000 MOV      R0,KMNHI     ;TOP OF TSKMON - KMNBAS
36 013250 062700 000777 ADD      #511.,R0     ;BOUND UP TO PAGE SIZE
37 013254 000241 CLC
38 013256 006000 ROR      R0           ;CVT TO # WORDS
39 013260 000300 SWAB     R0           ;CVT TO # PAGES
40 013262 042700 177400 BIC      #^C377,R0
41 013266 063700 000000 ADD      CXTPAG,R0    ;# PAGES NEEDED FOR JOB CONTEXT AREA
42 013272 010037 000000 MOV      R0,KMNPGS    ;# 256-wd pages needed for kmon + context area
43         ; Determine Kmon stack pointer.
44 013276 016237 000042 000000 MOV      42(R2),KMNSTK ;INITIAL STACK POINTER FOR KMON
45         ; Determine Kmon starting address.
46 013304 016237 000040 000000 MOV      40(R2),KMNSTR ;STARTING ADDRESS
47         ;
48         ; Set up demo-system time limit
49         ; (If this is a demo version of TSX-Plus, the number of minutes the system
50         ; is to run before it crashes is stored in location 300 of TSKMON)
51         ;
52 013312 016237 000300 000000 MOV      300(R2),DTLX  ;SET DEMO TIME-LIMIT
53         ;
54         ; Now save status of channel so we can do a reopen when we need kmon.
55         ;
56 013320 012700 000000 MOV      #KMNCHN,R0   ;GET KMON CHANNEL SAVE AREA
57 013324 013702 000072' MOV      KMNNAM,R2     ;GET KMON DEVICE NAME

```

```
58 013330 004737 024616'          CALL   SETCHN          ;SAVE CHANNEL STATUS
59                                     ;
60                                     ; Lookup CCL.SAV and save channel status for it.
61                                     ;
62 013334                                     . LOOKUP #AREA,#1,#CCLNAM;LOOKUP SY:CCL.SAV
63 013354 103410                          BCS     B$           ;BR IF CAN'T FIND CCL
64 013356 012700 000000G                   MOV     #CCLSAV,R0   ;CHANNEL SAVE AREA
65 013362 013702 000102'                   MOV     CCLNAM,R2    ;DEVICE NAME
66 013366 004737 024616'                   CALL   SETCHN          ;SAVE CHANNEL STATUS
67                                     ;
68                                     ; Finished
69                                     ;
70 013372 012602                          10$:    MOV     (SP)+,R2
71 013374 000207                          RETURN
72                                     ;
73                                     ; Error: We could not find SY:CCL.SAV
74                                     ;
75 013376                          8$:    .PRINT  #TSXHD
76 013404                          .PRINT  #NOCCL          ;PRINT ERROR MESSAGE
77 013412 000137 004250'                   JMP     INISTP        ;ABORT INITIALIZATION
78                                     ;
79                                     ; Error: We could not locate TSKMON SAV file.
80                                     ;
81 013416                          9$:    .PRINT  #TSXHD          ;PRINT ERROR MESSAGE
82 013424                          .PRINT  #NOKMON
83 013432 000137 004250'                   JMP     INISTP        ;ABORT INITIALIZATION
```

```

1          .SBTTL  CLINIT -- Initialize CL handler
2          ;-----
3          ; Perform initialization for CL (Communication Line) handler
4          ;
5          ; Inputs:
6          ;   R5 = Address of start of free memory area.
7          ;
8          ; Outputs:
9          ;   R5 = Address of new start of free memory area.
10         ;
11 013436 010146 CLINIT: MOV     R1,-(SP)
12 013440 010246      MOV     R2,-(SP)
13 013442 010346      MOV     R3,-(SP)
14         ;
15         ; Initialize tables for each CL unit
16         ;
17 013444 005003      CLR     R3           ;Accumulate ring buffer sizes in R3
18 013446 012701 000000C  MOV     #2*CLTOTL-1,R1;Get index # of last CL unit
19         ;
20         ; See if this CL unit is connected to hardware or is free to be
21         ; connected later to a time-sharing line.
22         ;
23 013452 016102 000000G 1$:  MOV     CL$LIX(R1),R2 ; Is this CL unit associated with a line?
24 013456 001416      BEQ     5$           ; Br if not
25 013460 012762 000000G 000000G  MOV     ##SXDN,LSW10(R2); Send XDN when we start the line
26 013466 010162 000000G  MOV     R1,LCLUNT(R2) ; Associate the CL unit with this line
27 013472 005762 000000G  TST     RSR(R2)       ; Does this unit have a specified RSR addr?
28 013476 001006      BNE     5$           ; Br if yes
29 013500 005762 000000G  TST     LMXNUM(R2)    ; Is this a mux line?
30 013504 001003      BNE     5$           ; Br if yes
31 013506 005061 000000G  CLR     CL$EPS(R1)    ; Say no endstring buffer
32 013512 000472      BR      4$           ; Line is not genned in
33         ;
34         ; Allocate and set up pointers for the output ring buffers
35         ;
36 013514 010561 000000G 5$:  MOV     R5,CL$ORB(R1) ; Start of output ring buffer
37 013520 010561 000000G  MOV     R5,CL$ORP(R1) ; Input character pointer
38 013524 010561 000000G  MOV     R5,CL$ORG(R1) ; Next available character pointer
39 013530 012700 000000G  MOV     #CLORSZ,R0    ; Get default output ring buffer size
40 013534 005702      TST     R2           ; Is this CL unit connected to a line?
41 013536 001402      BEQ     6$           ; Br if not
42 013540 016200 000000G  MOV     LOTSIZ(R2),R0 ; Get size of output ring buffer
43 013544 010061 000000G 6$:  MOV     R0,CL$ORA(R1) ; Set size of output ring buffer
44 013550 010061 000000G  MOV     R0,CL$ORS(R1) ; Available space in ring buffer
45 013554 060005      ADD     R0,R5         ; Point beyond end of ring buffer
46 013556 010561 000000G  MOV     R5,CL$ORE(R1) ; Address past end of ring buffer
47 013562 060003      ADD     R0,R3         ; Accumulate size of output ring buffers
48         ;
49         ; Allocate space for end-of-file string buffer
50         ;
51 013564 010561 000000G  MOV     R5,CL$EPS(R1) ; Set pointer to end-of-file string buffer
52 013570 005061 000000G  CLR     CL$EPP(R1)    ; No string to print yet
53 013574 062705 000001G  ADD     #<CLEDFS+1>,R5 ; Reserve space for buffer
54 013500 062703 000001G  ADD     #<CLEDFS+1>,R3 ; Accumulate buffer space
55         ;
56         ; Initialize end-of-file form-feed count
57         ;

```

CLINIT -- Initialize CL handler

```

58 013604 005061 000000G          CLR      CL$EPN(R1)      ; Init ENDPAGE=0
59                                     ;
60                                     ; Initialize option word
61                                     ;
62 013610 012700 000000G          MOV      #<CO$DEF>,R0      ; Get default option flags
63 013614 005702                   TST      R2                ; Is this CL unit connected with a line?
64 013616 001421                   BEQ      7$                ; Br if not
65 013620 016202 000000G          MOV      ILSW2(R2),R2     ; Get line options
66 013624 032702 000000G          BIT      #$TAB,R2        ; Does hardware support tabs?
67 013630 001402                   BEQ      2$                ; Br if not
68 013632 052700 000000G          BIS      #CO$TAB,R0      ; Set hardware-tab flag
69 013636 032702 000000G          2$:    BIT      #$FORM,R2   ; Does hardware support form feeds?
70 013642 001402                   BEQ      3$                ; Br if not
71 013644 052700 000000G          BIS      #CO$FF,R0       ; Set hardware-form-feed flag
72 013650 032702 000000G          3$:    BIT      #$8BIT,R2  ; Does hardware want 8 bit support?
73 013654 001402                   BEQ      7$                ; Br if not
74 013656 052700 000000G          BIS      #CO$8BT,R0      ; Enable 8 bit support for CL line
75 013662 010061 000000G          7$:    MOV      R0,CL$OPT(R1) ; Set options for this CL line
76                                     ;
77                                     ; Initialize page length
78                                     ;
79 013666 012761 000102 000000G    MOV      #66.,CL$LEN(R1) ; Say page length = 66 lines
80                                     ;
81                                     ; Initialize status flags
82                                     ;
83 013674 005061 000000G          CLR      CL$STA(R1)      ; Initialize status flags
84                                     ;
85                                     ; Do next line
86                                     ;
87 013700 162701 000002          4$:    SUB      #2,R1        ; Get index # of next unit
88 013704 002262                   BGE      1$                ; Loop if more units to initialize
89                                     ;
90                                     ; Make a device table entry for "CL" device
91                                     ;
92 013706 062737 000002 000000G    ADD      #2,NUMDEV        ; One more device
93 013714 013701 000000G          MOV      NUMDEV,R1        ; Get device table index
94 013720 010137 000000G          MOV      R1,CLDEVX        ; Remember index number of CL device
95 013724 013761 000202' 000000G    MOV      R5OCL,PNAME(R1) ; Set device name ("CL")
96 013732 012761 000000G 000000G    MOV      #CLSTS,DVSTAT(R1) ; Set dev status flags
97 013740 012761 000000C 000000G    MOV      #<DX$NCA!DX$NMT!DX$NRD>,DVFLAG(R1) ; Device info flags
98 013746 005061 000000G          CLR      DEVSIZ(R1)       ; Clear device size
99 013752 012761 000006G 000000G    MOV      #CLHEAD+6,HANENT(R1) ; Set handler entry point (4th word)
100 013760 062703 000000G          ADD      #CLSIZE,R3       ; Get size of handler
101 013764 062703 000000C          ADD      #<<CLTOTL*46.>+<NIDL*32.>>,R3 ; Add size of tables in TSGEN
102 013770 010361 000000G          MOV      R3,HANSIZ(R1)    ; Set size of handler
103 013774 005205                   INC      R5                ; Make sure free-memory pointer is even
104 013776 042705 000001          BIC      #1,R5
105                                     ;
106                                     ; Make a device table entry for C1 if there are more than 8 CL units
107                                     ;
108 014002 022727 000000G 000010    CMP      #CLTOTL,#8.      ; Are there more than 8 CL units?
109 014010 101430                   BLOS     13$              ; Br if not -- Don't need C1
110 014012 062737 000002 000000G    ADD      #2,NUMDEV        ; One more device
111 014020 013701 000000G          MOV      NUMDEV,R1        ; Get device table index
112 014024 010137 000000G          MOV      R1,C1DEVX        ; Remember index number of CL device
113 014030 013761 000210' 000000G    MOV      R5OC1,PNAME(R1) ; Set device name ("C1")
114 014036 012761 000000G 000000G    MOV      #CLSTS,DVSTAT(R1) ; Set dev status flags

```

```

115 014044 012761 000000C 000000G      MOV      #<DX#NCA!DX#NMT!DX#NRD>, DVFLAG(R1) ;Device info flags
116 014052 005061 000000G      CLR      DEVSIZ(R1)      ;Clear device size
117 014056 012761 000006G 000000G      MOV      #CLHEAD+6, HANENT(R1) ;Set handler entry point (4th word)
118 014064 012761 000004 000000G      MOV      #4., HANSIZ(R1) ;Set size of handler
119
120 ; Set the version number to be returned by the .SPFUN used by
121 ; VTCOM to see if it is matched to the correct version of XL/XC.
122
123 014072 105737 000000G      13$:    TSTB     CLVERS      ;Was a version specified in TSGEN?
124 014076 001023          BNE      9$           ;Br if yes
125 014100 113703 000000G      MOVVB   SYSVER, R3    ;Get current RT-11 version number
126 014104 000303          SWAB    R3           ;Put in high order byte
127 014106 153703 000000G      BISB    SYSUPD, R3    ;Put update number in low-order byte
128 014112 012702 000522'      MOV     #CLVTBL, R2   ;Point to table with CL version numbers
129 014116 020322      10$:    CMP     R3, (R2)+  ;Is this entry for our version?
130 014120 001407          BEQ     12$          ;Br if yes
131 014122 005722          TST    (R2)+        ;Skip cell with CL version
132 014124 020227 000556'      CMP     R2, #CLVEND  ;Checked all table entries?
133 014130 103772          BLO    10$          ;Loop if not
134 014132 012701 000021      MOV     #17., R1     ;Set default CL version if not found
135 014136 000401          BR     11$          ;
136 014140 011201      12$:    MOV     (R2), R1   ;Get correct CL version
137 014142 110137 000000G      11$:    MOVVB   R1, CLVERS ;Set CL version number
138
139 ; Finished
140
141 014146 012603      9$:     MOV     (SP)+, R3
142 014150 012602      MOV     (SP)+, R2
143 014152 012601      MOV     (SP)+, R1
144 014154 000207      RETURN

```

LDINIT -- Determine LD translation table format

```

1                                     .SBTTL  LDINIT -- Determine LD translation table format
2                                     ;-----
3                                     ; RT-11 V5.4 changed that format of the LD translation tables.
4                                     ; Determine which version of RT-11 is being used and set the
5                                     ; appropriate value for LDVERS to generate correct table
6                                     ; format in response to LD .SPFUN 372
7                                     ;
8 014156 010146 LDINIT: MOV      R1,-(SP)
9 014160 113701 000000G      MOVB   LDVERS,R1      ; See if value was specified in TSGEN
10 014164 001022           BNE    9$              ; If value specified in TSGEN, use it
11 014166 010246           MOV    R2,-(SP)
12 014170 010346           MOV    R3,-(SP)
13 014172 013703 000000G      MOV    SYSVER,R3      ; Get current RT version and update
14 014176 012701 000001      MOV    #1,R1          ; Assume version format RT5.3 or earlier
15 014202 012702 000556'     MOV    #LD1TBL,R2     ; Point to table of versions using format 1
16 014206 022203           1$:  CMP    (R2)+,R3      ; Does this entry match actual version?
17 014210 001404           BEQ    5$              ; Exit if so
18 014212 005712           TST   @R2             ; End of table?
19 014214 001374           BNE   1$              ; Keep looking if more entries
20 014216 012701 000002      MOV    #2,R1          ; If not in format 1 table, use format 2
21 014222 110137 000000G      5$:  MOVB   R1,LDVERS   ; Remember for LD SPFUN 372
22 014226 012603           MOV    (SP)+,R3
23 014230 012602           MOV    (SP)+,R2
24 014232 012601           9$:  MOV    (SP)+,R1
25 014234 000207           RETURN

```

INDINI -- Initialize IND program

```

1          .SBTTL  INDINI -- Initialize IND program
2          ;-----
3          ; Perform initialization for IND program.
4          ;
5          ; Outputs:
6          ; If IND is available, the following information is set up:
7          ;   INDSAV = 5 word .SAVESTATUS block for SY:IND.SAV file
8          ;   INDDBL = Lowest block # within IND.SAV file of data overlay segment.
9          ;   INDDBS = Number of blocks used for data overlay segment.
10         ;   INDTSV = 5 word .SAVESTATUS block for SY:TSXIND.TSX file
11         ;
12 014236 010246 INDINI: MOV     R2,-(SP)
13 014240 010346         MOV     R3,-(SP)
14         ;
15         ; Determine if IND support is wanted
16         ;
17 014242 005037 000000G         CLR     INDSAV         ;ASSUME IND SUPPORT NOT WANTED
18         ;
19         ; Lookup SY:IND.SAV file
20         ;
21 014246 013737 000000G 000264'         MOV     SYNAME,INDNAM ;LOOK UP IND ON BOOT DEVICE
22 014254         .LOOKUP #AREA,#1,#INDNAM ;TRY TO FIND SY:IND.SAV
23 014274 103002         BCC     4$           ;BR IF FOUND IND
24 014276 000137 014730'         JMP     9$           ;IF CAN'T FIND IND, THEN NO IND SUPPORT
25         ;
26         ; Set up information about IND overlay data segment
27         ;
28 014302 013703 000152' 4$:  MOV     WRKBUF,R3         ;Get pointer to work buffer
29 014306         .READW #AREA,#1,R3,#256.,#0 ;READ IN BLOCK 0 OF SAV FILE
30 014342 016302 000064         MOV     64(R3),R2         ;GET POINTER TO OVERLAY TABLE
31 014346         .READW #AREA,#1,R3,#256.,#1 ;READ IN BLOCK 1 WITH OVERLAY TABLE
32 014404 162702 001000         SUB     #1000,R2         ;GET ADDRESS OF OVERLAY TABLE REL TO BLOCK 1
33 014410 060302         ADD     R3,R2           ;ADD BASE ADDRESS WHERE BLOCK 1 DATA IS
34 014412 011203         MOV     (R2),R3         ;Get virtual address of segment 0
35 014414 020312 5$:  CMP     R3,(R2)         ;Search for 1st segment with different addr
36 014416 001003         BNE     6$           ;Br if found it (this is the data segment)
37 014420 062702 000006         ADD     #6,R2           ;Point to overlay table entry for next seg
38 014424 000773         BR     5$
39 014426 005722 6$:  TST     (R2)+         ;Point to word with block # if SAV file
40 014430 012237 000000G         MOV     (R2)+,INDDBL     ;GET BLOCK # IN SAV FILE OF DATA OVERLAY
41 014434 011202         MOV     (R2),R2         ;GET # OF WORDS IN OVERLAY SEGMENT
42 014436 062702 000377         ADD     #255.,R2        ;ROUND UP TO NEXT BLOCK
43 014442 000302         SWAB    R2             ;CONVERT # WORDS TO # BLOCKS
44 014444 042702 177400         BIC     #^C<377>,R2
45 014450 010237 000000G         MOV     R2,INDDBS       ;SAVE # BLOCKS USED FOR DATA OVERLAY
46         ;
47         ; Do .SAVESTATUS on channel opened to IND.SAV file so that we
48         ; can do a reopen to access it from KMON.
49         ;
50 014454 012700 000000G         MOV     #INDSAV,R0       ;GET ADDRESS OF SAVESTATUS BLOCK
51 014460 013702 000264'         MOV     INDNAM,R2       ;GET RAD50 DEVICE NAME
52 014464 004737 024616'         CALL    SETCHN         ;SAVE FILE STATUS
53         ;
54         ; Determine how much space is needed for SY:TSXIND.TSX swap file
55         ;
56 014470 013703 000000G         MOV     INDDBS,R3       ;GET # BLOCKS NEEDED PER JOB
57 014474 070327 000000C         MUL     #<LSTSL/2>,R3   ;TIMES TOTAL NUMBER OF JOBS

```

INDINI -- Initialize IND program

```

58
59
60 ; Load Rt-11 device handler for ind swap file.
61 ;
62 014500 013700 0000000 MOV INDFIL,R0 ;Get name of the device
63 014504 004737 025176' CALL RTFTCH ;Try to fetch the RT-11 device handler
64 014510 103522 BCS 11$ ;Br if error on handler fetch
65 ;
66 ; See if TSXIND file already exists
67 ;
68 014512 .LOOKUP #AREA,#1,#INDFIL ;DOES SY:TSXIND.TSX FILE EXIST NOW?
69 014532 103415 BCS 1$ ;BR IF NOT
70 014534 020003 CMP R0,R3 ;IS IT OF THE CORRECT SIZE?
71 014536 001462 BEQ 2$ ;BR IF YES
72 014540 .PURGE #1 ;FILE IS OF WRONG SIZE
73 014546 .DELETE #AREA,#1,#INDFIL;DELETE OLD FILE
74 ;
75 ; File does not now exist
76 ; Create new file
77 ;
78 014566 1$: .ENTER #AREA,#1,#INDFIL,R3 ;CREATE NEW TSXIND FILE
79 014612 103451 BCS 10$ ;BR IF ERROR ON CREATE
80 014614 010302 MOV R3,R2 ;# BLOCKS IN FILE
81 014616 005302 DEC R2 ;GET # OF LAST BLOCK IN FILE
82 014620 .WRITW #AREA,#1,WRKBUF,#256.,R2 ;WRITE TO LAST BLOCK OF FILE
83 014656 .CLOSE #1 ;NOW CLOSE THE FILE
84 014664 .LOOKUP #AREA,#1,#INDFIL ;REOPEN TSXIND FILE WITH LOOKUP
85 ;
86 ; Do .SAVESTATUS for SY:TSXIND.TSX file
87 ;
88 014704 012700 0000000 2$: MOV #INDTSV,R0 ;POINT TO SAVESTATUS BLOCK
89 014710 013702 0000000 MOV INDFIL,R2 ;GET RAD50 DEVICE NAME
90 014714 004737 024616' CALL SETCHN ;SAVE FILE INFO
91 014720 .RELEAS #INDFIL ;Release device handler
92 ;
93 ; Finished
94 ;
95 014730 012603 9$: MOV (SP)+,R3
96 014732 012602 MOV (SP)+,R2
97 014734 000207 RETURN
98 ;
99 ; Error occurred while opening SY:TSXIND.TSX file
100 ;
101 014736 10$: .PRINT #TSXHD ;Print error message
102 014744 .PRINT #INDOPN
103 014752 004737 010142' CALL SPNEED ;Print info about number of blocks needed
104 ;
105 ; Error: Invalid device specification.
106 ;
107 014756 010001 11$: MOV R0,R1 ;Save device name
108 014760 .PRINT #TSXHD ;Print error message
109 014766 .PRINT #INDOPN
110 014774 004737 010170' CALL BADDEV ;Print invalid device specification

```

UCLINI -- Initialize TSXUCL data file

```

1          .SBTTL  UCLINI -- Initialize TSXUCL data file
2          ;-----
3          ; UCLINI is called to initialize the TSXUCL data file which is used
4          ; to store user-defined commands.
5          ;
6          ; Outputs:
7          ;   TSXUCL data file is initialized.
8          ;   UCLBLK = Number of blocks in data file for each job.
9          ;
10         015000 010246 UCLINI: MOV     R2,-(SP)
11         015002 010346         MOV     R3,-(SP)
12         ;
13         ; Determine if TSXUCL data file is needed
14         ;
15         015004 105737 0000000  TSTB   VU#CL      ;Is TSXUCL being used at all?
16         015010 001536         BEQ    9#             ;Br if not
17         015012 013702 0000000  MOV    VUCLMC,R2   ;Get maximum number of commands
18         015016 001533         BEQ    9#             ;Br if none allowed
19         ;
20         ; Determine number of blocks needed in data file for each job
21         ;
22         015020 012700 0000000  MOV    #UK##SZ,R0  ;Size of each keyword descriptor
23         015024 062700 0000000  ADD    #US##SZ,R0  ;Size of each command string descriptor
24         015030 070200         MUL    R0,R2        ;Compute total # bytes for keywords+commands
25         015032 062703 0007770  ADD    #UC##SZ+511.,R3 ;Add space for control information & round up
26         015036 005502         ADC    R2           ;Propogate carry
27         015040 071227 001000  DIV    #512.,R2    ;Convert to # of blocks needed
28         015044 010237 0000000  MOV    R2,UCLBLK   ;Save number of blocks needed per job
29         ;
30         ; Multiply by number of jobs to get total file size
31         ;
32         015050 070227 0000000  MUL    #<LSTSL/2>,R2 ;Times total number of jobs
33         ;
34         ; Load Rt-11 device handler for ind swap file.
35         ;
36         015054 013700 0000000  MOV    UCLDAT,R0   ;Get name of the device
37         015060 004737 025176'  CALL   RTFTCH      ;Try to fetch the RT-11 device handler
38         015064 103523         BCS   11#          ;Br if error on handler fetch
39         ;
40         ; The total required file size is now in R3.
41         ; See if the file already exists.
42         ;
43         015066         .LOOKUP #AREA,#1,#UCLDAT ;See if the file exists now
44         015106 103415         BCS   1#           ;Br if file does not exist
45         015110 020003         CMP   R0,R3        ;Is existing file of correct size?
46         015112 001446         BEQ   2#           ;Br if yes -- use the old file
47         015114         .PURGE #1             ;Purge the channel
48         015122         .DELETE #AREA,#1,#UCLDAT;Delete the old file
49         ;
50         ; Create a new data file
51         ;
52         015142 1#:. ENTER #AREA,#1,#UCLDAT,R3 ;Create new data file
53         015166 103452         BCS   10#          ;Br if error creating the file
54         015170 005303         DEC   R3           ;Get # of last block in the file
55         015172         .WRITW #AREA,#1,WRKBUF,#256.,R3 ;Write to last block of file
56         ;
57         ; Translate possible logical device name to physical name and close

```

UCLINI -- Initialize TSXUCL data file

```

58          ; (Physical name is needed for TSXUCL program.)
59          ;
60 015230   2$: .CSTAT #AREA,#1,#NFSBLK      ;GET CHANNEL STATUS INFORMATION
61 015250   .MOV    <NFSBLK+12>,R2      ;FETCH DEVICE NAME IN RAD50
62 015254   .ADD    <NFSBLK+10>,R2      ;ADD IN DEVICE UNIT NUMBER
63 015260   .ADD    #^R 0,R2          ;CONVERT UNIT NUMBER TO RAD50
64 015264   .MOV    R2,UCLDAT          ;SET PHYSICAL NAME BACK INTO TSGEN CELL
65 015270   .CLOSE  #1                ;Close the file
66 015276   .RELEAS #UCLDAT           ;Release device handler
67          ;
68          ; Finished
69          ;
70 015306   9$: .MOV    (SP)+,R3
71 015310   .MOV    (SP)+,R2
72 015312   .RETURN
73          ;
74          ; Error creating the data file
75          ;
76 015314   10$: .PRINT #TSXHD          ;Print error message
77 015322   .PRINT #UCLOPN
78 015330   .CALL   SPNEED            ;Print info about number of blocks needed
79          ;
80          ; Error: Invalid device specification.
81          ;
82 015334   11$: .MOV    R0,R1          ;Save device name
83 015336   .PRINT #TSXHD          ;Print error message
84 015344   .PRINT #UCLOPN
85 015352   .CALL   BADDEV           ;Print invalid device specification

```

MEMINI -- Initialize memory management

```

1          .SBTTL  MEMINI -- Initialize memory management
2          ;-----
3          ; Initialize memory management registers for a 1-to-1 mapping.
4          ; But leave memory management turned off.
5          ;
6 015356 010146 MEMINI: MOV     R1,-(SP)
7 015360 010246      MOV     R2,-(SP)
8 015362 010346      MOV     R3,-(SP)
9 015364 010446      MOV     R4,-(SP)
10 015366 010546     MOV     R5,-(SP)
11         ;
12         ; Initialize all pages for a 1-to-1 mapping.
13         ;
14 015370 012700 000000G 12$:  MOV     #KPAR0,R0      ;Kernel mode PAR 0
15 015374 012701 000000G      MOV     #UPAR0,R1      ;User mode PAR 0
16 015400 012702 000000G      MOV     #KPDR0,R2      ;Kernel mode PDR 0
17 015404 012703 000000G      MOV     #UPDR0,R3      ;User mode PDR 0
18 015410 012704 000010      MOV     #8,R4         ;Initialize 8 pages
19 015414 005005      CLR     R5         ;Set initial PAR value
20 015416 010520     2$:  MOV     R5,(R0)+      ;Set kernel PAR
21 015420 010521      MOV     R5,(R1)+      ;Set user PAR value
22 015422 012722 077406      MOV     #077406,(R2)+   ;Set kernel PDR
23 015426 012723 077406      MOV     #077406,(R3)+   ;Set user PDR value
24 015432 062705 000200      ADD     #200,R5       ;Advance block number
25 015436 077411      SOB     R4,2$      ;Init all pages
26         ;
27         ; Map kernel mode I/O page (160000) to 17760000.
28         ;
29 015440 012737 000000G 000000G      MOV     #IOPAGE,@#KPAR7 ;Map I/O page
30         ;
31         ; Finished
32         ;
33 015446 012605      MOV     (SP)+,R5
34 015450 012604      MOV     (SP)+,R4
35 015452 012603      MOV     (SP)+,R3
36 015454 012602      MOV     (SP)+,R2
37 015456 012601      MOV     (SP)+,R1
38 015460 000207      RETURN

```

MEMTST -- Set up information about available memory space

```

1          .SBTTL  MEMTST -- Set up information about available memory space
2          ;-----
3          ; MEMTST is called to set up information related to memory management.
4          ; MEMTST performs the following functions:
5          ;   1. Determine how much memory is installed on machine.
6          ;   2. Load Kernel mode mapping registers.
7          ;
8          ; Inputs:
9          ;   R5 = top of memory currently allocated for TSX and low memory buffers.
10         ;
11         ; Outputs:
12         ;   PHYMEM = 64-byte block # above top of physical memory.
13         ;   FMEMHI = 64-byte block # above top of memory available for system.
14         ;   Kernel mode mapping registers loaded.
15         ;   Memory management is left turned off.
16         ;
17         ;
18         ;
19         ; Offset word to test for memory wrap - choose a location which will not
20         ; effect RT-11 or TSX-Plus initialization.
21         ;
22         000110      TSTWRD = 110          ; Offset word to test for memory wrap
23         ;
24         015462      010146      MEMTST:  MOV     R1, -(SP)
25         015464      010246              MOV     R2, -(SP)
26         015466      010346              MOV     R3, -(SP)
27         015470      010446              MOV     R4, -(SP)
28         015472      010546              MOV     R5, -(SP)
29         015474      013746      000004      MOV     @#4, -(SP)          ; Save illegal mem. ref. trap vector
30         ;
31         ; Determine if this machine has a memory management register # 3.
32         ; If it does not, then machine cannot possibly have more than 256Kb.
33         ;
34         015500      012737      015716' 000004      MOV     #TRCSET, @#4          ; Catch trap
35         015506      000240              NOP                      ; Clean out 11/73 pipeline
36         015510      000240              NOP                      ; Before attempting trap
37         015512      005737      00000006      TST     @#SR3MMR          ; Try to access status register 3
38         015516      103402              BCS    22$              ; Br if MMU 3 status register is non-existent
39         015520      105237      00000006      INCB   SR3FLG          ; No trap. We must have SR3
40         ;
41         ; If we are running on a Professional, there is a register that tells
42         ; us how much memory is installed on the machine.
43         ;
44         015524      22$:
45         .IF     NE, PROASM
46         015524      105737      00000006      TSTB   PROFLG          ; Are we running on a Professional?
47         015530      001410              BEQ    26$              ; Br if not
48         015532      005005              CLR    R5              ; Load byte without sign extension
49         015534      153705      173050      BISB   @#173050, R5      ; Get 32Kb top of system RAM boundary
50         015540      072527      000011      ASH   #9, R5          ; Convert to # 64 byte blocks
51         015544      162705      000010      SUB   #10, R5         ; Don't use the last 512 bytes of memory
52         015550      000400              BR    7$
53         015552      26$:
54         .ENDC   ; NE, PROASM
55         .IF     NE, <PROASM-1>          ; Assemble if could be on a PDP-11
56         ;
57         ; We are not running on a Professional.

```

MEMTST -- Set up information about available memory space

```

58      ; Test each page above TSX to see where the top of memory is.
59      ;
60      MOV     #RTNKM, @#20      ; Use IOT instruction to get out of user mode
61      CLR     @#22
62      BIS     #MMENBL, @#SR0MMR; Enable memory management
63      TSTB   SR3FLG           ; Does maching have mem management reg # 3?
64      BEQ    4$
65      BIS     #EMMAP, @#SR3MMR ; Enable 22-bit extended memory
66      ;
67      ; Map user page 7 to each successive 256-word block and attempt to access.
68      ;
69      4$: MOV     #1024, R5      ; Start checking at 64Kb
70      5$: MOV     R5, @#UPAR7   ; Map user page 7 to page to be tested
71      BIS     #UMODE, @#PSW    ; Go into user mode
72      TST    @#160000         ; Can we access the page?
73      ;
74      ; Use IOT to get back into kernel mode.
75      ;
76      IOT    ; Return to kernel mode
77      BCS    6$
78      ADD    #10, R5
79      CMP    R5, #177600      ; Don't enter I/O page
80      BLO   5$
81      ;
82      ; Check for potential memory wrap (on 18-bit 256K byte computers).
83      ;
84      6$: CMP    R5, #10000    ; Is physical memory above 256K bytes
85      BLOS   7$
86      CLR    @#TSTWRD        ; Clear physical location
87      MOV    #10000, @#UPAR7 ; Map to 256K byte boundary
88      BIS    #UMODE, @#PSW    ; Go into user mode
89      MOV    #-1, @#160000+TSTWRD ; Store -1 at 256K physical location
90      IOT    ; Return to kernel mode
91      TST    @#TSTWRD        ; Test physical location
92      BEQ    7$
93      MOV    #10000, R5      ; Constrain memory to 256K byte total
94      .ENDC ; NE, <PROASM-1>
95      ;
96      ; Reached end of available memory.
97      ;
98      7$: MOV    R5, PHYMEM    ; set physical memory size
99      CMP    R5, MAPSIZ      ; Constrain kernel to user specified cutoff
100     BLOS   8$
101     MOV    MAPSIZ, R5       ; Only use this much memory
102     8$: MOV    R5, $MEMSZ    ; Set # 64-byte blocks of total memory
103     MOV    R5, FMEMHI      ; Save base 64-byte block # of top of free mem
104     ;
105     ; Turn off memory management
106     ;
107     015552 010537 000000G   ; Do we have memory management reg # 3?
108     015604 001403           ; Br if non-existent
109     015606 042737 000000G 000000G ; Disable extended memory management
110     015614 042737 000000G 000000G 9$: BIC    #MMENBL, @#SR0MMR; Turn off memory management
111     ;
112     ; If this is a Q-bus machine with >256Kb then set EXTLSI flag in ICONFG
113     ;
114     015622 023727 000134' 010000  CMP    FMEMHI, #4096. ; Does machine have at least 256Kb?

```

```

115 015630 103411          BLD      25$          ;Br if not
116 015632 105237 000000G  INCB     MEM256        ;Remember machine has at least 256kb
117 015636 123727 000000G 000000G  CMPB     VBUSTP,#QBUS   ;Is this a Q-bus machine?
118 015644 001003          BNE     25$          ;Br if not
119 015646 052737 000001 000306'  BIS     #EXTLSI,ICONFG ;Set extended-LSI flag in ICONFG
120
121          ; See if this machine needs UNIBUS mapping
122
123 015654 123727 000000G 000000G 25$:  CMPB     VBUSTP,#UNIBUS ;Is this a UNIBUS machine?
124 015662 001005          BNE     29$          ;Br if not
125 015664 105737 000000G          TSTB     MEM256        ;Does machine have at least 256kb of memory?
126 015670 001402          BEQ     29$          ;Br if not
127 015672 105237 000000G          INCB     UBUSMP        ;Say UNIBUS mapping is needed
128
129          ; Finished
130
131 015676 012637 000004          29$:  MOV     (SP)+,@#4      ;Reset trap vector
132 015702 012605          MOV     (SP)+,R5
133 015704 012604          MOV     (SP)+,R4
134 015706 012603          MOV     (SP)+,R3
135 015710 012602          MOV     (SP)+,R2
136 015712 012601          MOV     (SP)+,R1
137 015714 000207          RETURN
138
139          ; Trap - return with C-bit set.
140
141 015716 052766 000001 000002  TRCSET: BIS     #1,2(SP) ;Set c-bit for return
142 015724 000002          RTI          ;Return from trap
143
144          ; IOT - return at kernel mode with c-bit preserved.
145
146 015726 042766 000000G 000002  RTNKM: BIC     #UMODE,2(SP) ;Clear user mode - return to kernel
147 015734 000002          RTI          ;Return from trap
148
149          ; Error: System does not have memory management hardware.
150
151 015736          NOXM:  .PRINT  #TSXHD      ;PRINT ERROR MESSAGE
152 015744          .PRINT  #NXMMMSG
153 015752 000137 004250'  JMP     INISTP        ;ABORT INITIALIZATION

```

CXTALC -- Set up info about job context area

```

1          .SBTTL  CXTALC -- Set up info about job context area
2          ;-----
3          ; Set up information about the size of the job context area.
4          ;
5          ; Outputs:
6          ; CXTWDS = Number of words needed for job context area.
7          ; CXTPAG = Number of 512-byte pages needed for context area.
8          ; CXTPDR = Value to load into PDR when mapping job context area.
9          ; CXTRMN = Address of simulated RMON in context area.
10         ; RMNPDR = Value to load into PDR to map to simulated RMON.
11         ;
12 015756  CXTALC:
13         ;
14         ; Get size of base portion of job context area
15         ;
16 015756  012700  000000G      MOV      #CXTSIZ,RO      ;Get # bytes for base context area
17         ;
18         ; Bound up to 64-byte boundary and add size of simulated RMON
19         ; which is allocated above the base job context data.
20         ;
21 015762  062700  000077      ADD      #63.,RO      ;Bound up to 64 byte boundary
22 015766  042700  000077      BIC      #77,RO
23 015772  010037  000000G      MOV      RO,CXTRMN    ;Offset to start of simulated RMON
24 015776  062737  000000G 000000G  ADD      #CXTBAS,CXTRMN ;Add base virtual address of context area
25 016004  062700  000001G      ADD      #MVSIZ+1,RO  ;Add space for simulated RMON & channels
26         ;
27         ; Save number of words needed for context area
28         ;
29 016010  006200              ASR      RO          ;Convert to # words
30 016012  010037  000000G      MOV      RO,CXTWDS   ;This is # words for whole job context area
31         ;
32         ; Compute PDR value to use to map to job context area
33         ;
34 016016  062700  000037      ADD      #31.,RO      ;Bound up to # 32 word units
35 016022  072027  177773      ASH      #-5.,RO     ;Get # 32-word units for context area
36 016026  000300              SWAB     RO          ;Put # 32-word units in high-order byte
37 016030  052700  000006      BIS      #6,RO       ;Set PDR control flags
38 016034  010037  000000G      MOV      RO,CXTPDR   ;This is the PDR value
39         ;
40         ; Compute # 512-byte pages needed for job context block
41         ;
42 016040  013700  000000G      MOV      CXTWDS,RO   ;Get back # words for context area
43 016044  062700  000377      ADD      #255.,RO    ;Bound up to # 256-word blocks
44 016050  072027  177770      ASH      #-8.,RO     ;Get # 256-word pages for context area
45 016054  010037  000000G      MOV      RO,CXTPAG   ;# pages for job context area
46         ;
47         ; Set up PDR value used when mapping to simulated RMON
48         ;
49 016060  012700  000000G      MOV      #MVSIZ,RO   ;Get size of monitor vector table
50 016064  062700  000077      ADD      #63.,RO     ;Round up to # 32 word blocks
51 016070  072027  177772      ASH      #-6,RO      ;Cvt to # 32-word blocks
52 016074  005300              DEC      RO          ;Get # blocks - 1
53 016076  000300              SWAB     RO          ;Put # blocks in left byte
54 016100  052700  000006      BIS      #6,RO       ;Allow read and write access
55 016104  042700  100261      BIC      #100261,RO  ;Make sure unused PDR bits are zero
56 016110  010037  000000G      MOV      RO,RMNPDR   ;This is PDR value to map to sim. RMON
57

```

```
58           ; Finished  
59           ;  
60 016114 000207           RETURN
```

MAPALC -- Allocate memory usage table

```

1          .SBTTL  MAPALC -- Allocate memory usage table
2          ;-----
3          ; MAPALC is called to allocate a table that keeps track of which pages
4          ; of memory are currently in use by user jobs and which are free.
5          ; Each byte in the table corresponds to a 512-byte block of physical memory.
6          ; The portion of physical memory used by the system is not represented
7          ; in the memory allocation table.
8          ;
9          ; Inputs:
10         ; R5      = 64-byte block number of top of free memory area.
11         ; FMEMLO = 64-byte block number of base of free memory area.
12         ;
13         ; Outputs:
14         ; FMEMHI = 64-byte block number of top of free memory area.
15         ; MAPPAR = 64-byte block number used to map to the memory alloc table.
16         ; BASMAP = Virtual address of memory allocation table that would
17         ;           correspond to physical address 0. Note, the entries
18         ;           in the allocation table between BASMAP and LOMAP are
19         ;           actually not allocated.
20         ; LOMAP  = Virtual address of memory allocation table that corresponds
21         ;           to 1st physical 512-byte page that is available to user jobs.
22         ;           Note, LOMAP always contains 120000 because we access the
23         ;           allocation table by mapping it through PAR 5.
24         ; HIMAP  = Virtual address of memory allocation table that corresponds
25         ;           to 512-byte page above the top of the user area.
26         ;
27 016116 010246 MAPALC: MOV      R2,-(SP)
28 016120 010346      MOV      R3,-(SP)
29 016122 010446      MOV      R4,-(SP)
30         ;
31         ; Determine how many bytes will be required for the memory allocation table.
32         ; One byte in the table is required for each 512-byte physical page.
33         ;
34 016124 010503      MOV      R5,R3          ;Get 64-byte block # of top of free mem
35 016126 072327 177775 ASH      #-3,R3          ;Convert to 512-byte page #
36 016132 042703 160000 BIC      #160000,R3       ;Kill possible sign extension
37 016136 013702 000136' MOV      FMEMLO,R2       ;Get 64-byte block # of base of free memory
38 016142 062702 000007 ADD      #7,R2          ;Round up
39 016146 072227 177775 ASH      #-3,R2          ;Convert to 512-byte page #
40 016152 042702 160000 BIC      #160000,R2       ;Kill possible sign extension
41 016156 160203      SUB      R2,R3          ;Get # bytes needed for allocation table
42 016160 003440      BLE      10$          ;Br if memory overflow
43 016162 010304      MOV      R3,R4          ;Get # bytes for allocation table
44 016164 062704 001000 ADD      #512.,R4        ;Add 1 extra byte and round up to 512-byte
45 016170 072427 177767 ASH      #-9.,R4         ;Get # 512-byte units needed for alloc table
46         ;
47         ; Set up virtual address pointers for the allocation table
48         ;
49 016174 012700 000000G MOV      #VPAR5,RO       ;We will map to alloc table through PAR 5
50 016200 010037 000000G MOV      RO,LOMAP        ;Pointer to 1st entry in alloc table
51 016204 160200      SUB      R2,RO          ;Get pseudo virtual address for page # 0
52 016206 010037 000000G MOV      RO,BASMAP       ;This would point to alloc entry for page 0
53 016212 012700 000000G MOV      #VPAR5,RO       ;Get back base address of table
54 016216 060300      ADD      R3,RO          ;Add # bytes used by table
55 016220 160400      SUB      R4,RO          ;Subtract space used by table itself
56 016222 010037 000000G MOV      RO,HIMAP        ;Virtual address of 1st entry for system page
57         ;

```

MAPALC -- Allocate memory usage table

```

58          ; Allocate space for the allocation table
59          ;
60 016226 072427 000003      ASH    #3,R4      ;Get # 64-byte units for alloc table
61 016232 160405              SUB    R4,R5      ;Compute physical 64-byte base for table
62 016234 020537 000136'    CMP    R5,FMEMLO  ;Did we run out of memory space?
63 016240 101410              BLOS  10$        ;Br if memory overflow
64 016242 010537 000000G    MOV    R5,MAPPAR  ;Use this value to map PAR 5 to alloc table
65 016246 010537 000134'    MOV    R5,FMEMHI  ;Save new top of free memory area
66          ;
67          ; Finished
68          ;
69 016252 012604              MOV    (SP)+,R4
70 016254 012603              MOV    (SP)+,R3
71 016256 012602              MOV    (SP)+,R2
72 016260 000207              RETURN
73          ;
74          ; Error: Generated system is too large
75          ;
76 016262      10$: .PRINT #TSXHD      ;Print error message heading
77 016270      .PRINT #PHSOVF     ;Physical memory overflow
78 016276 000137 004250'    JMP    INISTP     ;Abort the initialization

```

SETJSZ -- Set up information about maximum job sizes

```

1          .SBTTL  SETJSZ -- Set up information about maximum job sizes
2          ;-----
3          ; SETJSZ is called to set up some information about the maximum
4          ; job sizes to be allowed. The maximum job size is chosen so that
5          ; we are guaranteed to be able to get at least one job logged on.
6          ;
7          ; Inputs:
8          ; LOMAP = Address of 1st MEMMAP entry available to user jobs.
9          ; HIMAP = Address of 1st MEMMAP entry above top of user job area.
10         ;
11         ; Outputs:
12         ; FREPGS = Total number of 512-byte pages available to user jobs.
13         ; MXJMEM = max # K bytes available to a job
14         ; DFJMEM = Default job memory size (kb)
15         ;
16 016302 010546 SETJSZ: MOV      R5,-(SP)
17         ;
18         ; Determine total number of pages of memory available to user jobs
19         ;
20 016304 013705 0000000 MOV      HIMAP,R5      ; POINTER ABOVE LAST FREE PAGE ENTRY
21 016310 163705 0000000 SUB      LOMAP,R5      ; GET TOTAL # OF FREE PAGES
22 016314 010537 0000000 MOV      R5,FREPGS     ; # FREE PAGES AVAILABLE TO USER JOBS
23         ;
24         ; Make sure there is enough free space to run TSKMON.
25         ;
26 016320 020537 0000000 CMP      R5,KMNPGS     ; COMPARE # FREE PAGES TO # PAGES FOR TSKMON
27 016324 103436          BLO      1$          ; BR IF INSUFFICIENT MEMORY TO RUN TSKMON
28         ;
29         ; Set up max memory limit for jobs
30         ;
31 016326 013700 0000000 MOV      CXTPAG,R0     ; # PAGES NEEDED FOR JOB CONTEXT AREA
32 016332 010037 0000000 MOV      R0,JCXPGS
33 016336 160005          SUB      R0,R5
34 016340 006205          ASR      R5
35 016342 020537 0000000 CMP      R5,VHIMEM     ; COMPARE WITH TSGEN SPECIFIED MAX SIZE
36 016346 101402          BLOS     10$        ; BR IF CONSTRAINED BY PHYSICAL SIZE
37 016350 013705 0000000 MOV      VHIMEM,R5     ; LIMIT BY VALUE SPECIFIED IN TSGEN
38 016354 010537 0000000 10$: MOV      R5,MXJMEM     ; MAX # K BYTES AVAILABLE TO A JOB
39 016360 010500          MOV      R5,R0
40 016362 072027 000012  ASH      #10.,R0     ; CONVERT # KB TO BYTE ADDRESS
41 016366 001002          BNE      2$
42 016370 012700 177774  MOV      #177774,R0   ; GET 64KB TOP ADDRESS
43 016374 010037 0000000 2$:  MOV      R0,MXJADR     ; ADDRESS ABOVE TOP OF JOB
44         ;
45         ; Set default memory size of jobs
46         ;
47 016400 020537 0000000 CMP      R5,VDFMEM     ; COMPARE TO DEFAULT SPECIFIED IN TSGEN
48 016404 101402          BLOS     11$        ; BR IF CONSTRAINED BY PHYSICAL LIMIT
49 016406 013705 0000000 MOV      VDFMEM,R5     ; CONSTRAIN BY TSGEN PARAMETER
50 016412 010537 0000000 11$: MOV      R5,DFJMEM     ; SET DEFAULT JOB MEMORY SIZE
51         ;
52         ; Finished
53         ;
54 016416 012605          MOV      (SP)+,R5
55 016420 000207          RETURN
56         ;
57         ; Error -- Insufficient memory space available to run TSKMON.

```

58

59 016422 004737 025332'

;

1#:

CALL

SIZERR

;Generated system is too big -- abort

SETJSZ -- Set up information about maximum job sizes

```

1          . IF      NE, <PROASM-1> ; No parity control if PRO only
2          . SBTTL   PARSET -- Setup memory parity control
3          ; -----
4          ; PARSET is called to set up memory parity control.
5          ; Currently this consists of disabling memory parity.
6          ;
7          PARSET: TST      #MPARFL      ; Does he want to disable memory parity?
8                  BNE     20#          ; Br if not
9                  MOV     R2, -(SP)
10                 MOV     @#4, -(SP)    ; Save contents of trap vector
11          ;
12          ; Catch traps that occur when we access unimplemented parity registers
13          ;
14                 MOV     #2#, @#4     ; Send traps to 2#
15                 NOP                    ; Clean out instruction pipeline
16                 NOP
17          ;
18          ; Disable parity for each block of memory
19          ;
20                 MOV     #MPARO, R2    ; Point to 1st memory control register
21          1#: BIC     #PARENL, (R2)    ; Disable memory parity
22                 BR      3#           ; We did not trap
23          2#: ADD     #4, SP           ; Clean trap PS and PC off of stack
24          3#: ADD     #2, R2          ; Point to next parity control register
25                 CMP     R2, #MPAR16  ; Have we cleared all registers?
26                 BLOS   1#           ; Loop if not
27          ;
28          ; Finished
29          ;
30                 MOV     (SP)+, @#4    ; Restore trap vector
31                 MOV     (SP)+, R2
32          20#: RETURN
33          . IFF    ; NE, <PROASM-1>
34          PARSET: RETURN
35          . ENDC ; NE, <PROASM-1>

```

34 016426 000207

GETHNL -- Load device handlers into memory

```

1          .SBTTL  GETHNL -- Load device handlers into memory
2          ;-----
3          ; GETHNL performs two functions:
4          ; 1. Set up information in the device tables about all devices.
5          ; 2. Load those handlers that reside in low memory.
6          ;
7          ; Inputs:
8          ; R5 = Address of start of free memory.
9          ;
10         ; Outputs:
11         ; R5 = Address of new start of free memory.
12         ; NMXHAN = Number of handlers to load into extended memory.
13         ;
14 016430 010146 GETHNL: MOV     R1,-(SP)
15 016432 010246         MOV     R2,-(SP)
16 016434 010446         MOV     R4,-(SP)
17         ;
18         ; Begin loop to check all handlers specified in TSGEN with DEVDEF.
19         ;
20 016436 005001         CLR     R1           ; Init device table index
21 016440 020127 000000C 1#:    CMP     R1,#CAHEND-AUTHAN> ; Done all devices?
22 016444 103015         BHS    2$           ; Br if yes
23 016446 016102 000000G         MOV     AUTHAN(R1),R2 ; Get the name of the device
24 016452 001407         BEQ    3$           ; Ignore null devices
25 016454 020227 000000G         CMP     R2,#DMYDEV       ; Is this a dummy device entry?
26 016460 001404         BEQ    3$           ; Skip it if yes
27 016462 016104 000000G         MOV     DTYPE(R1),R4    ; Get flags specified in TSGEN
28         ;
29         ; Load this handler
30         ;
31 016466 004737 016540'         CALL   LDHAND        ; Try to load handler into memory
32         ;
33         ; Check next device
34         ;
35 016472 062701 0000002 3#:    ADD     #2,R1           ; Advance device index
36 016476 000760         BR     1$           ; See if more devices to load
37         ;
38         ; Now see if there are spooled devices to contend with
39         ;
40 016500 012704 000000G 2#:    MOV     #SPLND,R4       ; Are there any spooled devices?
41 016504 001411         BEQ    9$           ; Br if not
42 016506 012701 000000G         MOV     #SPLDEV,R1      ; Point to spooled device name table
43 016512 012102 5#:    MOV     (R1)+,R2     ; Get the name of the next spooled device
44 016514 010446         MOV     R4,-(SP)        ; Save device count
45 016516 005004         CLR     R4           ; Say no TSGEN flags for device
46 016520 004737 016540'         CALL   LDHAND        ; Load the handler
47 016524 012604         MOV     (SP)+,R4      ; Recover the device count
48 016526 077407         SOB    R4,5$         ; Loop if more handlers to load
49         ;
50         ; Finished
51         ;
52 016530 012604 9#:    MOV     (SP)+,R4
53 016532 012602         MOV     (SP)+,R2
54 016534 012601         MOV     (SP)+,R1
55 016536 000207         RETURN

```

LDHAND -- Load a device handler

```

1          .SBTTL  LDHAND -- Load a device handler
2          ;-----
3          ; LDHAND sets up the device tables for a handler and loads into memory
4          ; those handlers that reside in low memory.
5          ; The device interrupt vectors are NOT set up by LDHAND.
6          ;
7          ; Inputs:
8          ; R2 = Rad-50 name of device.
9          ; R4 = TSX-Plus DX$xxx status flags for device from TSGEN.
10         ; R5 = Address where handler is to be loaded.
11         ;
12         ; Outputs:
13         ; R5 = New free memory address.
14         ; NUMDEV = Incremented by 2.
15         ; PNAME(i) = Rad-50 name of device.
16         ; ENTRY(i) = Handler entry point.
17         ; DVSTAT(i) = Device status flags.
18         ; DVFLAG(i) = TSX-Plus device status flags.
19         ; HANPAR(i) = PAR offset if this is a mapped handler.
20         ;
21 016540 010446 LDHAND: MOV      R4, -(SP)
22         ;
23         ; Determine if we should ignore this device
24         ;
25 016542 004737 016710' CALL     INSCK1      ;Should we ignore this device?
26 016546 103456          BCS     9$          ;Br if yes
27         ;
28         ; The initial tests indicate that this handler should be loaded.
29         ; Now open the handler file and perform some additional checks.
30         ;
31 016550 004737 017066' CALL     INSCK2      ;Perform some additional checks on handler
32 016554 103450          BCS     8$          ;Br if we should not load this device
33         ;
34         ; At this point channel 1 is open to the handler file and block 0
35         ; of the handler is in WRKBUF.
36         ; Set up information tables for this device.
37         ;
38 016556 004737 017500' CALL     STDVTB      ;Set up info in tables for this device
39         ;
40         ; Determine if this handler is to be loaded into low memory or
41         ; extended memory.
42         ;
43 016562 016400 0000000 MOV     DVFLAG(R4),R0 ;Get TSX-Plus status flags for device
44 016566 032700 0000000 BIT     #DX$NHM,R0   ;Are we never to map this handler?
45 016572 001035          BNE     1$          ;Br if handler cannot be mapped
46 016574 032700 0000000 BIT     #DX$MPH,R0   ;Is mapping wanted for this handler?
47 016600 001432          BEQ     1$          ;Br if not
48 016602 032700 0000000 BIT     #DX$IBH,R0   ;Does this handler have an internal I/O buff?
49 016606 001412          BEQ     2$          ;Br if not
50 016610 105737 0000000 TSTB   UBUSMP        ;Does this machine have a mapped UNIBUS?
51 016614 001024          BNE     1$          ;Br if yes -- Don't map this handler
52 016616 032700 0000000 BIT     #DX$MAP,R0   ;Does this handler require I/O mapping?
53 016622 001404          BEQ     2$          ;Br if not
54 016624 032737 000001 000306' BIT     #EXTLSI,ICONFG ;Is this a Q-bus system with more than 256Kb?
55 016632 001015          BNE     1$          ;Br if yes -- Don't map this handler
56         ;
57         ; This handler can be mapped and will be loaded in extended memory

```

LDHAND -- Load a device handler

```

58 ;
59 016634 005237 000124' 2#: INC NMXHAN ;Count # of mapped handlers
60 016640 012764 000001 000000G MOV #1,HANPAR(R4) ;Set flag saying handler should be mapped
61 ;
62 ; Make sure size of mapped handler does not exceed 8KB
63 ;
64 016646 026427 000000G 020000 CMP HANSIZ(R4),#8192. ;Is mapped handler too big?
65 016654 101410 BLOS B# ;Br if not too big
66 016656 012700 002452' MOV #HN2BIG,R0 ;Get error message address
67 016662 000137 020566' JMP HLERR ;Abort initialization
68 ;
69 ; This handler must be loaded into low memory
70 ;
71 016666 005064 000000G 1#: CLR HANPAR(R4) ;Say this handler is not mapped
72 016672 004737 017610' CALL LDHNLO ;Load handler into low memory
73 ;
74 ; Close the handler file
75 ;
76 016676 B#: .CLOSE #1 ;Close the handler file
77 ;
78 ; Finished
79 ;
80 016704 012604 9#: MOV (SP)+,R4
81 016706 000207 RETURN
    
```

INSCK1 -- Determine if a handler should be installed

```

1          .SBTTL  INSCK1 -- Determine if a handler should be installed
2          ;-----
3          ;  INSCK1 is called to determe if a certain device handler should be
4          ;  loaded.
5          ;
6          ;  Inputs:
7          ;  R2 = Rad50 name of the device.
8          ;  R4 = Initial DX$xxx flags as specified in TSGEN.
9          ;
10         ;  Outputs:
11         ;  C-flag cleared ==> Load the handler.
12         ;  C-flag set      ==> Do not load the handler.
13         ;  R2 = Device name with unit number removed.
14         ;  R4 = DX$xxx combined with default flags for the device.
15         ;
16 016710 010146  INSCK1: MOV      R1, -(SP)
17         ;
18         ;  Strip off any specified unit number
19         ;
20 016712 010201          MOV      R2, R1          ; Get full device name
21 016714 005000          CLR      R0              ; Set for divide
22 016716 071027 000050  DIV      #50, R0          ; Split off last digit
23 016722 070027 000050  MUL      #50, R0          ; Now correct for divide
24 016726 010102          MOV      R1, R2          ; Get device name less 3rd digit
25 016730 010237 000146'  MOV      R2, CURNAM        ; Set name of handler being loaded
26         ;
27         ;  See if this is a device such as DK, SY, or TT which we don't
28         ;  need to load as a device handler.
29         ;
30 016734 020237 000176'  CMP      R2, R5OLD        ; Is device name LD?
31 016740 001004          BNE      1$              ; Br if not
32 016742 105737 000000G  TSTB   VLDSYS           ; Is standard system LD support included?
33 016746 001044          BNE      5$              ; Br if yes -- Don't load LD
34 016750 000417          BR       3$              ; Load LD
35 016752 012701 000224'  1$: MOV      #SKPDEV, R1    ; Point to table of devices to skip
36 016756 020221          2$: CMP      R2, (R1)+      ; Is this a device to be skipped?
37 016760 001437          BEQ      5$              ; Br if yes
38 016762 005711          TST      (R1)           ; Reached end of skip list?
39 016764 001374          BNE      2$              ; Loop if not
40         ;
41         ;  See if we have already loaded the handler for this device
42         ;
43 016766 013701 000000G  MOV      NUMDEV, R1      ; Get index for last device
44 016772 001406          BEQ      3$              ; Br if no devices installed yet
45 016774 020261 000000G  4$: CMP      R2, PNAME(R1) ; See if this device is already installed
46 017000 001427          BEQ      5$              ; Br if already installed
47 017002 162701 000002  SUB      #2, R1          ; More installed devices to check?
48 017006 003372          BGT      4$              ; Loop if yes
49         ;
50         ;  This handler is to be loaded.
51         ;  Get default TSX-Plus control flags for this device.
52         ;
53 017010 012701 000342'  3$: MOV      #DVFLBS, R1   ; Point to start of table
54 017014 020261 000000  6$: CMP      R2, DV$NAM(R1) ; Search for device in the table
55 017020 001003          BNE      7$              ; Br if this is not it
56 017022 056104 000002  BIS      DV$FLG(R1), R4   ; Combine default flags
57 017026 000405          BR       8$

```

INSCK1 -- Determine if a handler should be installed

```

58 017030 062701 000004      7$:   ADD     #DV##SZ,R1      ;Point to next entry
59 017034 020127 000522'    CMP     R1,#DVFLND      ;Checked all entries?
60 017040 103765              BLD     6$              ;Loop if not
61                               ;
62                               ;   If this is a DMA device, set flag saying buffers must be on
63                               ;   even byte boundaries.
64                               ;
65 017042 032704 000000G    8$:   BIT     #DX$DMA,R4      ;Is this a DMA device?
66 017046 001402              BEQ     10$              ;Br if not
67 017050 052704 000000G    BIS     #DX$EBA,R4      ;Set even-buffer-boundary flag
68                               ;
69                               ;   Load this handler
70                               ;
71 017054 000241            10$:  CLC                    ;Set flag saying to load the handler
72 017056 000401              BR      9$
73                               ;
74                               ;   Do not load this handler
75                               ;
76 017060 000261            5$:   SEC                    ;Set flag saying not to load the handler
77                               ;
78                               ;   Finished
79                               ;
80 017062 012601            9$:   MOV     (SP)+,R1
81 017064 000207              RETURN

```

INSCK2 -- Additional checking for handler installation

```

1          .SBTTL  INSCK2 -- Additional checking for handler installation
2          ;-----
3          ;  INSCK2 is called to determine if a device handler should be installed.
4          ;
5          ;  Inputs:
6          ;  R2 = Rad50 device name (without unit number).
7          ;
8          ;  Outputs:
9          ;  C-flag cleared ==> Load this handler.
10         ;  C-flag set      ==> Do not load this handler.
11         ;  If the handler is to be loaded, its block 0 is in WRKBUF and channel
12         ;  number 1 is opened to the handler file.
13         ;
14 017066 010146  INSCK2: MOV     R1,-(SP)
15 017070 010246          MOV     R2,-(SP)
16 017072 010346          MOV     R3,-(SP)
17 017074 010446          MOV     R4,-(SP)
18 017076 010546          MOV     R5,-(SP)
19 017100 013746 000004    MOV     @#4,-(SP)      ;Save the bus timeout vector
20 017104 013746 000010    MOV     @#10,-(SP)   ;Save illegal instruction vector
21         ;
22         ;  Try to lookup handler file on system disk
23         ;
24 017110 010237 000246'   MOV     R2,HANNAM+2   ;Set the device name for the lookup
25 017114          .LOOKUP #AREA,#1,#HANNAM;Try to open the handler file
26 017134 103011          BCC     1$            ;Br if we found the handler file
27         ;
28         ;  Error -- Cannot find handler file
29         ;
30 017136 105737 000000G   TSTB   VINABT        ;Abort or continue on errors?
31 017142 001002          BNE     2$            ;Br if abort on errors
32 017144 000137 017446'   JMP     10$          ;Say not to load this handler
33 017150 012700 001636'  2$:    MOV     #CFHMSG,RO ;Can't find handler
34 017154 000137 020566'   JMP     HLERR        ;Abort initialization
35         ;
36         ;  We were able to open the handler file.
37         ;  Read in block 0 of handler.
38         ;
39 017160          1$:    .READW  #AREA,#1,WRKBUF,#256.,#0 ;Read block 0 into WRKBUF
40 017216 103004          BCC     3$            ;Br if read ok
41 017220 012700 001700'   MOV     #ERHMSG,RO   ;Error during read
42 017224 000137 020566'   JMP     HLERR
43         ;
44         ;  Determine if the handler is supported under the current RT-11 version
45         ;
46 017230 012700 000330'  3$:    MOV     #HVTBL,RO   ;Point to table with handler version info
47 017234 013701 000152'   MOV     WRKBUF,R1    ;Point to buffer with block 0 of handler
48 017240 116101 000000G   MOVB   H.DSTS(R1),R1 ;Get device ID code from handler
49 017244 120160 000000    51$:   CMPB   R1,HV$ID(RO)  ;Compare handler ID code with table entry
50 017250 001020          BNE     53$          ;Br if this entry not for this handler
51 017252 123760 000000G 000001    CMPB   SYSVER,HV$VER(RO);Compare curr RT-11 vers with min acceptable
52 017260 103405          BLD     52$          ;Br if version is not adequate
53 017262 101020          BHI     54$          ;Br if version is ok
54 017264 123760 000000G 000002    CMPB   SYSUPD,HV$UPD(RO);Compare update level
55 017272 103014          BHIS   54$          ;Br if update level is ok
56 017274 105737 000000G  52$:   TSTB   VINABT        ;Handler not loadable - abort or continue?
57 017300 001462          BEQ     10$          ;Br if continue but don't allow loading

```

INSCK2 -- Additional checking for handler installation

```

58 017302 012700 001744'          MOV    #ERHNDV,RO    ;Wrong version of RT for handler
59 017306 000137 020566'          JMP    HLERR        ;Report error and abort
60 017312 062700 000003          53$:  ADD    #HV#$SZ,RO    ;Point to next handler version table entry
61 017316 020027 000341'          CMP    RO,#HVEND    ;Are there more entries?
62 017322 103750                   BLD    51$          ;Loop if more to check
63                                ;
64                                ; Check handler sysgen options
65                                ;
66 017324 013700 000152'          54$:  MOV    WRKBUF,RO    ;Point to buffer with handler block 0
67 017330 032760 000000G 000000G BIT    #SG$MMU,H.GEN(RO);Was handler genned with XM support?
68 017336 001004                   BNE    4$           ;Br if yes
69 017340 012700 002045'          MOV    #HSGER,RO    ;Error if not XM version of handler
70 017344 000137 020566'          JMP    HLERR
71 017350 016037 000000G 000304' 4$:  MOV    H.GEN(RO),HGENFL;Save handler sysgen flags for later
72                                ;
73                                ; Check the CSR address specified in the handler to see if the
74                                ; hardware device for this handler exists.
75                                ;
76 017356 012737 015716' 000004    MOV    #TRCSET,@#4    ;Catch bus timeout traps
77 017364 012737 015716' 000010    MOV    #TRCSET,@#10   ;Catch illegal instruction traps
78 017372 013700 000152'          MOV    WRKBUF,RO    ;Point to start of block 0 of handler
79 017376 016001 000000G          MOV    H.CSR(RO),R1  ;Get address of CSR for device
80 017402 001402                   BEQ    5$           ;Br if no CSR specified
81 017404 005711                   TST    (R1)         ;Is CSR accessible?
82 017406 103422                   BCS    13$          ;Br if trap occurred while accessing CSR
83                                ;
84                                ; Execute the device installation code.
85                                ; The installation code will set the C-flag if the handler should
86                                ; not be loaded.
87                                ;
88 017410 062700 000000G          5$:  ADD    #H.INS,RO    ;Offset 200 in block 0
89 017414 005710                   TST    @RO          ;Does any installation code exist?
90 017416 001415                   BEQ    11$          ;Br if no driver installation code
91 017420 013746 000000G          MOV    @#RMON,-(SP)  ;Save RT-11 RMON pointer
92 017424 012737 000000G 000000G MOV    #MONVEC,@#RMON ;Set TSX-Plus RMON pointer
93 017432 013703 000000G          MOV    RPRVEC,R3    ;Get pointer to Pro vec addr routine
94 017436 004710                   CALL   @RO          ;Call the installation code
95 017440 012637 000000G          MOV    (SP)+,@#RMON ;Restore RT-11 RMON pointer
96 017444 000403                   BR     13$          ;C-flag now indicates handler load status
97                                ;
98                                ; Finished with installation verification.
99                                ;
100 017446 000261          10$:  SEC                    ;Set c-bit for indicating handler
101 017450 000401          BR     13$          ;should not be installed
102 017452 000241          11$:  CLC                    ;Clear the c-bit for driver installation
103 017454 012637 000010          13$:  MOV    (SP)+,@#10 ;Restore illegal instruction vector
104 017460 012637 000004          MOV    (SP)+,@#4    ;Restore the bus timeout vector
105 017464 012605          MOV    (SP)+,R5
106 017466 012604          MOV    (SP)+,R4
107 017470 012603          MOV    (SP)+,R3
108 017472 012602          MOV    (SP)+,R2
109 017474 012601          MOV    (SP)+,R1
110 017476 000207          RETURN

```

STDVTB -- Set up device table entries for a device

```

1          .SBTTL  STDVTB -- Set up device table entries for a device
2          ;-----
3          ; STDVTB is called to set up device table entries for a device whose
4          ; handler is being loaded.
5          ;
6          ; Inputs:
7          ; R2 = Rad50 name of device (less unit number).
8          ; R4 = DX$xxx device flags for DVFLAG table.
9          ; Block 0 of the handler must be in WRKBUF.
10         ;
11         ; Outputs:
12         ; R4 = Device table index number for this device.
13         ; NUMDEV = Incremented by 2.
14         ; PNAME(i) = Rad50 name of the device.
15         ; DVSTAT(i) = Device status flags.
16         ; DVFLAG(i) = TSX-Plus control flags.
17         ; HANSIZ(i) = Size of handler (bytes).
18         ; DEVSIZ(i) = Size of device (blocks).
19         ;
20 017500  STDVTB:
21         ;
22         ; Increment device counter
23         ;
24 017500  062737  000002  000000G      ADD      #2,NUMDEV      ; Say another device added to tables
25 017506  013700  000000G      MOV      NUMDEV,R0      ; Get device index number
26         ;
27         ; Set up PNAME and DVFLAG.
28         ;
29 017512  010260  000000G      MOV      R2,PNAME(R0)   ; Set permanent device name
30 017516  010460  000000G      MOV      R4,DVFLAG(R0) ; Set up TSX-Plus control flags for the device
31         ;
32         ; Set HANDSK entry to 1.
33         ; This entry is supposed to hold the absolute block number on the disk where
34         ; block 1 of the handler is located. We set to 1 because all I/O we do
35         ; on behalf of the handler is relative to the base of the handler rather than
36         ; relative to the start of the disk.
37         ;
38 017522  012760  000001  000000G      MOV      #1,HANDSK(R0) ; Set block # of block 1 of handler file
39         ;
40         ; Extract parameters from handler block 0
41         ;
42 017530  010004      MOV      R0,R4      ; Carry device index in R4
43 017532  013700  000152'      MOV      WRKBUF,R0    ; Point to block 0 of handler
44 017536  016064  000000G 000000G      MOV      H.SIZ(R0),HANSIZ(R4) ; Set handler size
45 017544  016064  000000G 000000G      MOV      H.DVSZ(R0),DEVSIZ(R4) ; Number of blocks on device
46 017552  016064  000000G 000000G      MOV      H.DSTS(R0),DVSTAT(R4) ; Set device status flags
47         ;
48         ; Disable MOUNTs and data caching for certain devices
49         ;
50 017560  032764  000000G 000000G      BIT      #DS$DIR,DVSTAT(R4) ; Is this a directory structured device?
51 017566  001404      BEQ      1$          ; Br if not -- No mounts allowed
52 017570  032764  000000G 000000G      BIT      #DS$NRD,DVSTAT(R4) ; Non RT-11 directory structure (mag tape)?
53 017576  001403      BEQ      9$          ; Br if not
54 017600  052764  000000C 000000G 1$:  BIS      #<DX$NMT!DX$NCA>,DVFLAG(R4) ; Disable mounts and data caching
55         ;
56         ; Finished
57         ;

```

TSINIT -- TSX startup initializ MACRO V05.04 Thursday 17-Dec-87 08:39 Page 54-1  
STDVTB -- Set up device table entries for a device

58 017606 000207

9#: RETURN

LDHNL0 -- Load device handler into low memory

```

1          .SBTTL  LDHNL0 -- Load device handler into low memory
2          ;-----
3          ; LDHNL0 is called to load a device handler into low memory.
4          ;
5          ; Inputs:
6          ;   R4 = Device index number.
7          ;   R5 = Address of start of free memory area.
8          ;
9          ; Outputs:
10         ;   R5 = Address of new start of free memory area.
11         ;
12 017610 010346 LDHNL0: MOV      R3,-(SP)
13         ;
14         ; Determine if we have enough free memory space to read the handler
15         ;
16 017612 005064 000000G CLR      HANPAR(R4)      ; Say this handler is not mapped
17 017616 010500      MOV      R5,R0      ; Get current top of memory address
18 017620 016403 000000G MOV      HANSIZ(R4),R3   ; Get size of handler
19 017624 060300      ADD      R3,R0      ; Get address above top of handler
20 017626 004737 025312' CALL     CHKMEM         ; See if handler will fit in memory
21         ;
22         ; Handler will fit. Read it into memory.
23         ;
24 017632 006203      ASR      R3          ; Get number of words to read
25 017634      . READW  #AREA,#1,R5,R3,#1
26 017670 103004      BCC      1$          ; Br if read ok
27 017672 012700 001700' MOV      #ERHMSG,R0     ; Error reading handler
28 017676 000137 020566' JMP      HLERR         ; Abort initialization
29         ;
30         ; Set address of handler entry point and compute address beyond
31         ; end of the handler.
32         ;
33 017702 010564 000000G 1$: MOV      R5,HANENT(R4)   ; Set address of handler entry point
34 017706 062764 000006 000000G ADD      #6,HANENT(R4)  ; (Point to fourth word of handler)
35 017714 006303      ASL      R3          ; Convert handler size to bytes
36 017716 060305      ADD      R3,R5          ; Point beyond end of handler
37         ;
38         ; Set up table of addresses of support routines at end of handler.
39         ;
40 017720 010503      MOV      R5,R3          ; Get address past end of handler
41 017722 004737 020474' CALL     STHNPV        ; Set up pointer vector in handler
42         ;
43         ; If handler has any load-time execution code, run it now
44         ;
45 017726 004737 020624' CALL     DOHNL0        ; Run any load-time code for handler
46         ;
47         ; Finished
48         ;
49 017732 012603      MOV      (SP)+,R3
50 017734 000207      RETURN

```

GETHNNH -- Load handlers into extended memory

```

1          .SBTTL  GETHNNH -- Load handlers into extended memory
2          ;-----
3          ; GETHNNH is called to load those handlers that can be placed in extended
4          ; memory.  The status tables for these devices have already been set up
5          ; by GETHNL.
6          ;
7          ; Inputs:
8          ;   R5 = 64-byte block number of top of free memory area.
9          ;
10         ; Outputs:
11         ;   R5 = 64-byte block number of new top of free memory area.
12         ;
13 017736 010446  GETHNNH: MOV      R4, -(SP)
14         ;
15         ; Begin looking for handlers that are to be loaded into extended memory.
16         ; GETHNL stored a non-zero (but meaningless) value in the HANPAR entry
17         ; for each handler that is to be mapped.
18         ;
19 017740 012704 000002      MOV      #2, R4          ;Get index for first device entry
20         ;
21         ; See if this device has a mapped handler
22         ;
23 017744 005764 000000G 1$:   TST      HANPAR(R4)      ;Is this handler mapped?
24 017750 001402          BEQ      2$              ;Br if not
25         ;
26         ; We found an entry for a device with a mapped handler.
27         ; Load the handler.
28         ;
29 017752 004737 017774'   CALL     LDHNNHI          ;Load a mapped handler
30         ;
31         ; Look for more mapped handlers
32         ;
33 017756 062704 000002 2$:   ADD      #2, R4          ;Increment device index
34 017762 020437 000000G   CMP      R4, NUMDEV      ;Checked all devices?
35 017766 101766          BLOS    1$              ;Loop if not
36         ;
37         ; Finished
38         ;
39 017770 012604          MOV      (SP)+, R4
40 017772 000207          RETURN

```

LDHNHI -- Load device handler into extended memory

```

1          .SBTTL  LDHNHI -- Load device handler into extended memory
2          ;-----
3          ; LDHNHI is called to load a device handler into extended memory.
4          ;
5          ; Inputs:
6          ; R4 = Device index number.
7          ; R5 = 64-byte block number of top of free memory area.
8          ;
9          ; Outputs:
10         ; R5 = 64-byte block number of new top of free memory area.
11         ;
12 017774 010146 LDHNHI: MOV     R1,-(SP)
13 017776 010246         MOV     R2,-(SP)
14 020000 010346         MOV     R3,-(SP)
15 020002 010446         MOV     R4,-(SP)
16         ;
17         ; Open channel 1 to the handler file.
18         ;
19 020004 016437 000000G 000246'      MOV     PNAME(R4),HANNAM+2 ;Set the device name for the lookup
20 020012 016437 000000G 000146'      MOV     PNAME(R4),CURNAM;Set name in case we have an error
21 020020         .LOOKUP #AREA,#1,#HANNAM;Try to open the handler file
22 020040 103004         BCC     B$                ;Br if we found the handler file
23 020042 012700 001636'      MOV     #CFHMSG,R0        ;Can't find handler
24 020046 000137 020566'      JMP     HLERR            ;Abort initialization
25         ;
26         ; Read block 0 of the handler file and extract some information
27         ;
28 020052 013702 000152'      B$:    MOV     WRKBUF,R2        ;Get address of work buffer
29 020056         .READW #AREA,#1,R2,#256.,#0 ;Read block 0 of handler
30 020112 016237 000000G 000304'      MOV     H.GEN(R2),HGENFL;Save handler sysgen flags
31         ;
32         ; Set virtual address of handler entry point
33         ;
34 020120 012764 000006G 000000G      MOV     #VPAR5+6,HANENT(R4) ;Set virtual addr of handler entry point
35         ;
36         ; Get information about the size of the handler and determine the
37         ; address in extended memory where the handler is to be loaded.
38         ;
39 020126 016402 000000G      MOV     HANSIZ(R4),R2      ;Get size of handler (bytes)
40 020132 005202         INC     R2                ;Make sure handler size is even
41 020134 042702 000001      BIC     #1,R2
42 020140 010200         MOV     R2,R0
43 020142 062700 000077      ADD     #63.,R0          ;Round up to # 64-byte blocks
44 020146 072027 177772      ASH     #-6,R0          ;Get # 64-byte blocks for handler
45 020152 060037 000000G      ADD     R0,MHNSIZ       ;Accumulate total space for mapped handlers
46 020156 160005         SUB     R0,R5           ;Reserve room for handler
47 020160 010564 000000G      MOV     R5,HANPAR(R4)   ;Set mapping value for handler
48 020164 010537 000126'      MOV     R5,HMAP        ;Set initial PAR base for handler
49 020170 012737 000001 000142'      MOV     #1,FILBLK      ;Set # of block to read from file
50         ;
51         ; Begin loop to read handler into memory
52         ;
53 020176 010203         1$:    MOV     R2,R3                ;Get remaining size of handler
54 020200 020327 001000      CMP     R3,#512.       ;Compare with max we can read at one time
55 020204 101402         BLOS   2$                ;Br if we can read remainder of handler
56 020206 012703 001000      MOV     #512.,R3       ;Read one block
57 020212 160302         2$:    SUB     R3,R2                ;Reduce amt of handler left to read

```

LDHNI -- Load device handler into extended memory

```

58 ;
59 ; Read next block of handler
60 ;
61 020214 006203 ASR R3 ;Get # words to read
62 020216 013701 000152' MOV WRKBUF,R1 ;Get address of buffer for read
63 020222 . READW #AREA,#1,R1,R3,FILBLK ;Read a block
64 020256 103004 BCC 3$ ;Br if read ok
65 020260 012700 001700' MOV #ERHMSG,R0 ;Get error message
66 020264 000137 020566' JMP HLERR ;Abort initialization
67 ;
68 ; Move the code we just read into the XM area for the handler
69 ;
70 020270 012700 000000G 3$: MOV #VPAR5,R0 ;Get virtual address of mapped region
71 020274 DISABL ;** Disable interrupts **
72 020302 013746 000000G MOV @#KPAR5,-(SP) ;;;Save current mapping of PAR 5
73 020306 013737 000126' 000000G MOV HMAP,@#KPAR5 ;;;Set up mapping to get to XM area
74 020314 052737 000000G 000000G BIS #MMENBL,@#SR0MMR ;;;Enable memory management
75 020322 105737 000000G TSTB MEM256 ;;;Does machine have > 256KB?
76 020326 001403 BEQ 4$ ;;;Br if not
77 020330 052737 000000G 000000G BIS #EMMAP,@#SR3MMR ;;;Enable extended memory addressing
78 020336 012120 4$: MOV (R1)+,(R0)+ ;;;Move from WRKBUF to XM region
79 020340 077302 SOB R3,4$ ;;;Loop till all moved
80 020342 105737 000000G TSTB MEM256 ;;;Does machine have > 256KB?
81 020346 001403 BEQ 5$ ;;;Br if not
82 020350 042737 000000G 000000G BIC #EMMAP,@#SR3MMR ;;;Disable extended memory addressing
83 020356 042737 000000G 000000G 5$: BIC #MMENBL,@#SR0MMR ;;;Enable memory management
84 020364 012637 000000G MOV (SP)+,@#KPAR5 ;;;Replace PAR 5 mapping
85 020370 ENABL ; ** Enable interrupts **
86 ;
87 ; See if there is more to read
88 ;
89 020376 062737 000010 000126' ADD #8.,HMAP ;Increase XM region base
90 020404 005237 000142' INC FILBLK ;Increment file block number
91 020410 005702 TST R2 ;Is there more to read?
92 020412 001271 BNE 1$ ;Loop if more to read
93 ;
94 ; We have finished moving the handler into its XM region.
95 ; Set up addresses of system routines in a vector at the end of the handler
96 ;
97 020414 012703 000000G MOV #VPAR5,R3 ;Get virtual address of handler base
98 020420 066403 000000G ADD HANSIZ(R4),R3 ;Get virtual address beyond end of handler
99 020424 004737 021324' CALL HANMAP ;;;Map KPAR5 to the handler
100 020430 004737 020474' CALL STHNPV ;;;Set up handler pointer vector
101 020434 004737 021400' CALL HANUMP ;Restore mapping
102 ;
103 ; If handler has any load-time code, run it now
104 ;
105 020440 010537 000134' MOV R5,FMEMHI ;Set addr of top of free memory area
106 020444 004737 020624' CALL DOHNLG ;Run any load-time code for handler
107 020450 013705 000134' MOV FMEMHI,R5 ;Get new top of free memory address
108 ;
109 ; Close the handler file
110 ;
111 020454 . CLOSE #1 ;Close the handler file
112 ;
113 ; Finished
114 ;

```

115	020462	012604	MOV	(SP)+, R4
116	020464	012603	MOV	(SP)+, R3
117	020466	012602	MOV	(SP)+, R2
118	020470	012601	MOV	(SP)+, R1
119	020472	000207	RETURN	

STHNPV -- Initialize pointer vector in a handler

```

1          .SBTTL  STHNPV -- Initialize pointer vector in a handler
2          ;-----
3          ; STHNPV is called to initialize the pointer vector at the end of a
4          ; handler which provides the addresses of various system routines to the
5          ; handler.
6          ;
7          ; Inputs:
8          ; R3 = Address beyond the end of the handler.
9          ; HGENFL = Sysgen option flags for the handler being loaded.
10         ;
11 020474 010346 STHNPV: MOV      R3,-(SP)
12         ;
13         ; Set up addresses in the pointer vector
14         ;
15 020476 012743 000000G      MOV      #FORK,-(R3)      ;Address of fork routine
16 020502 012743 000000G      MOV      #INTEN,-(R3)     ;Address of inten routine
17 020506 032737 000000G 000304' BIT      #SG#IOT,HGENFL ;Does handler want timeout support?
18 020514 001402              BEQ      2$              ;Br if not
19 020516 012743 000000G      MOV      #IOTIMR,-(R3)    ;Set address of timeout support routine
20 020522 032737 000000G 000304' 2$: BIT      #SG#ELG,HGENFL ;Does handler want error logging support?
21 020530 001402              BEQ      3$              ;Br if not
22 020532 012743 000000G      MOV      #ERRLOG,-(R3)   ;Set address of error logging routine
23 020536 012743 000000G      3$: MOV      #PTWRD,-(R3)
24 020542 012743 000000G      MOV      #PTBYT,-(R3)
25 020546 012743 000000G      MOV      #GTBYT,-(R3)
26 020552 012743 000000G      MOV      #MPPHY,-(R3)
27 020556 012743 000000G      MOV      #RELOC,-(R3)
28         ;
29         ; Finished
30         ;
31 020562 012603              MOV      (SP)+,R3
32 020564 000207              RETURN

```

```
1 ;  
2 ; Error occured while loading device handler.  
3 ; RO = error message address; CURNAM = device name.  
4 ;  
5 020566 010001 HLERR: MOV RO,R1 ;SAVE ERROR MESSAGE ADDRESS  
6 020570 .PRINT #TSXHD ;PRINT ERROR MESSAGE HEADING  
7 020576 .PRINT R1 ;PRINT ERROR MESSAGE  
8 020602 013700 000146' MOV CURNAM,RO ;GET RAD50 DEVICE NAME  
9 020606 004737 025502' CALL PRTR50 ;PRINT DEVICE NAME  
10 020612 .PRINT #CRLF  
11 020620 000137 004250' JMP INISTP ;ABORT INITIALIZATION
```

DOHNLC -- Execute and handler load/fetch code

```

1          .SBTTL DOHNLC -- Execute and handler load/fetch code
2          ;-----
3          ; If the handler being loaded has any Load-time execution code, read it
4          ; into our work buffer and execute it now.
5          ;
6          ; Inputs:
7          ; R4 = Device index number of handler that is being loaded.
8          ;
9          ; Outputs:
10         ; C-flag is set on return if load code signals an error during its
11         ; execution.
12         ;
13 020624 010146 DOHNLC: MOV R1,-(SP)
14 020626 010246      MOV R2,-(SP)
15 020630 010346      MOV R3,-(SP)
16 020632 010446      MOV R4,-(SP)
17 020634 010546      MOV R5,-(SP)
18         ;
19         ; Examine 1st word of handler to see if it could have any load-time code.
20         ;
21 020636 016405 0000006 MOV HANENT(R4),R5 ;Get address of handler entry point
22 020642 004737 021324' CALL HANMAP ;;;Map Kpar5 to handler if mapped handler
23 020646 016500 0000004 MOV 4(R5),R0 ;;;Get 1st instruction located at 4 in handler
24 020652 004737 021400' CALL HANUMP ;Restore normal mapping
25 020656 020027 000240 CMP R0,#240 ;Is it a NOP?
26 020662 103516 BLO 7$ ;Br if can't be any load code
27 020664 020027 000277 CMP R0,#277 ;
28 020670 101113 BHI 7$ ;Br if can't be any load code
29 020672 132700 0000004 BITB #4,R0 ;Is there load code?
30 020676 001510 BEQ 7$ ;Br if not
31         ;
32         ; Handler may have load code.
33         ; Read block 0 of handler and get offset to load code.
34         ;
35 020700 013702 000152' MOV WRKBUF,R2 ;Get addr of our work buffer
36 020704 . READW #AREA,#1,R2,#256.,#0 ;Read block 0 of handler
37 020740 022227 031066 CMP (R2)+,#^RHAN ;Is this a new type handler?
38 020744 001065 BNE 7$ ;Br if not
39 020746 016203 0000004 MOV 4(R2),R3 ;Get offset to load code
40 020752 001462 BEQ 7$ ;Br if there is none
41         ;
42         ; There is load-time code.
43         ; Read into WRKBUF the portion of the handler with the load code.
44         ;
45 020754 020327 001000 CMP R3,#1000 ;Is load code in block 0 of handler?
46 020760 103424 BLO 1$ ;Br if yes
47 020762 010302 MOV R3,R2 ;Get offset to start of load code
48 020764 072227 177767 ASH #-9.,R2 ;Convert to a block number
49 020770 042702 177400 BIC #^C377,R2 ;Clear all but block number
50 020774 . READW #AREA,#1,WRKBUF,#512.,R2 ;Read 2 blocks from handler file
51         ;
52         ; The load code is now in WRKBUF. Set up and execute it.
53         ;
54 021032 010437 000144' 1$: MOV R4,CURDEV ;Save current device index number
55 021036 042703 177000 BIC #^C777,R3 ;Get offset within block of load code entry pt
56 021042 010300 MOV R3,R0 ;Get entry point offset
57 021044 063700 000152' ADD WRKBUF,R0 ;Add base address

```

DOHNLC -- Execute and handler load/fetch code

```

58 021050 013701 000000G      MOV      RPRVEC,R1      ;Get pointer to GETVEC routine for Pro
59 021054 012702 000000C      MOV      #MAXDEV*2,R2   ;Get 2*# entries in device tables
60 021060 012703 0000004      MOV      #4,R3         ;Set code saying this is load code
61 021064 012705 000000G      MOV      #HANENT,R5     ;Point to handler entry address vector
62 021070 060405              ADD      R4,R5         ;Point to entry cell for this handler
63 021072 004737 021324'      CALL    HANMAP         ;;;Map Kpar5 to handler if it is a mapped
64 021076 012704 021154'      MOV      #LDREAD,R4    ;;;Get pointer to Read routine
65 021102 004710              CALL    (R0)          ;;;Execute the load code
66 021104 103407              BCS     2$            ;;;Br if handler load code signaled an error
67                          ;
68                          ; Fetch/load code ran ok.
69                          ; Turn off handler mapping.
70                          ;
71 021106 012737 001400 000000G      MOV      #1400,@#KPAR6  ;;;Restore original mapping for RT-11
72 021114 004737 021400'      CALL    HANUMP         ;Unmap the handler
73 021120 000241              7$: CLC                ;Clear the carry flag for return
74 021122 000406              BR      9$
75                          ;
76                          ; Error occurred in fetch/load code
77                          ;
78 021124 012737 001400 000000G 2$: MOV      #1400,@#KPAR6  ;;;Restore original mapping for RT-11
79 021132 004737 021400'      CALL    HANUMP         ;Unmap the handler
80 021136 000261              SEC                ;Set the carry flag for return
81                          ;
82                          ; Finished
83                          ;
84 021140 012605              9$: MOV      (SP)+,R5
85 021142 012604              MOV      (SP)+,R4
86 021144 012603              MOV      (SP)+,R3
87 021146 012602              MOV      (SP)+,R2
88 021150 012601              MOV      (SP)+,R1
89 021152 000207              RETURN

```

```

1          .SBTTL  LDREAD -- Perform I/O for handler load code
2          ;-----
3          ; This routine performs Read operations for handler load code.
4          ; It simulates the operation of the bootstrap read routine.
5          ; When called, Channel 1 must be open to the handler file.
6          ;
7          ; Inputs:
8          ; R0 = Block number within handler file to be read.
9          ; R1 = Number of words to read.
10         ; R2 = Buffer address
11         ;
12         ; Outputs:
13         ; C-flag is set if a read error occurs
14         ;
15 021154 010046 LDREAD: MOV     R0,-(SP)
16 021156 010446      MOV     R4,-(SP)      ;;;
17 021160 010004      MOV     R0,R4      ;;;Get starting block number
18         ;
19         ; Save current mapping information
20         ;
21 021162 105737 000000G      TSTB   MEM256      ;;;Does machine have > 256?
22 021166 001402      BEQ     1$      ;;;Br if not
23 021170 013746 000000G      MOV     @#SR3MMR,-(SP) ;;;Save extended memory address register
24 021174 013746 000000G 1$:  MOV     @#SROMMR,-(SP) ;;;Save memory mapping
25 021200 013746 000000G      MOV     @#KPAR5,-(SP) ;;;Save current KPAR5 mapping
26 021204 013746 000000G      MOV     @#KPAR6,-(SP) ;;;Save current KPAR6 mapping
27 021210 012737 001400 000000G  MOV     #1400,@#KPAR6 ;;;Restore original mapping for RT-11
28         ;
29         ; Turn off handler mapping
30         ;
31 021216 004737 021400'      CALL    HANUMP      ;Turn off handler mapping
32         ;
33         ; Read the requested data from the handler
34         ;
35 021222      .READW  #AREA,#1,R2,R1,R4 ;Read the blocks
36 021254 103420      BCS     9$      ;Br if read error
37         ;
38         ; Restore handler mapping
39         ;
40 021256 013704 000144'      MOV     CURDEV,R4      ;Get current device index
41 021262 004737 021324'      CALL    HANMAP      ;;;Map Kpar5 if necessary
42         ;
43         ; Restore mapping information
44         ;
45 021266 012637 000000G      MOV     (SP)+,@#KPAR6 ;;;Restore KPAR6 mapping
46 021272 012637 000000G      MOV     (SP)+,@#KPAR5 ;;;Restore KPAR5 mapping
47 021276 012637 000000G      MOV     (SP)+,@#SROMMR ;;;Restore memory mapping
48 021302 105737 000000G      TSTB   MEM256      ;;;Does machine have > 256?
49 021306 001402      BEQ     2$      ;;;Br if not
50 021310 012637 000000G      MOV     (SP)+,@#SR3MMR ;;;Restore extended memory address register
51         ;
52         ; Finished
53         ;
54 021314 000241 2$:  CLC      ;;;Signal success on return
55 021316 012604 9$:  MOV     (SP)+,R4
56 021320 012600      MOV     (SP)+,R0
57 021322 000207      RETURN

```

HANMAP -- Set up KPAR5 to access a mapped handler

```

1          .SBTTL  HANMAP -- Set up KPAR5 to access a mapped handler
2          ;-----
3          ; This routine is called to determine if a handler is mapped and if so
4          ; to turn on mapping and set up KPAR5 to access the mapped handler.
5          ; If the handler is not mapped, mapping is not turned on and KPAR5 is
6          ; not altered.
7          ; Interrupts are left disabled by this routine.
8          ; In addition to setting up mapping, this routine also changes the RMON
9          ; pointer to point to the TSX-Plus simulated RMON vector.
10         ;
11         ; Inputs:
12         ;   R4 = Device index number
13         ;
14 021324  HANMAP:
15         ;
16         ; Disable interrupts
17         ;
18 021324          DISABL          ;;Disable interrupts
19         ;
20         ; Change RMON pointer to point to TSX-Plus vector
21         ;
22 021332  012737  000000G 000000G          MOV      #MONVEC,@#RMON ;;Say TSX-Plus is the monitor
23         ;
24         ; See if this handler is mapped
25         ;
26 021340  005764  000000G          TST      HANPAR(R4) ;;Is this handler mapped?
27 021344  001403          BEQ      9$          ;;Br if not
28         ;
29         ; This handler is mapped.
30         ; Set up mapping to access it.
31         ;
32 021346  016437  000000G 000000G          MOV      HANPAR(R4),@#KPAR5;;Map KPAR5 to the handler code
33 021354  052737  000000G 000000G 9$:    BIS      #MMENBL,@#SR0MMR;;Enable memory mapping
34 021362  105737  000000G          TSTB    MEM256          ;;Does machine have > 256KB?
35 021366  001403          BEQ      10$          ;;Br if not
36 021370  052737  000000G 000000G          BIS      #EMMAP,@#SR3MMR ;;Enable extended memory addressing
37         ;
38         ; Finished
39         ;
40 021376  000207          10$:    RETURN
41         ;
42          .SBTTL  HANUMP -- Turn off memory mapping to a handler
43          ;-----
44         ; This routine is the companion to HANMAP. It turns off memory mapping
45         ; and restores KPAR5 to its normal mapping value.
46         ; Enter with interrupts disabled. Interrupts are enabled on return.
47         ; This routine also changes the RMON pointer back to RT-11.
48         ;
49 021400  HANUMP:
50         ;
51         ; Turn off memory management
52         ;
53 021400  105737  000000G          TSTB    MEM256          ;;Does machine have > 256KB?
54 021404  001403          BEQ      1$          ;;Br if not
55 021406  042737  000000G 000000G          BIC      #EMMAP,@#SR3MMR ;;Turn off extended memory addressing
56 021414  042737  000000G 000000G 1$:    BIC      #MMENBL,@#SR0MMR;;Turn off memory mapping
57 021422  012737  001200  000000G          MOV      #1200,@#KPAR5 ;;Reset KPAR5 to its normal mapping

```

```
58 ;  
59 ; Restore RMON pointer to RT11  
60 ;  
61 021430 013737 000046' 0000006      MOV      RTMNVC,@#RMON    ;;;Reset RMON pointer  
62 ;  
63 ; Enable interrupts  
64 ;  
65 021436      ENABL          ;Enable interrupts  
66 ;  
67 ; Finished  
68 ;  
69 021444 000207      RETURN
```

```

1          .SBTTL  FNDHRB -- Try to find a handler global region
2          ;-----
3          ; This routine is called to try to locate an allocated XM region with
4          ; a specified name.
5          ; If a region control block with the specified name cannot be found,
6          ; the address of a free one is returned and the specified name is stored
7          ; into the free block.
8          ;
9          ; Inputs:
10         ; R5 = Pointer to 2-word cell containing Rad50 name of region to be found.
11         ;
12         ; Outputs:
13         ; C-flag cleared ==> Found the specified RCB.
14         ; R1 = Address of the RCB
15         ; C-flag set ==> Could not find the specified RCB.
16         ; R1 = Pointer to a free RCB or 0 if no available RCB's.
17         ;
18 021446 010246 FNDHRB: MOV     R2,-(SP)
19         ;
20         ; Search for specified RCB and also remember if we see a free RCB
21         ;
22 021450 005002          CLR     R2          ; Say no free RCB found
23 021452 013701 0000000 MOV    HANRCB,R1      ; Point to start of RCB area
24 021456 005721          TST    (R1)+       ; Skip over -1 word at front
25 021460 005711 1$:    TST    (R1)         ; What is the status of this RCB
26 021462 001002          BNE    2$         ; Br if this is not a free RCB
27 021464 010102          MOV    R1,R2       ; Remember address of a free RCB
28 021466 000412          BR     3$         ;
29 021470 021127 177777 2$:    CMP    (R1), #-1      ; Are we at the end of the list?
30 021474 001412          BEQ    4$         ; Br if yes
31 021476 021561 000006          CMP    (R5), 6(R1)     ; Compare the names
32 021502 001004          BNE    3$         ; Br if don't match
33 021504 026561 000002 000010 CMP    2(R5), 10(R1)  ; Compare 2nd half of name
34 021512 001417          BEQ    6$         ; Br if found the RCB we were searching for
35 021514 062701 000012 3$:    ADD    #12,R1      ; Point to the next RCB
36 021520 000757          BR     1$         ; Continue searching
37         ;
38         ; We could not find the specified RCB.
39         ; If there was a free one, initialize the name.
40         ;
41 021522 010201 4$:    MOV    R2,R1          ; Was there a free RCB?
42 021524 001410          BEQ    5$         ; Br if not
43 021526 011561 000006          MOV    (R5), 6(R1)     ; Set name in the RCB
44 021532 016561 000002 000010 MOV    2(R5), 10(R1)  ; (2nd word of name)
45 021540 063761 000144' 000010 ADD    CURDEV, 10(R1) ; Make name unique to device
46 021546 000261 5$:    SEC                     ; Signal that we did not find the RCB
47 021550 000401          BR     9$         ;
48         ;
49         ; We found the RCB
50         ;
51 021552 000241 6$:    CLC                     ; Signal success on return
52         ;
53         ; Finished
54         ;
55 021554 012602 9$:    MOV    (SP)+, R2
56 021556 000207          RETURN

```

HANXMR -- Allocate XM region during handler load

```

1          .SBTTL  HANXMR -- Allocate XM region during handler load
2          ;-----
3          ; This routine can be called by a handler as its is being loaded to
4          ; allocate an XM region for the handler.
5          ;
6          ; Inputs:
7          ;   R2 = Number of 64-byte units needed for XM region
8          ;
9          ; Outputs:
10         ;   C-flag cleared ==> Successfully allocated a region
11         ;   R1 = 64-byte address of base of allocated region
12         ;   R2 = Requested size
13         ;   C-flag set ==> Could not allocate the region
14         ;   R2 = Largest available region size
15         ;
16         ; Notes: FMEMLO and FMEMHI are used by this routine to indicate the
17         ; bottom and top of the free memory area that can be allocated.
18         ; The allocation is done from the top of free memory downward.
19         ; FMEMHI is updated to have the new top of free memory after the
20         ; allocation has been done.
21         ;
22 021560 010046 HANXMR: MOV     RO,-(SP)
23         ;
24         ; Get the total amount of free memory space available now
25         ; and see if the requested region can be allocated.
26         ;
27 021562 013700 000134'      MOV     FMEMHI,RO      ;Top of free memory
28 021566 163700 000136'      SUB     FMEMLO,RO      ;-Base of free memory
29 021572 020200              CMP     R2,RO        ;Do we have room for the requested region?
30 021574 101006              BHI     B$           ;Br if not
31         ;
32         ; There is room for the region so allocate it from the top of memory
33         ;
34 021576 160237 000134'      SUB     R2,FMEMHI     ;Allocate the region
35 021602 013701 000134'      MOV     FMEMHI,R1     ;Return the address of the base of the region
36 021606 000241              CLC                     ;Signal success on return
37 021610 000402              BR      9$
38         ;
39         ; We are not able to allocate the region.
40         ; Return with C-flag set and the size of the largest possible region in R2.
41         ;
42 021612 010002 B$:      MOV     RO,R2      ;Get the size of the largest possible region
43 021614 000261      SEC                     ;Signal failure on return
44         ;
45         ; Finished
46         ;
47 021616 012600 9$:      MOV     (SP)+,RO
48 021620 000207      RETURN

```

HANXMR -- Allocate XM region during handler load

```

1          .IF      NE,<PROASM-1> ;If assembling for PDP-11
2          .SBTTL   SETMIO -- Set up information about mapped devices
3          ;-----
4          ; SETMIO is called to set up information about which devices have to have
5          ; their I/O mapped through system buffers. I/O mapping is done for DMA
6          ; devices with 18-bit controllers being used on Q-bus systems with more
7          ; than 256Kb of memory.
8          ; The DX$MAP flag is set in the DVFLAG word for devices that require mapping.
9          ;
10         ; Inputs:
11         ; R5 = Pointer to low-memory free area
12         ;
13         ; Outputs:
14         ; MIOFLAG = 0==>I/O mapping not required for any device;
15         ;             1==>I/O mapping required for some device.
16         ; R5 = Pointer to new low-memory free area.
17         ;
18         SETMIO: MOV      R1,-(SP)
19         ;
20         ; Determine if this machine requires mapping at all
21         ;
22         TST      #MIODBG      ;Are we debugging mapped I/O system?
23         BNE      2$          ;Br if yes
24         BIT      #EXTLSI,ICONFG ;Is this a Q-bus machine with more than 256Kb?
25         BNE      2$          ;Br if yes
26         ;
27         ; This is not a Q-bus system with more than 256Kb.
28         ; Mapping is not required at all.
29         ;
30         MOV      #2,R1        ;Get initial device index number
31         1$: BIC      #DX$MAP,DVFLAG(R1) ;Clear mapped-I/O flag
32         ADD      #2,R1        ;Get next device index
33         CMP      R1,NUMDEV    ;More to do?
34         BLOS     1$          ;Br if yes
35         BR      9$
36         ;
37         ; This is a Q-bus system with more than 256Kb.
38         ; See if any devices have requested mapped I/O.
39         ;
40         2$: CLR      R0        ;Clear composite flag word
41         MOV      #2,R1        ;Initialize device index number
42         3$: BIS      DVFLAG(R1),R0 ;Combine flags from all devices
43         ADD      #2,R1        ;Get next device index number
44         CMP      R1,NUMDEV    ;Checked all devices?
45         BLOS     3$          ;Br if not
46         BIT      #DX$MAP,R0   ;Does any device require mapping?
47         BEQ      9$          ;Br if not
48         ;
49         ; I/O mapping is required
50         ;
51         INCB     MIOFLG      ;Remember that mapping is required
52         ;
53         ; Zero the area where we will build the control structures
54         ;
55         MOVB     VMIOBF,R1    ;Get # buffers wanted
56         MUL      #MI$$SZ,R1   ;Times size for each control block
57         ADD      #MIONWB*MW$$SZ,R1 ;Add space for MIO wait blocks

```

HANXMR -- Allocate XM region during handler load

```

58          ASR      R1          ;Get # words to zero
59          MOV      R5,R0      ;Get pointer to start of area
60 4$:      CLR      (R0)+      ;Zero the entire area
61          SOB      R1,4$
62          ;
63          ; Allocate and initialize the control structures
64          ;
65          MOV      R5,MIOBHD    ;Start of area for I/O mapping control blks
66          MOVB     VMIOBF,R0   ;Get # buffers wanted
67 5$:      MOV      R5,R1      ;Get pointer to current control block
68          ADD      #MI$$SZ,R1  ;Get pointer to next control block
69          DEC      R0          ;Need to link more together?
70          BLE      6$         ;Br if not
71          MOV      R1,MI$LNK(R5) ;Set pointer to next control block
72          MOV      R1,R5      ;Advance pointer to next block
73          BR      5$         ;See if more to do
74 6$:      MOV      R1,R5      ;Set pointer past last block
75          MOV      R5,MIOWHD    ;Start of wait blocks
76          MOV      #MIONWB-1,R0 ;Get # wait blocks wanted - 1
77 7$:      MOV      R5,R1      ;Get pointer to current block
78          ADD      #MW$$SZ,R1  ;Get pointer to next block
79          MOV      R1,MW$LNK(R5) ;Set pointer to next wait block
80          MOV      R1,R5      ;Advance pointer to next block
81          SOB      R0,7$      ;Loop if more to allocate
82          ADD      #MW$$SZ,R5  ;Allocate space for last block (with 0 link)
83          ;
84          ; Finished
85          ;
86 9$:      MOV      (SP)+,R1
87          RETURN
88          . IFF      ;NE,<PROASM-1>
89          ;
90          ; Define dummy routine for Pro
91          ;
92 021622 000207 SETMID: RETURN
93          . ENDC      ;NE,<PROASM-1>

```

OVLPOS -- Determine which overlays go over TSINIT

```

1          .SBTTL  OVLPOS -- Determine which overlays go over TSINIT
2          ;-----
3          ; OVLPOS is called to determine which system overlays are to be placed
4          ; over the TSINIT code (specifically, between @OVLBAS and INITOP).
5          ;
6          ; Inputs:
7          ; R5 = Base address in TSINIT where overlays may be loaded.
8          ;
9          ; Outputs:
10         ; Overlay segment information is set up in OSTABL.
11         ; OS$FLG(seg) = 0==>Load seg into high memory; 1==>Load over TSINIT.
12         ; OVLBAS = Address of location within TSINIT where overlays start.
13         ; R5 = Pointer past last overlay loaded over TSINIT.
14         ;
15 021624 010146 OVLPOS: MOV     R1,-(SP)
16 021626 010246      MOV     R2,-(SP)
17 021630 010346      MOV     R3,-(SP)
18 021632 010446      MOV     R4,-(SP)
19 021634 010504      MOV     R5,R4          ;Get address where we may load overlays
20         ;
21         ; Build the table that holds information about the overlays
22         ;
23 021636 004737 022034' CALL    OVLBLD          ;Build overlay information table
24         ;
25         ; First determine how much space will be used by those overlays that are
26         ; forced to be loaded over TSINIT.
27         ;
28 021642 062704 000077      ADD     #63,R4          ;Bound address to 64-byte boundary
29 021646 042704 000077      BIC     #77,R4
30 021652 010437 000140'      MOV     R4,OVLBAS      ;Remember address where we load overlays
31 021656 012702 000576'      MOV     #OSTABL,R2     ;Point to start of table
32 021662 005762 000000      1$:   TST     OS$SIZ(R2) ;Is this overlay to be loaded?
33 021666 001423              BEQ     2$              ;Br if not
34 021670 016200 000004      MOV     OS$OVL(R2),R0  ;Point to linker-built entry
35 021674 016000 000000G      MOV     D.ADR(R0),R0  ;Get Rad50 segment ID
36 021700 012701 001026'      MOV     #LOWOVL,R1    ;Point to table of overlays to go over TSINIT
37 021704 020021      6$:   CMP     R0,(R1)+    ;Must this overlay go over TSINIT?
38 021706 001404              BEQ     4$              ;Br if yes
39 021710 020127 001034'      CMP     R1,#LOWEND    ;End of low-overlay table?
40 021714 103773              BLO     6$              ;Br if not
41 021716 000407              BR      2$              ;This overlay is not forced over TSINIT
42 021720 005262 000002      4$:   INC     OS$FLG(R2)  ;Set flag saying load over TSINIT
43 021724 016200 000000      MOV     OS$SIZ(R2),R0 ;Get # 64-byte blocks needed for overlay
44 021730 072027 000006      ASH     #6,R0          ;Get # bytes needed for overlay
45 021734 060004              ADD     R0,R4          ;Advance address within TSINIT
46 021736 062702 000006      2$:   ADD     #OS$#SZ,R2  ;Point to entry for next segment
47 021742 020237 001024'      CMP     R2,OSLAST     ;Have we finished?
48 021746 103745              BLO     1$              ;Loop if not
49         ;
50         ; Determine how much memory space is available in TSINIT for other overlays
51         ;
52 021750 020427 025474'      CMP     R4,#INITOP-50 ;Any space left for other overlays?
53 021754 103021              BHS     9$              ;Br if not
54 021756 012705 025556'      MOV     #INITOP,R5    ;Point to top of overlay area
55 021762 160405              SUB     R4,R5          ;Total space available for overlays
56 021764 072527 177772      ASH     #-6,R5         ;Convert to # 64-byte blocks
57         ;

```

OVLPOS -- Determine which overlays go over TSINIT

```

58      ; Now begin loop which determines which other overlays go over TSINIT.
59      ; We do this in the order of largest to smallest to try to fill
60      ; the overlay area as completely as possible.
61      ;
62 021770 004737 023226' 3#: CALL OVLTRY ; Try to find largest overlay that will fit
63 021774 103411          BCS 9# ; Br if no more overlays will fit
64 021776 005262 000002   INC OS#FLG(R2) ; Remember to load over TSINIT
65 022002 016200 000000   MOV OS#SIZ(R2),R0 ; Get # 64-byte blocks needed for overlay
66 022006 160005          SUB R0,R5 ; Reduce remaining free space in TSINIT
67 022010 072027 000006   ASH #6,R0 ; Get # bytes needed for overlay
68 022014 060004          ADD R0,R4 ; Advance overlay address in TSINIT
69 022016 000764          BR 3# ; See if we can find more segments to load
70      ;
71      ; Finished
72      ;
73 022020 010405 9#: MOV R4,R5 ; Return top-of-overlay address in R5
74 022022 012604          MOV (SP)+,R4
75 022024 012603          MOV (SP)+,R3
76 022026 012602          MOV (SP)+,R2
77 022030 012601          MOV (SP)+,R1
78 022032 000207          RETURN

```

OVLBLD -- Build overlay information table

```

1          .SBTTL  OVLBLD -- Build overlay information table
2          ;-----
3          ; OVLBLD is called to build an overlay information table that is used
4          ; by TSINIT while loading TSX overlays into memory.
5          ;
6          ; Outputs:
7          ; Overlay segment information is set up in OSTABL.
8          ; OSLAST = Pointer past last entry in OSTABL.
9          ;
10         OVLBLD: MOV     R1,-(SP)
11                MOV     R2,-(SP)
12                MOV     R3,-(SP)
13         ;
14         ; Read 1st block of SAV file to get pointer to overlay table
15         ;
16         OVLBLD: MOV     WRKBUF,R2      ;Point to work buffer
17                .READW  #AREA,#17,R2,#256.,#0 ;read first block of the save file
18                BCS     22$           ;Br if error on read
19                MOV     64(R2),R1      ;point to the overlay table
20                BNE     15$           ;br if overlays exist
21         ;
22         ; Must be verion 3B overlays structure at absolute location.
23         ;
24         OVLBLD: MOV     #137,@#1000    ;position jump instruction over 3b ovly handler
25                MOV     ##OVRH,@#1002 ;position overlay intercept location
26                MOV     #1104,R1      ;point to the overlay table
27                MOV     R1,OVRADD     ;save the address of the overlay table
28         ;
29         ; Initialize the table that holds information about the overlays
30         ;
31         OVLBLD: 15$: MOV     #OSTABL,R3  ;Point to table for overlay info
32                11$: MOV     R1,OS#OVL(R3) ;Save pointer to overlay control block
33                CLR     OS#FLG(R3)     ;Assume seg will be loaded in high memory
34                CALL    ALCOVL        ;Determine if we should load this overlay
35                MOV     R2,OS#SIZ(R3) ;Remember total size of overlay+data
36                ADD     #OS##SZ,R3    ;Point to next overlay table entry
37                12$: ADD     #6,R1     ;find the next region
38                CMP     (R1),#4537    ;compare with a <JSR R5,#OVRH> instruction
39                BNE     11$           ;Br if not at end
40                MOV     R3,OSLAST     ;Save pointer past last overlay table entry
41         ;
42         ; Finished
43         ;
44         OVLBLD: MOV     (SP)+,R3
45                MOV     (SP)+,R2
46                MOV     (SP)+,R1
47                RETURN
48         ;
49         ; Error -- Read error occured while reading overlay table
50         ;
51         OVLBLD: 22$: .PRINT #TSXHD
52                .PRINT #RDERR
53                JMP     INISTP

```

GETMAP -- Load any mapped system code regions

```

1          .SBTTL  GETMAP -- Load any mapped system code regions
2          ;-----
3          ; GETMAP is called to load those system overlays that are placed
4          ; in high memory.
5          ;
6          ; Inputs:
7          ;   R5 = 64-byte block number of top of free memory.
8          ;
9          ; Outputs:
10         ;   R5 = New 64-byte block number of top of free memory.
11         ;
12 022234 010146 GETMAP: MOV     R1,-(SP)
13 022236 010246         MOV     R2,-(SP)
14 022240 010346         MOV     R3,-(SP)
15 022242 010537 000000G        MOV     R5,SMRSIZ      ;Save memory pointer at start of allocation
16         ;
17         ; Now that most of the system initialization is completed, we must check
18         ; again to see which overlays need to be loaded.
19         ;
20 022246 012703 000576'        MOV     #OSTABL,R3      ;Point to 1st overlay table entry
21 022252 004737 022520' 1$:  CALL    OPTOVL      ;See if this segment should be loaded
22 022256 010263 000000        MOV     R2,OS$SIZ(R3)  ;Save # 64-byte blocks needed for overlay
23 022262 062703 000006        ADD     #OS*$SZ,R3     ;Point to next overlay table entry
24 022266 020337 001024'        CMP     R3,OSLAST     ;Checked all entries in overlay table?
25 022272 103767                BLO    1$             ;Br if not
26         ;
27         ; Load those overlays that go into high memory
28         ;
29 022274 012702 000576'        MOV     #OSTABL,R2     ;Point to 1st overlay entry
30 022300 005762 000000        3$:  TST     OS$SIZ(R2)   ;Is this overlay segment wanted?
31 022304 001405                BEQ    4$             ;Br if not
32 022306 005762 000002        TST     OS$FLG(R2)   ;Load over TSINIT or into high memory?
33 022312 001002                BNE    4$             ;Br if load over TSINIT
34 022314 004737 023324'        CALL   GETOVL      ;Load overlay into high memory
35 022320 062702 000006        4$:  ADD     #OS*$SZ,R2  ;Point to next overlay table entry
36 022324 020237 001024'        CMP     R2,OSLAST   ;Have we done all overlays?
37 022330 103763                BLO    3$             ;Loop if not
38         ;
39         ; Finished
40         ;
41 022332 013700 000000G        19$:  MOV     SMRSIZ,R0   ;Get memory pointer at start of allocation
42 022336 160500                SUB     R5,R0         ;Calc amt of space allocated
43 022340 010037 000000G        MOV     R0,SMRSIZ   ;Save total space used for mapped regions
44 022344 012603                MOV     (SP)+,R3
45 022346 012602                MOV     (SP)+,R2
46 022350 012601                MOV     (SP)+,R1
47 022352 000207                RETURN

```

ALCOVL -- Allocate space for a system overlay region

```

1          .SBTTL  ALCOVL -- Allocate space for a system overlay region
2          ;-----
3          ; ALCOVL is called to determine if a system overlay region is wanted
4          ; (based on sysgen options), and if it is wanted to determine how
5          ; much space is needed for the code and data.
6          ;
7          ; Inputs:
8          ;   R3 = Pointer to overlay table entry (OS$xxx)
9          ;
10         ; Outputs:
11         ;   C-flag cleared ==> This segment is to be loaded.
12         ;   C-flag set    ==> Do not load this overlay segment.
13         ;   R2 = # 64-Byte blocks needed for segment including data areas within it.
14         ;
15 022354 010146 ALCOVL: MOV     R1,-(SP)
16         ;
17         ; Get pointer to linker-build overlay entry for segment
18         ;
19 022356 016301 000004         MOV     OS$OVL(R3),R1 ;Get pointer to linker-built entry for seg
20         ;
21         ; Read in the first block of the overlay segment
22         ;
23 022362 013702 000152'         MOV     WRKBUF,R2 ;Point to work buffer
24 022366         .READW  #AREA,#17,R2,#256.,D.BLK(R1) ;read the first block
25 022424 103415         BCS     3$ ;Br if read error
26         ;
27         ; Save the 3 character Rad50 segment ID in the D.ADR cell of the
28         ; linker-built overlay table entry for this segment.
29         ;
30 022426 016261 000002 000000G     MOV     2(R2),D.ADR(R1) ;save the rad50 overlay identifier
31         ;
32         ; Make sure the segment is not larger than 8Kb
33         ;
34 022434 016102 000000G     MOV     D.SIZ(R1),R2 ;get the word count of the code region
35 022440 006302         ASL     R2 ;convert to byte count
36 022442 020227 020000         CMP     R2,#20000 ;check for 8kb overflow
37 022446 101014         BHI     21$ ;Br if region is too big
38         ;
39         ; Don't load some optional segments if features were not selected
40         ; in TSGEN.
41         ;
42 022450 004737 022520'         CALL    OPTOVL ;See if we want to load this segment
43         ;
44         ; Finished
45         ; The C-flag is set or reset by OPTOVL.
46         ;
47 022454 012601         MOV     (SP)+,R1
48 022456 000207         RETURN
49         ;
50         ; Error -- Error on reading from SAV file
51         ;
52 022460 3$: .PRINT  #TSXHD ;Print heading
53 022466         .PRINT  #RDERR ;Read error
54 022474 000137 004250'         JMP     INISTP ;Abort initialization
55         ;
56         ; Error -- Insufficient memory space to load run-time systems
57         ;

```

58 022500	21#:	.PRINT	#TSXHD	;PRINT HEADING
59 022506		.PRINT	#TSXSIZ	;PRINT ERROR MESSAGE
60 022514 000137 004250'		JMP	INISTP	;ABORT INITIALIZATION

```

1          .SBTTL  OPTOVL -- Check for optional system overlay regions
2          ;-----
3          ; OPTOVL is called to determine if a specific system overlay is or is
4          ; not to be loaded based on sysgen options.
5          ; This routine may also add space for buffers to the overlay regions size.
6          ;
7          ; Inputs:
8          ;   R3 = Pointer to overlay table entry for segment (OS$xxx)
9          ;
10         ; Outputs:
11         ;   C-flag cleared ==> Load this overlay.
12         ;   C-flag set    ==> Do not load this overlay.
13         ;   R2 = # 64-byte blocks needed for code + data for the segment.
14         ;
15 022520 010346 OPTOVL: MOV     R3,-(SP)
16 022522 010446         MOV     R4,-(SP)
17 022524 010546         MOV     R5,-(SP)
18         ;
19         ; Get the name of the overlay segment
20         ;
21 022526 016305 000004         MOV     OS$OVL(R3),R5 ;Get pointer to linker-built entry
22 022532 016504 000000G        MOV     O.ADR(R5),R4 ;Get name of the segment
23         ;
24         ; Get size of code portion of overlay segment
25         ;
26 022536 016502 000000G        MOV     O.SIZ(R5),R2 ;Get # words needed by code portion of seg
27 022542 006302                 ASL     R2 ;Convert to # bytes
28         ;
29         ; See if this is an optional segment that we need to deal with specially
30         ;
31 022544 012700 022570'         MOV     #OVLLST,R0 ;Point to overlay name list
32 022550 020420 1$:             CMP     R4,(R0)+ ;Found name of overlay?
33 022552 001405                 BEQ     2$ ;Br if yes
34 022554 005720                 TST     (R0)+ ;No -- Skip over address word
35 022556 020027 022654'        CMP     R0,#OVLEND ;Checked all names in the list?
36 022562 103772                 BLD     1$ ;Loop if not
37 022564 000577                 BR     OQYES ;Load this overlay
38         ;
39         ; Branch off to processing routine
40         ;
41 022566 000130 2$:             JMP     @(R0)+ ;Enter processing routine for the overlay
42         ;
43         ; Table of overlay names and processing routines
44         ;
45         .MACRO  OVLTBLE  NAME
46         .RAD50  /'NAME'/
47         .WORD   OOR'NAME
48         .ENDM   OVLTBLE
49         ;
50 022570 OVLLST:
51 022570         OVLTBLE  USR ;TSUSR -- File management
52 022574         OVLTBLE  SPL ;TSSPOL -- Spooling system
53 022600         OVLTBLE  LOK ;TSLOCK -- Shared file record locking
54 022604         OVLTBLE  MSG ;TSMSC -- Inter-job message communication
55 022610         OVLTBLE  SWP ;TSSWAP -- Job swapper
56 022614         OVLTBLE  PLS ;TSPLAS -- PLAS support
57 022620         OVLTBLE  SLE ;TSSLE -- Single line editor
    
```

OPTOVL -- Check for optional system overlay regions

```

58 022624          OVLTBL  WIN          ;TSWIN  -- Display window management
59 022630          OVLTBL  MIO          ;TSMIO  -- Mapped I/O
60 022634          OVLTBL  CLO          ;TSCLO  -- CL handler
61 022640          OVLTBL  DBG          ;TSDBG  -- Program debugger
62 022644          OVLTBL  CSH          ;TSCASH -- Data caching
63 022650          OVLTBL  DMP          ;TSDUMP -- Crash dump generator
64 022654          OVLEND:
65                ;
66                ; File management
67                ;
68 022654 013703 000000G OORUSR: MOV    VNFCSH,R3      ;Get # file cache entries
69 022660 070327 000000G      MUL    #FC##SZ,R3      ;Multiply by size of each entry
70 022664 060302          ADD    R3,R2        ;Allocate space for directory cache
71 022666 000536          BR     OOXYES       ;Load the segment
72                ;
73                ; Spooling system
74                ;
75 022670 005727 000000G OORSPL: TST    #SPLND          ;Are there any spooled devices?
76 022674 001530          BEQ    OOXNO        ;Br if not
77 022676 062702 000000C      ADD    #<SPLNB*512.>,R2 ;Reserve room for spool buffers
78 022702 013703 000000G      MOV    NSPLBL,R3        ;Get # blocks for spool file
79 022706 062703 0000007      ADD    #7,R3           ;Bound up to byte boundary
80 022712 072327 177775      ASH    #-3,R3          ;Divide by 8 to get # bytes for table
81 022716 005203          INC    R3             ;Round up to word boundary
82 022720 042703 0000001      BIC    #1,R3          ;
83 022724 060302          ADD    R3,R2        ;Add space for spool file allocation table
84 022726 000516          BR     OOXYES       ;Load the segment
85                ;
86                ; Record locking system
87                ;
88 022730 005737 000000G OORLOK: TST    VMXSF          ;Any shared files?
89 022734 001510          BEQ    OOXNO        ;Br if not
90 022736 005737 000000G      TST    VNUMDC          ;Shared file data caching wanted?
91 022742 001110          BNE    OOXYES       ;Br if yes
92 022744 162702 000000G      SUB    #DCCSIZ,R2      ;Reduce size of segment - Leave out cache code
93 022750 000505          BR     OOXYES       ;Load the segment
94                ;
95                ; Message communication system
96                ;
97 022752 013703 000000G OORMSG: MOV    VMAXMC,R3      ;Is message communication facility wanted?
98 022756 001477          BEQ    OOXNO        ;Br if not
99 022760 070327 000000G      MUL    #MB##SZ,R3      ;Space for message channel blocks
100 022764 060302          ADD    R3,R2        ;
101 022766 013703 000000G      MOV    VMXMRB,R3      ;Number of message request blocks
102 022772 070327 000000G      MUL    #MR##SZ,R3      ;Times size of request block
103 022776 060302          ADD    R3,R2        ;
104 023000 013703 000000G      MOV    VMSCHR,R3      ;Max # chars in a message
105 023004 005203          INC    R3             ;Bound up to word
106 023006 042703 0000001      BIC    #1,R3          ;Reserve whole number of words
107 023012 062703 000000G      ADD    #MU$TXT,R3      ;Plus space for message header
108 023016 070337 000000G      MUL    VMXMSG,R3      ;Times maximum number of messages
109 023022 060302          ADD    R3,R2        ;Space for message buffers
110 023024 000457          BR     OOXYES       ;
111                ;
112                ; PLAS support
113                ;
114 023026 013703 000000G OORPLS: MOV    VPLAS,R3      ;PLAS support wanted?

```

```

115 023032 001451          BEQ    OOXNO          ;Br if not
116 023034 062703 000021  ADD    #17.,R3        ;Bound up # blocks
117 023040 072327 177775  ASH    #-3,R3         ;Get # bytes needed for swap file bit map
118 023044 060302          ADD    R3,R2          ;Reserve room for swap file bit map
119 023046 000446          BR     OOXYES         ;Load the segment
120                          ;
121                          ; Job swapper
122                          ;
123 023050 105737 000000G  OORSWP: TSTB   VSWPFL      ;Is this a swapping system?
124 023054 001440          BEQ    OOXNO          ;Br if not
125 023056 000442          BR     OOXYES         ;Br if yes -- Load the segment
126                          ;
127                          ; Single line editor
128                          ;
129 023060 105737 000000G  OORSLE: TSTB   VSLEDT      ;Is SL editor wanted?
130 023064 001434          BEQ    OOXNO          ;Br if not
131 023066 000436          BR     OOXYES         ;Load the segment
132                          ;
133                          ; Display windows
134                          ;
135 023070 013703 000000G  OORWIN: MOV    VMXWIN,R3    ;Are any display windows wanted?
136 023074 001430          BEQ    OOXNO          ;Br if not
137 023076 070327 000000G  MUL    #DW##SZ,R3        ;Amt of space needed for window control blks
138 023102 060302          ADD    R3,R2          ;Add to size of overlay
139 023104 000427          BR     OOXYES         ;Load the segment
140                          ;
141                          ; Mapped I/O
142                          ;
143 023106 105737 000000G  OORMIO: TSTB   MIOFLG      ;Is I/O mapping needed?
144 023112 001421          BEQ    OOXNO          ;Br if not
145 023114 000423          BR     OOXYES         ;Load the segment
146                          ;
147                          ; CL handler
148                          ;
149 023116 005727 000000G  OORCLO: TST    #CLTOTL     ;Any I/O lines?
150 023122 001415          BEQ    OOXNO          ;Br if not
151 023124 000417          BR     OOXYES         ;Yes, load the segment
152                          ;
153                          ; Program debugger
154                          ;
155 023126 105737 000000G  OORDBG: TSTB   VDBFLG      ;Is the program debugger wanted?
156 023132 001411          BEQ    OOXNO          ;Br if not
157 023134 000413          BR     OOXYES         ;Load this segment
158                          ;
159                          ; Data caching
160                          ;
161 023136 005737 000000G  OORCSH: TST    CSHALC      ;Is data caching wanted?
162 023142 001405          BEQ    OOXNO          ;Br if not
163 023144 000407          BR     OOXYES         ;Load this segment
164                          ;
165                          ; Crash dump generator
166                          ;
167 023146 105737 000000G  OORDMP: TSTB   VSYDMP      ;Is dump facility wanted?
168 023152 001401          BEQ    OOXNO          ;Br if not
169 023154 000403          BR     OOXYES         ;Br if yes
170                          ;
171                          ; Don't load this segment

```

```
172 ;  
173 023156 005002 ODXNO: CLR R2 ; Say no space needed for overlay  
174 023160 000261 SEC ; Signal don't load the segment  
175 023162 000415 BR ODXFIN  
176 ;  
177 ; Load this segment  
178 ;  
179 023164 005202 ODXYES: INC R2 ; Make sure size is even  
180 023166 042702 000001 BIC #1,R2  
181 023172 020227 020000 CMP R2,#8192. ; Don't allow code + data to exceed 8Kb  
182 023176 101402 BLOS 1$ ; Br if ok  
183 023200 012702 020000 MOV #8192.,R2 ; Note, init code in segment will truncate dat  
184 023204 062702 000077 1$: ADD #63.,R2 ; Convert to # 64-byte blocks  
185 023210 072227 177772 ASH #-6,R2  
186 023214 000241 CLC ; Signal to load the segment  
187 ;  
188 ; Finished  
189 ;  
190 023216 012605 ODXFIN: MOV (SP)+,R5  
191 023220 012604 MOV (SP)+,R4  
192 023222 012603 MOV (SP)+,R3  
193 023224 000207 RETURN
```

```

1          .SBTTL  OVLTRY -- Find an overlay to place over TSINIT
2          ;-----
3          ; OVLTRY is called to identify the largest overlay segment which
4          ; will fit in the TSINIT area and which is not already marked to go
5          ; over TSINIT.
6          ;
7          ; Inputs:
8          ;   R5 = # 64-byte blocks available for segment in TSINIT.
9          ;
10         ; Outputs:
11         ;   R2 = Pointer to OSTABL entry for segment
12         ;   C-flag set ==> No more segments will fit.
13         ;
14 023226 010346 OVLTRY: MOV     R3,-(SP)
15         ;
16         ; Begin loop to examine all segments
17         ;
18 023230 005002          CLR     R2          ; Say we haven't found any segment yet
19 023232 012703 000576'  MOV     #OSTABL,R3      ; Point to entry for 1st segment
20 023236 005763 000000  1$:  TST     OS$SIZ(R3)    ; Is this segment to be loaded?
21 023242 001415          BEQ     2$          ; Br if not
22 023244 005763 000002  TST     OS$FLG(R3)    ; Is this segment already over TSINIT?
23 023250 001012          BNE     2$          ; Br if yes
24 023252 026305 000000  CMP     OS$SIZ(R3),R5  ; Will this segment fit?
25 023256 101007          BHI     2$          ; Br if not
26 023260 005702          TST     R2          ; Have we found any other seg yet?
27 023262 001404          BEQ     3$          ; Br if not
28 023264 026362 000000 000000  CMP     OS$SIZ(R3),OS$SIZ(R2) ; Is new seg larger than old?
29 023272 101401          BLOS   2$          ; Br if not
30 023274 010302 3$:  MOV     R3,R2          ; Remember largest segment
31 023276 062703 000006  2$:  ADD     #OS$SZ,R3      ; Point to entry for next segment
32 023302 020337 001024'  CMP     R3,OSLAST    ; Have we checked all segments?
33 023306 103753          BLD     1$          ; Loop if not
34         ;
35         ; Finished
36         ;
37 023310 000241          CLC          ; Assume we found a segment
38 023312 005702          TST     R2          ; Did we find a segment that will fit?
39 023314 001001          BNE     9$          ; Br if yes
40 023316 000261          SEC          ; Signal failure on return
41 023320 012603 9$:  MOV     (SP)+,R3
42 023322 000207          RETURN

```

GETOVL -- Load system overlay into high memory

```

1          .SBTTL  GETOVL -- Load system overlay into high memory
2          ;-----
3          ; GETOVL is called to load a system overlay into high memory.
4          ;
5          ; Inputs:
6          ;   R2 = Pointer to overlay table entry for segment in OSTABL.
7          ;   R5 = 64-byte physical memory block number where seg is to be loaded.
8          ;
9          ; Outputs:
10         ;   R5 = Update 64-byte physical memory block pointer for next segment.
11         ;
12 023324  GETOVL:
13         ;
14         ; Allocate space for the overlay segment
15         ;
16 023324 166205 000000      SUB    OS$SIZ(R2),R5    ;Allocate space for overlay
17 023330 020527 001600      CMP    R5,#1600        ;Are we about to run over RT-11?
18 023334 103405           BLD    10$              ;Br if yes -- Insufficient memory
19         ;
20         ; Remember the base address of some key segments
21         ;
22 023336 004737 004636'    CALL   KEYSEG          ;Remember address of some segments
23         ;
24         ; Load the segment
25         ;
26 023342 004737 023370'    CALL   LODOVL         ;Load the segment
27         ;
28         ; Finished
29         ;
30 023346 000207           RETURN
31         ;
32         ; Error: Memory overflow
33         ;
34 023350          10$:    .PRINT  #TSXHD
35 023356          .PRINT  #TSXSIZ
36 023364 000137 004250'    JMP    INISTP

```

L0DOVL -- Read and relocate system overlay

```

1          .SBTTL  L0DOVL -- Read and relocate system overlay
2          ;-----
3          ; L0DOVL is called to load a system overlay region into memory.
4          ;
5          ; Inputs:
6          ; R2 = Pointer to DSTABL entry for segment being loaded.
7          ; R5 = 64-byte physical memory block number where segment is to be loaded.
8          ;
9 023370 010146 L0DOVL: MOV      R1,-(SP)
10 023372 010246      MOV      R2,-(SP)
11 023374 010346      MOV      R3,-(SP)
12 023376 010446      MOV      R4,-(SP)
13 023400 010546      MOV      R5,-(SP)
14
15          ;
16          ; Get info about size of the overlay and position within SAV file
17          ;
18 023402 016201 000004      MOV      OS#OVL(R2),R1  ;Get pointer to linker-built segment entry
19 023406 016103 000000G     MOV      0.SIZ(R1),R3  ;Get size of overlay segment (# words)
20 023412 016137 000000G 000142' MOV      0.BLK(R1),FILBLK;Get block in SAV file where segment starts
21 023420 010302      MOV      R3,R2  ;Get total number of words in segment
22 023422 062702 000377     ADD      #255.,R2  ;round to the nearest number of blocks
23 023426 000302      SWAB     R2  ;Divide by 256. words per segment
24 023430 042702 177400     BIC      #177400,R2  ;kill sign extension bits
25 023434 010561 000000G     MOV      R5,0.PAR(R1) ;Remember where segment is being loaded
26          ;
27          ; Read next block of overlay segment into low-memory buffer
28          ;
29 023440 013704 000152' 10#:  MOV      WRKBUF,R4  ;Point to work buffer
30 023444      .READW  #AREA,#17,R4,#256.,FILBLK ;read a block
31 023502 103466      BCS      22$  ;read error occurred
32          ;
33          ; Move from low buffer to high position in memory
34          ;
35 023504 012701 000000G     MOV      #VPAR5,R1  ;get the virtual address of the mapped region
36 023510 012700 000400     MOV      #256.,R0  ;obtain the number of words to move
37 023514 020300      CMP      R3,R0  ;Do we need to move as many as 256 words?
38 023516 103001      BHS      2$  ;Br if yes
39 023520 010300      MOV      R3,R0  ;Get number of words to move for last block
40 023522 160003 2#:  SUB      R0,R3  ;Get number of words left after this move
41 023524      DISABL  ;** Disable interrupts **
42 023532 013746 000000G     MOV      @#KPAR5,-(SP) ;save the contents of the mapping register
43 023536 010537 000000G     MOV      R5,@#KPAR5  ;change the mapping register
44 023542 052737 000000G 000000G BIS      #MMENBL,@#SR0MMR;enable memory management
45 023550 105737 000000G     TSTB     MEM256  ;Does machine have at least 256Kb of memory?
46 023554 001403      BEQ      11$  ;Br if not
47 023556 052737 000000G 000000G BIS      #EMMAP,@#SR3MMR ;enable extended memory addressing
48 023564 012421 11#:  MOV      (R4)+,(R1)+ ;move into high memory
49 023566 077002      SOB      R0,11$
50 023570 105737 000000G     TSTB     MEM256  ;Does this machine have at least 256Kb?
51 023574 001403      BEQ      12$  ;Br if not
52 023576 042737 000000G 000000G BIC      #EMMAP,@#SR3MMR ;disable extended memory management
53 023604 042737 000000G 000000G 12#:  BIC      #MMENBL,@#SR0MMR;disable memory management
54 023612 012637 000000G     MOV      (SP)+,@#KPAR5  ;restore the mapping register
55 023616      ENABL  ;** Enable interrupts **
56 023624 062705 000010     ADD      #10,R5  ;advance 64-byte block # by 512-bytes
57 023630 005237 000142'     INC      FILBLK  ;increment file block #

```

LDOOVL -- Read and relocate system overlay

```

58 023634 005302          DEC      R2          ;More to be copied?
59 023636 001402          BEQ      5$          ;Br if not
60 023640 000137 023440'  JMP      10$          ;Read and copy rest of mapped segment
61                          ;
62                          ; Finished loading the segment
63                          ;
64 023644 012605 5$:      MOV      (SP)+,R5
65 023646 012604          MOV      (SP)+,R4
66 023650 012603          MOV      (SP)+,R3
67 023652 012602          MOV      (SP)+,R2
68 023654 012601          MOV      (SP)+,R1
69 023656 000207          RETURN
70                          ;
71                          ; Error occurred on read
72                          ;
73 023660 22$:      .PRINT  #TSXHD      ;Print heading
74 023666          .PRINT  #RDERR      ;Read error
75 023674 000137 004250' JMP      INISTP      ;Abort initialization

```

GETSRT -- Load any shared run-time systems

```

1          .SBTTL  GETSRT -- Load any shared run-time systems
2          ;-----
3          ; GETSRT is called to load into memory a shared run-time system.
4          ; Shared run-time systems are loaded into the top of memory.
5          ;
6          ; Inputs:
7          ; R1 = Pointer to shared run-time descriptor block.
8          ; R5 = 64-byte block number of top of free memory.
9          ;
10         ; Outputs:
11         ; R5 = New top of memory block number
12         ;
13 023700 010146 GETSRT: MOV     R1,-(SP)
14 023702 010246      MOV     R2,-(SP)
15 023704 010346      MOV     R3,-(SP)
16 023706 010446      MOV     R4,-(SP)
17         ;
18         ; See if this is a dummy run-time entry to allow for patching
19         ;
20 023710 021127 0000000 CMP     (R1),#DMYDEV ;Dummy run-time entry?
21 023714 001540      BEQ     7$ ;Br if yes
22         ;
23         ; Try to open a channel to run-time file
24         ;
25 023716          . LOOKUP #AREA,#1,R1 ; OPEN CHANNEL TO RUN-TIME FILE
26 023734 103010      BCC     8$ ;BR IF OPEN WAS SUCCESSFUL
27         ;
28         ; Cannot open shared run-time file.
29         ; See if he wants to abort or continue.
30         ;
31 023736 105737 0000000 TSTB   VINABT ;ABORT OR CONTINUE
32 023742 001132      BNE     9$ ;BR IF ABORT WANTED
33 023744 005061 0000000 CLR     RT$NAM(R1) ;Mark run-time as not-available
34 023750 005061 0000020 CLR     RT$NAM+2(R1)
35 023754 000520      BR      7$ ;GO LOAD NEXT RUN-TIME SYSTEM
36         ;
37         ; Set up information about position of run-time in physical memory
38         ;
39 023756 116102 0000000 B$:    MOVB   RT$SKP(R1),R2 ;GET # BLOCKS TO SKIP AT FRONT OF RUN-TIME
40 023762 042702 177400      BIC     #^C377,R2 ;CLEAR SIGN EXTENSION
41 023766 160200      SUB     R2,R0 ;GET # BLOCKS TO READ (LOOKUP SET RO W SIZE)
42 023770 010561 0000000 MOV     R5,RT$TOP(R1) ;SET 64-BYTE BLOCK # ABOVE TOP OF RUN-TIME
43 023774 010003      MOV     R0,R3 ;GET # 512-BYTE BLOCKS IN RUN-TIME
44 023776 072027 0000003 ASH     #3,R0 ;CONVERT TO # 64-BYTE BLOCKS
45 024002 160005      SUB     R0,R5 ;CALCULATE BASE 64-BYTE BLOCK # OF RUN-TIME
46 024004 020527 001600      CMP     R5,#1600 ;ARE WE ABOUT TO RUN OVER RT-11?
47 024010 103530      BLO     11$ ;BR IF YES
48 024012 010561 0000000 MOV     R5,RT$BAS(R1) ;SET BASE 64-BYTE BLOCK # OF RUN-TIME
49         ;
50         ; Read run-time system into memory and position in high-memory
51         ;
52 024016 010546      MOV     R5,-(SP) ;Save address of bottom of run-time
53 024020 013704 000152' 4$:    MOV     WRKBUF,R4 ;Point to work buffer
54 024024          . READW #AREA,#1,R4,#256.,R2 ;READ A BLOCK OF RUN-TIME FILE
55         ; Use memory management to access high-memory area.
56 024060 012701 0000000 MOV     #VPAR6,R1 ;GET VIRTUAL ADDRESS OF PAR6 ADDRESS REGION
57 024064 010537 0000000 MOV     R5,@#UPAR6 ;SET USER-MODE PAR6 MAP OFFSET VALUE

```

```

58 024070 012737 077406 000000G      MOV      #077406,@#UPDR6 ;SET PDR TO ALLOW FULL ACCESS TO PAGE
59 024076 052737 000000G 000000G      BIS      #UPMODE,@#PSW   ;SET PREVIOUS-MODE = USER FOR MTPD ACCESS
60 024104 012700 000400                MOV      #256.,R0       ;GET # WORDS TO MOVE
61 024110                DISABL                    ;** Disable interrupts **
62 024116 052737 000000G 000000G      BIS      #MMENBL,@#SROMMR;enable memory management
63 024124 105737 000000G                TSTB    MEM256          ;DOES THIS MACHINE HAVE AT LEAST 256KB?
64 024130 001403                BEQ     3$              ;BR IF NOT
65 024132 052737 000000G 000000G      BIS      #EMMAP,@#SR3MMR;enable extended memory addressing
66 024140 012446                3$:     MOV      (R4)+,-(SP)   ;TRANSFER DATA FROM BUFFER TO HIGH MEMORY
67 024142 106621                MTPD    (R1)+
68 024144 077003                SOB     R0,3$
69 024146 105737 000000G                TSTB    MEM256          ;DOES THIS MACHINE HAVE AT LEAST 256KB?
70 024152 001403                BEQ     31$            ;BR IF NOT
71 024154 042737 000000G 000000G      BIC      #EMMAP,@#SR3MMR;DISABLE EXTENDED MEMORY MANAGEMENT
72 024162 042737 000000G 000000G 31$:    BIC      #MMENBL,@#SROMMR;DISABLE MEMORY MANAGEMENT
73 024170                ENABL                    ;** Enable interrupts **
74 024176 062705 000010                ADD     #10,R5          ;ADVANCE 64-BYTE BLOCK # BY 512-BYTES
75 024202 005202                INC     R2              ;INC FILE BLOCK #
76 024204 077373                SOB     R3,4$          ;READ AND COPY REST OF FILE
77
78 ; Finished loading the run-time system.
79 ;
80 024206 012605                MOV     (SP)+,R5
81 024210                .CLOSE #1
82 ;
83 ; Finished
84 ;
85 024216 012604                7$:     MOV     (SP)+,R4
86 024220 012603                MOV     (SP)+,R3
87 024222 012602                MOV     (SP)+,R2
88 024224 012601                MOV     (SP)+,R1
89 024226 000207                RETURN
90 ;
91 ; Error -- Cannot find run-time system file
92 ;
93 024230                9$:     .PRINT #TSXHD      ;PRINT MESSAGE HEADING
94 024236                .PRINT #COSRT        ;PRINT ERROR MESSAGE
95 024244 012702 000004                MOV     #4,R2          ;PRINT 4 RAD50 VALUES
96 024250 012100                10$:    MOV     (R1)+,R0   ;GET PART OF NAME
97 024252 004737 025502'                CALL    PRTR50         ;PRINT RAD50 VALUE
98 024256 077204                SOB     R2,10$
99 024260                .PRINT #CRLF          ;END LINE
100 024266 000137 004250'                JMP     INISTP         ;ABORT INITIALIZATION
101 ;
102 ; Error -- Insufficient memory space to load run-time systems
103 ;
104 024272                11$:    .PRINT #TSXHD      ;PRINT HEADING
105 024300                .PRINT #SRTOVF       ;PRINT ERROR MESSAGE
106 024306 000137 004250'                JMP     INISTP         ;ABORT INITIALIZATION

```

CSHBUF -- Allocate space for data cache tables

```

1          .SBTTL  CSHBUF -- Allocate space for data cache tables
2          ;-----
3          ; Allocate space for data cache blocks and control tables.
4          ;
5          ; Inputs:
6          ; R4 = 64-byte block number of base of free memory.
7          ; R5 = 64-byte block number of top of free memory.
8          ;
9          ; Outputs:
10         ; R5 = Updated 64-byte block number of top of free memory.
11         ;
12 024312 010246 CSHBUF: MOV     R2,-(SP)
13 024314 010346      MOV     R3,-(SP)
14         ;
15         ; See if data caching is wanted
16         ;
17 024316 013737 000000G 000000G      MOV     CSHALC,VCSHNB ;Set # blocks in use = # blocks allocated
18 024324 013702 000000G      MOV     CSHALC,R2      ;Did used request data caching?
19 024330 001464      BEQ     9$          ;Br if not
20         ;
21         ; Calculate number of 64-byte blocks needed for each cache control table
22         ;
23 024332 062702 000037      ADD     #31.,R2      ;Bound up to 32 word block
24 024336 072227 177773      ASH     #-5.,R2      ;Convert to # 64-byte blocks
25         ;
26         ; Compute total space that will be used by all cache data
27         ;
28 024342 013703 000000G      MOV     CSHALC,R3      ;Get # blocks in cache
29 024346 072327 000003      ASH     #3,R3        ;Get # 64-byte blks used by cache data buffers
30 024352 012700 000010      MOV     #8.,R0        ;Get number of cache control tables
31 024356 060203 1$: ADD     R2,R3        ;Accumulate total space needed
32 024360 077002      SOB     R0,1$
33 024362 010337 000000G      MOV     R3,CSHSIZ    ;Save total space used by cache data
34         ;
35         ; See if there is enough memory space available for the specified cache
36         ;
37 024366 010500      MOV     R5,R0        ;Get top of memory address
38 024370 160400      SUB     R4,R0        ;Compute # free 64-byte blocks
39 024372 020300      CMP     R3,R0        ;Is there enough total space?
40 024374 103045      BHIS   10$          ;Br if not
41         ;
42         ; Allocate space for cache data buffers
43         ;
44 024376 013700 000000G      MOV     CSHALC,R0      ;Get # blocks in data cache
45 024402 072027 000003      ASH     #3,R0        ;Get # 64-byte blocks needed for allocation
46 024406 160005      SUB     R0,R5        ;Allocate space for cache data buffers
47 024410 010537 000000G      MOV     R5,CSHBFP    ;Save pointer to base of buffer area
48         ;
49         ; Allocate space for each control table
50         ;
51 024414 160205      SUB     R2,R5        ;Allocate space for table
52 024416 010537 000000G      MOV     R5,CA$BLK    ;Block number associated with entry
53 024422 160205      SUB     R2,R5        ;Allocate space for table
54 024424 010537 000000G      MOV     R5,CA$DVU    ;Device and unit number
55 024430 160205      SUB     R2,R5        ;Allocate space for table
56 024432 010537 000000G      MOV     R5,CA$WCT    ;Number of words
57 024436 160205      SUB     R2,R5        ;Allocate space for table

```

CSHBUF -- Allocate space for data cache tables

```

58 024440 010537 0000000      MOV      R5,CA$UFL      ;LRU chain forward link
59 024444 160205              SUB      R2,R5         ;Allocate space for table
60 024446 010537 0000000      MOV      R5,CA$UBL      ;LRU chain backward link
61 024452 160205              SUB      R2,R5         ;Allocate space for table
62 024454 010537 0000000      MOV      R5,CA$HFL      ;Hash chain forward link
63 024460 160205              SUB      R2,R5         ;Allocate space for table
64 024462 010537 0000000      MOV      R5,CA$HBL      ;Hash chain backward link
65 024466 160205              SUB      R2,R5         ;Allocate space for table
66 024470 010537 0000000      MOV      R5,CA$HSH      ;Hash chain list heads
67 024474 020527 001600      CMP      R5,#1600      ;Did we run over RT-11?
68 024500 101403              BLOS    10$            ;Br if yes
69
70                          ; Finished
71
72 024502 012603      9$:      MOV      (SP)+,R3
73 024504 012602      MOV      (SP)+,R2
74 024506 000207      RETURN
75
76                          ; Insufficient memory space available for cache data
77
78 024510      10$:    .PRINT  #TSXHD      ;Print heading
79 024516      .PRINT  #CSHOVF     ;Overflow message
80 024524 000137 004250'    JMP      INISTP       ;Abort the initialization

```

CSHBUF -- Allocate space for data cache tables

```

1          .IF      EQ,PROCID      ;Don't allow ODT for production PRO version
2          .SBTTL   GETODT -- Load ODT
3          ;-----
4          ; GETODT is called to load ODT into memory above TSX and transfer control
5          ; to it. On return, ODT has been started.
6          ;
7          ; Inputs:
8          ; R5 = Address where ODT is to be loaded.
9          ;
10         ; Outputs:
11         ; R5 = Address above top of ODT.
12         ;
13         GETODT: MOV      R1,-(SP)
14                MOV      R2,-(SP)
15                MOV      R3,-(SP)
16                MOV      R4,-(SP)
17         ;
18         ; Try to lookup ODT rel file.
19         ;
20         .LOOKUP #AREA,#1,#ODTBLK ;LOOKUP ODT REL FILE
21         BCC      1$           ;BR IF FOUND IT
22         .PRINT  #TSXHD       ;CAN'T FIND ODT
23         .PRINT  #NOODT
24         JMP      INISTP      ;ABORT INITIALIZATION
25         ;
26         ; Read first block of ODT file and determine size of ODT.
27         ;
28         1$:  ADD      #200.,R5   ;RESERVE SPACE FOR ODT STACK
29                MOV      R5,R0   ;CHECK MEMORY ADDRESS FOR OVERFLOW
30                ADD      #512.,R0
31                CALL     CHKMEN
32                .READW  #AREA,#1,R5,#256.,#0
33                BCC      2$           ;Br if no read error
34                JMP      ODTRDX     ;Read error
35         2$:  MOV      RSZ(R5),R2  ;GET SIZE OF ODT
36                MOV      R2,R3
37                ADD      R5,R3     ;GET ADDRESS ABOVE TOP OF ODT
38                MOV      R3,R0     ;CHECK MEMORY ADDRESS FOR OVERFLOW
39                CALL     CHKMEN
40                CLC
41                ROR      R2        ;GET # WORDS IN ODT
42         ; Get starting address of ODT
43                MOV      STA(R5),R0 ;GET OFFSET TO START ADDRESS
44                SUB      #ODTBAS,R0 ;CALCULATE ABSOLUTE STARTING ADDRESS
45                ADD      R5,R0
46                MOV      R0,ODTSTA ;THIS IS REAL STARTING ADDRESS
47                MOV      RBD(R5),R1 ;GET # OF BLOCK WITH RELOCATION INFO
48         ;
49         ; Read in ODT rel file image.
50         ;
51                .READW  #AREA,#1,R5,R2,#1
52                BCS      ODTRDX     ;BR IF READ ERROR
53         ;
54         ; Relocate addresses in ODT.
55         ; R5 = Address of base of ODT; R3 = Address above top of ODT.
56         ; R1 = Block number in rel file of start of relocation info.
57         ;

```

```

58      RELFIL: MOV      R3,ODTTOP      ;SAVE ADDRESS ABOVE TOP OF ODT
59      MOV      R3,RLBF
60      .IF      NE,PROCID              ;Only if PRO protection code is included
61      TSTB     PRUFLG                 ;Are we running on a Pro?
62      BNE     1$                      ;Br if yes
63      .ENDC   ;NE,PROCID
64      MOV      WRKBUF,RLBF            ;READ RELOCATION INFO HERE
65      1$:     MOV      RLBF,RLBFND
66      ADD      #1024.,RLBFND         ;GET ADDRESS OF END OF BUFFER AREA
67      MOV      R5,R4                  ;GET BASE ADDRESS OF ODT
68      SUB      #ODTBAS,R4            ;SUBTRACT LINK BASE ADDRESS
69      ; Read in relocation address list.
70      4$:     .READW  #AREA,#1,RLBF,#512.,R1
71      BCC     7$                      ;BR IF NO READ ERROR
72      TSTB     @#52                    ;END OF FILE IS OK
73      BNE     ODRDX                    ;BR IF READ ERROR
74      ; Relocate some addresses in ODT.
75      7$:     MOV      RLBF,R2          ;POINT TO RELOCATION INFO
76      3$:     MOV      (R2)+,R3        ;GET ADDRESS OF LOCATION TO RELOCATE
77      CMP      R3,#-2                  ;TIME TO STOP?
78      BEQ     9$                      ;BR IF FINISHED
79      MOV      (R2)+,R0                ;GET VALUE TO RELOCATE
80      ASL     R3                        ;CVT TO BYTE ADDRESS
81      BCC     5$                      ;BR IF ADDITIVE RELOCATION
82      SUB     R4,R0                    ;RELOCATE THE ADDRESS
83      BR      6$
84      5$:     ADD      R4,R0            ;RELOCATE THE ADDRESS
85      6$:     ADD      R5,R3            ;GET LOCATION WHERE WORD GOES
86      MOV     R0,@R3                   ;STORE RELOCATED ADDRESS
87      CMP     R2,RLBFND                ;TIME TO READ NEXT BUFFER FULL?
88      BLO     3$                      ;BR IF NOT
89      ADD     #2,R1                     ;ADVANCE BLOCK #
90      BR     4$                        ;GO READ NEXT BUFFER FULL
91      ;
92      ; Finished relocation.
93      ; Close ODT rel file.
94      ;
95      9$:     .CLOSE  #1
96      ;
97      ; Direct interrupts to 60 and 64 to an RTI instruction
98      ;
99      ;     MOV      #DORTI,@#60        ;Catch interrupt 60
100     ;     MOV      #DORTI,@#64        ;Catch interrupt 64
101     ;
102     ; Load registers with the following values for initial entry to ODT:
103     ; R0 = Base of TSINIT
104     ; R1 = Important breakpoint (^R) in TSX
105     ; R2 = Base of TSGEN
106     ; R3 = Base of TSEXC
107     ; R4 = Base of TSEMT
108     ; R5 = Return address to start execution
109     ; 0(SP) = Address of mapsys routine
110     ; 2(SP) = Address of sysmap cell
111     ;
112     MOV      #TSINIT,R0
113     MOV      #BRKPT,R1
114     MOV      #TSGEN,R2

```

```
115             MOV     #TSEXEC,R3
116             MOV     #TSEMT,R4
117             MOV     #SYSMAP,-(SP) ;PASS ADDRESS OF SYSMAP CELL TO ODT
118             MOV     #MAPSYS,-(SP) ;PASS ADDRESS OF MAPSYS ROUTINE
119             MOV     #10$,R5      ;ADDRESS FOR ODT TO RETURN TO
120             ;
121             ; Enter ODT
122             ;
123             JMP     @ODTSTA      ;JUMP TO START OF ODT
124             ;
125             ; Return from ODT.
126             ; Continue initialization of TSX.
127             ;
128             10$:   MOV     @#14,ODTTRP ;SAVE ODT BREAKPOINT ENTRY ADDRESS
129                 MOV     ODTTOP,R5   ;ADDRESS ABOVE TOP OF ODT
130                 MOV     (SP)+,R4
131                 MOV     (SP)+,R3
132                 MOV     (SP)+,R2
133                 MOV     (SP)+,R1
134                 RETURN
135             ;
136             ; Error while reading ODT rel file.
137             ;
138             ODTRDX: .PRINT #TSXHD      ;PRINT ERROR MESSAGE
139                   .PRINT #ODTRDM
140                   JMP     INISTP      ;ABORT INITIALIZATION
141             ;
142             ; RTI instruction to disable interrupts
143             ;
144             DORTI: RTI
145                   .ENDC ;EQ,PROCID
```

OPNCHN -- Open a TSX-Plus channel

```

1          .SBTTL  OPNCHN -- Open a TSX-Plus channel
2          ;-----
3          ; OPNCHN is called to set up information in a TSX-Plus channel block
4          ; to make it look as if the channel has been opened to a specified
5          ; device with a .ENTER.
6          ;
7          ; Inputs:
8          ; R0 = Address of channel block to be opened.
9          ; R2 = Rad50 device name.
10         ;
11         ; Outputs:
12         ; C-flag set ==> Cannot open the device.
13         ;
14 024530 010146 OPNCHN: MOV     R1,-(SP)
15 024532 010346          MOV     R3,-(SP)
16 024534 010003          MOV     R0,R3          ;Carry channel block address in R3
17         ;
18         ; Initialize the channel block
19         ;
20 024536 010301          MOV     R3,R1          ;Point to the channel block
21 024540 012700 000000C  MOV     #<CHNSIZ/2>,R0 ;Get # words to zero
22 024544 005021 2#:    CLR     (R1)+      ;Zero the channel block
23 024546 077002          SOB     R0,2#
24 024550 012763 000000C 000000G MOV     #<CS#OPN!CS#ENT>,C.CSW(R3) ;Initialize CSW to say chan open
25         ;
26         ; Convert the device name into device # and unit #
27         ;
28 024556 010200          MOV     R2,R0          ;Get the full device name
29 024560 004737 011476' CALL    CVTDVU        ;Convert to dev # and unit #
30 024564 103411          BCS     9#           ;Br if we don't recognize the device name
31 024566 010001          MOV     R0,R1          ;Get index # and unit #
32 024570 000301          SWAB    R1           ;Get unit # to low byte
33 024572 110163 000000G MOVB   R1,C.DEVQ(R3)  ;Set unit # in channel block
34 024576 042700 000000C BIC     #^C<CS#NMX>,R0 ;Clear all but device index number in R0
35 024602 050063 000000G BIS     R0,C.CSW(R3)  ;Store device index # into CSW
36         ;
37         ; Success
38         ;
39 024606 000241          CLC          ;Signal success on return
40         ;
41         ; Finished
42         ;
43 024610 012603 9#:    MOV     (SP)+,R3
44 024612 012601          MOV     (SP)+,R1
45 024614 000207          RETURN

```

```

1          .SBTTL  SETCHN -- Copy RT-11 channel information into TSX system chan
2          ;-----
3          ; SETCHN is called to set up a TSX system channel block to access a file
4          ; that has been opened using RT-11.  The device index number is converted
5          ; from the RT-11 device number to the corresponding TSX device number.
6          ; Note: the channel must have been opened with a .lookup (not .enter)
7          ; to use this routine.
8          ;
9          ; Inputs:
10         ; Channel # 1 = Open to file of interest.
11         ; R0 = Address of TSX channel block which is to be set up.
12         ; R2 = Rad-50 device name.
13         ;
14         ; Outputs:
15         ; Channel block pointed to by R0 is set up for future TSX I/O.
16         ; Channel # 1 is closed.
17         ;
18 024616 010146 SETCHN: MOV     R1,-(SP)
19 024620 010246         MOV     R2,-(SP)
20 024622 010346         MOV     R3,-(SP)
21 024624 010001         MOV     R0,R1          ;GET ADDRESS OF TSX CHANNEL BLOCK
22         ;
23         ; Do .SAVESTATUS to store channel information into TSX channel block.
24         ;
25 024626         .SAVEST #AREA,#1,R1      ;STORE CHANNEL STATUS INTO TSX CHANNEL BLOCK
26         ;
27         ; Now convert RT-11 device table index number into corresponding TSX
28         ; device table index number.
29         ;
30 024644 011103         MOV     (R1),R3      ;GET CSW FOR CHANNEL
31 024646 042703 177701  BIC     #^C76,R3      ;GET RT-11 DEVICE INDEX NUMBER
32 024652         .GVAL   #AREA,#404      ;GET RT-11 OFFSET TO PNAME TABLE
33 024672 060003         ADD     R0,R3      ;GET ADDRESS OF NAME OF DEVICE IN PNAME TABLE
34 024674         .GVAL   #AREA,R3      ;GET NAME OF DEVICE FROM RT-11
35 024712 013703 000000G  MOV     NUMDEV,R3      ;GET INDEX # FOR LAST TSX DEVICE
36 024716 020063 000000G 1$:  CMP     R0,PNAME(R3)    ;LOOK FOR DEVICE IN OUR TABLES
37 024722 001404         BEQ     2$      ;BR IF FOUND
38 024724 162703 000002   SUB     #2,R3      ;CHECK NEXT ENTRY
39 024730 002372         BGE     1$      ;BR IF MORE TO CHECK
40 024732 000407         BR      MTSXDV      ;VERY STRANGE THAT WE DIDN'T FIND IT
41 024734 042711 000076 2$:  BIC     #76,(R1)      ;CLEAR OUT RT-11 DEVICE #
42 024740 050311         BIS     R3,(R1)      ;STORE TSX DEVICE #
43         ;
44         ; Finished
45         ;
46 024742 012603         MOV     (SP)+,R3
47 024744 012602         MOV     (SP)+,R2
48 024746 012601         MOV     (SP)+,R1
49 024750 000207         RETURN
50         ;
51         ; Error: Could not locate Rt-11 device number in TSX device table.
52         ;
53 024752         MTSXDV: .PRINT #TSXHD      ;PRINT ERROR MESSAGE
54 024760         .PRINT #REQMIS      ;Missing a required device
55 024766 010200         MOV     R2,R0      ;GET RAD50 DEVICE NAME
56 024770 004737 025502'  CALL   PRTR50      ;DISPLAY DEVICE NAME
57 024774         .PRINT #CRLF

```

58 025002 000137 004250' JMP INISTP ;ABORT INITIALIZATION

SETSY -- Set up information about SY device

```

1          .SBTTL  SETSY  -- Set up information about SY device
2          ;-----
3          ; SETSY is called to set up information about the SY device.
4          ; It does this by determining what device RT-11 recognizes as SY.
5          ;
6          ; Inputs:
7          ;   R5 = Address of base of free memory area
8          ;
9          ; Outputs:
10         ;   SYNAME = RAD50 spec for physical system disk
11         ;   SYINDX = TSX device table index for SY device
12         ;   SYUNIT = SY device unit number
13         ;
14 025006 010146 SETSY:  MOV     R1,-(SP)
15 025010 010246         MOV     R2,-(SP)
16         ;
17         ; Set up system device unit number
18         ;
19 025012         .GVAL  #AREA,#274      ;Get system unit # from RT-11 (high byte)
20 025032 010037 000000G  MOV     R0,SYUNIT      ;Set system unit number
21         ;
22         ; Set up system device index number
23         ;
24 025036         .GVAL  #AREA,#364      ;Get RT-11 system device index number
25 025056 010002         MOV     R0,R2      ;Save device index number
26 025060         .GVAL  #AREA,#404      ;Get offset within RMON of PNAME table
27 025100 060002         ADD     R0,R2      ;Get offset to name of SY device
28 025102         .GVAL  #AREA,R2      ;Get name of RT-11 system device
29 025120 013701 000000G  MOV     NUMDEV,R1      ;Get index to last TSX-Plus device entry
30 025124 020061 000000G  1$:  CMP     R0,PNAME(R1)    ;Search for device in TSX tables
31 025130 001405         BEQ     2$      ;Br if found it
32 025132 162701 000002   SUB     #2,R1      ;Keep looking if more
33 025136 002372         BGE     1$      ;
34 025140 010002         MOV     R0,R2      ;Save name of system device
35 025142 000703         BR      MTSXDV      ;Missing device error
36 025144 010137 000000G  2$:  MOV     R1,SYINDX      ;Store index # of TSX-Plus system device
37         ;
38         ; Set up RAD50 name of SY disk
39         ;
40 025150 113702 000001G  MOVVB  SYUNIT+1,R2      ;GET SYSTEM UNIT NUMBER
41 025154 062702 000036   ADD     #36,R2      ;PUT IN "0" AS 3'RD CHARACTER OF NAME
42 025160 066102 000000G  ADD     PNAME(R1),R2    ;ADD DEVICE NAME
43 025164 010237 000000G  MOV     R2,SYNAME      ;THIS IS THE FULL SY DISK NAME
44         ;
45         ; Finished
46         ;
47 025170 012602         MOV     (SP)+,R2
48 025172 012601         MOV     (SP)+,R1
49 025174 000207         RETURN

```

RTFTCH -- Fetch a RT-11 device handler

```

1          .SBTTL  RTFTCH -- Fetch a RT-11 device handler
2          ;-----
3          ; RTFTCH is called to fetch an RT-11 device handler.
4          ; if the handler is already resident, nothing is done.
5          ; If the handler will fit in WRKBUF, it is fetched into there.
6          ; If the handler will not fit in WRKBUF, it is fetched into the top
7          ; of memory.
8          ;
9          ; Inputs:
10         ; RO = RAD50 device name.
11         ; R5 = Address of start of free memory.
12         ;
13         ; Outputs:
14         ; C-flag cleared ==> Fetch was successful.
15         ; C-flag set    ==> Error on fetch.
16         ;
17 025176 010046 RTFTCH: MOV     RO,-(SP)
18 025200 010246         MOV     R2,-(SP)
19 025202 010546         MOV     R5,-(SP)
20         ;
21         ; Set the name of the device being fetched
22         ;
23 025204 010037 000130'        MOV     RO,FETDEV      ;Set name of device whose handler to fetch
24         ;
25         ; Do a .DSTAT to get information about the handler
26         ;
27 025210         .DSTAT  #DSTBLK,#FETDEV ;Get information about the device handler
28 025222 103425        BCS     9$          ;Br if device not recognized
29         ;
30         ; Determine if the handler is currently resident
31         ;
32 025224 005737 000116'        TST     DSTBLK+4      ;Is the handler resident now?
33 025230 001021        BNE     8$          ;Br if yes
34         ;
35         ; The handler is not currently resident.
36         ; See if it will fit in WRKBUF.
37         ;
38 025232 013702 000152'        MOV     WRKBUF,R2      ;Set address where handler will be loaded
39 025236 013700 000114'        MOV     DSTBLK+2,R0    ;Get the size of the handler
40 025242 020037 000154'        CMP     RO,WRKSIZ     ;Will handler fit in WRKBUF?
41 025246 101405        BLOS    1$          ;Br if handler will fit in WRKBUF
42         ;
43         ; Handler will not fit in WRKBUF.
44         ; See if there is room to load it into the top of memory.
45         ;
46 025250 060500         ADD     R5,R0          ;Get address above top of area needed
47 025252 020037 000132'        CMP     R0,TPMEM     ;Is there room for handler?
48 025256 101013        BHI     10$         ;Br if not
49 025260 010502        MOV     R5,R2          ;Set address where handler is to be loaded
50         ;
51         ; Fetch the handler
52         ;
53 025262         1$: .FETCH  R2,#FETDEV    ;Try to fetch the handler
54 025272 103401        BCS     9$          ;Br if error on fetch
55         ;
56         ; We successfully fetched the handler
57         ;

```

RTFTCH -- Fetch a RT-11 device handler

```
58 025274 000241      8#:      CLC                ;Signal success on return
59                    ;
60                    ; Finished
61                    ;
62 025276 012605      9#:      MOV      (SP)+,R5
63 025300 012602                MOV      (SP)+,R2
64 025302 012600                MOV      (SP)+,R0
65 025304 000207                RETURN
66                    ;
67                    ; Insufficient memory available to load the handler
68                    ;
69 025306 004737 025332' 10#:     CALL     SIZERR          ;Generated system is too big -- abort
```

CHKMEM -- Check for memory space overflow

```

1          .SBTTL  CHKMEM -- Check for memory space overflow
2          ;-----
3          ;  CHKMEM is called to make sure we have not overflowed the available memory
4          ;  space while allocating space for TSX.
5          ;  If a memory overflow occurs, an error message is printed and
6          ;  the initialization is aborted.
7          ;
8          ;  Inputs:
9          ;  RO = Address to be tested for validity.
10         ;
11 025312 020037 000302'  CHKMEM: CMP      RO,MEMLIM      ; IS THE ADDRESS OK?
12 025316 103402          BLO      1$          ; BR IF OK
13 025320 004737 025332'          CALL     SIZERR          ; Generated system is too big -- abort
14 025324 004737 025352' 1$:      CALL     CCATST          ; CHECK FOR ^C ABORT REQUEST
15 025330 000207          RETURN
16
17         ;-----
18         ;  Generated system is too big.  Abort the initialization.
19         ;
20 025332  SIZERR: .PRINT  #TSXHD          ; PRINT MESSAGE HEADING
21 025340          .PRINT  #TOOBIG        ; PRINT ERROR MESSAGE
22 025346 000137 004250'          JMP      INISTP          ; ABORT INITIALIZATION
23
24         ;-----
25         ;  Check for control-C and abort initialization if requested.
26         ;
27 025352 005737 000040'  CCATST: TST      CCAFLG          ; DID USER REQUEST ^C ABORT?
28 025356 001402          BEQ      1$          ; BRANCH IF NOT
29 025360 000137 004250'          JMP      INISTP          ; ELSE ABORT INITIALIZATION
30 025364 000207          1$:      RETURN

```

PRTOCT -- Print octal value

```

1
2
3
4
5
6
7
8 025366 010146
9 025370 010246
10 025372 010001
11 025374 012702 000006
12 025400 005000
13 025402 073027 000001
14 025406 000403
15 025410 005000
16 025412 073027 000003
17 025416 062700 000060
18 025422
19 025426 077210
20 025430 012602
21 025432 012601
22 025434 000207

```

```

.SBTTL PRTOCT -- Print octal value
-----
; PRTOCT is called to print an octal value without trailing Cr-Lf.
;
; Inputs:
; RO = value to be printed.
;
PRTOCT: MOV     R1, -(SP)
        MOV     R2, -(SP)
        MOV     RO, R1          ; GET VALUE TO PRINT
        MOV     #6, R2         ; PRINT 6 DIGITS
        CLR     RO
        ASHC   #1, RO          ; GET 1ST OCTAL DIGIT (1 BIT)
        BR     2$
1$:     CLR     RO              ; INITIALIZE FOR SHIFT
        ASHC   #3, RO          ; SHIFT AN OCTAL DIGIT INTO RO
2$:     ADD     #'0, RO        ; CONVERT TO ASCII CHARACTER
        .TTYOUT                ; PRINT THE CHARACTER
        SOB    R2, 1$         ; LOOP AND PRINT MORE DIGITS
        MOV     (SP)+, R2
        MOV     (SP)+, R1
        RETURN

```

PRTDEC -- Print decimal value

```

1
2
3
4
5
6
7
8
9 025436 010146
10 025440 005046
11
12
13
14 025442 010001
15 025444 005000
16 025446 071027 000012
17 025452 062701 000060
18 025456 010146
19 025460 010001
20 025462 001370
21
22
23
24 025464 012600
25 025466 001403
26 025470
27 025474 000773
28
29
30
31 025476 012601
32 025500 000207

```

```

.SBTTL PRTDEC -- Print decimal value
-----
; PRTDEC is called to print a decimal value with leading zeroes suppressed
; and with no trailing Cr-Lf.
;
; Inputs:
; RO = Value to be printed
;
PRTDEC: MOV R1, -(SP)
        CLR -(SP) ; NULL ON STACK TO STOP US
;
; Convert value to ascii digit string and stack the digits.
;
        MOV RO, R1 ; GET VALUE TO BE CONVERTED
1$: CLR RO ; SET HIGH-ORDER PART OF VALUE TO 0
    DIV #10, RO ; DIVIDE RO-R1 BY 10.
    ADD #'0, R1 ; CONVERT REMAINDER TO ASCII DIGIT
    MOV R1, -(SP) ; AND STACK THE DIGIT
    MOV RO, R1 ; GET QUOTIENT
    BNE 1$ ; BR IF MORE DIGITS TO CONVERT
;
; Finished conversion. Print result.
;
2$: MOV (SP)+, RO ; GET A DIGIT FROM THE STACK
    BEQ 3$ ; BR IF REACHED END
    .TTYOUT ; PRINT THE DIGIT
    BR 2$ ; PRINT MORE
;
; Finished
;
3$: MOV (SP)+, R1
    RETURN

```

PRTR50 -- Print Rad-50 value

```

1          .SBTTL  PRTR50 -- Print Rad-50 value
2          ;-----
3          ; PRTR50 is called to print a Rad-50 value.
4          ;
5          ; Inputs:
6          ; RO = value to be printed.
7          ;
8 025502 010146 PRTR50: MOV     R1,-(SP)
9 025504 010246          MOV     R2,-(SP)
10         ;
11        ; Convert value to ascii string and stack the characters.
12        ;
13 025506 012702 000003          MOV     #3,R2          ;GET # CHARS TO CVT
14 025512 010001          MOV     R0,R1          ;GET VALUE TO BE CONVERTED
15 025514 005000 1$: CLR     R0          ;CLEAR HIGH-ORDER VALUE
16 025516 071027 000050          DIV     #50,R0          ;DIVIDE R0-R1 BY 50
17 025522 116101 003464'        MOVVB  R50CHR(R1),R1      ;CONVERT REMAINDER TO ASCII CHARACTER
18 025526 010146          MOV     R1,-(SP)          ;STACK THE CHARACTER
19 025530 010001          MOV     R0,R1          ;GET QUOTIENT
20 025532 077210          SOB     R2,1$          ;BR IF MORE CHARS TO CONVERT
21        ;
22        ; Finished conversion. Print the result.
23        ;
24 025534 012702 000003          MOV     #3,R2          ;GET # CHARS TO PRINT
25 025540 012600 2$: MOV     (SP)+,R0          ;GET NEXT CHARACTER
26 025542          .TTYOUT          ;PRINT THE CHARACTER
27 025546 077204          SOB     R2,2$          ;LOOP IF MORE CHARS TO PRINT
28        ;
29        ; Finished
30        ;
31 025550 012602          MOV     (SP)+,R2
32 025552 012601          MOV     (SP)+,R1
33 025554 000207          RETURN
34        ;-----
35        ; Define top of TSINIT
36        ;
37 025556 INITOP:
38        ;

```

```

1      .IF      NE,PROCID      ;Only assemble for protected Pro 350 version
2      ;
3      ; The following startup code is only included for the Pro version.
4      ; it is loaded here and executed very early during initialization
5      ; and subsequently overwritten by I/O buffers.
6      ;
7      .SBTTL  INSCHK  -- Installation validation subroutines for Pro-350
8      .MCALL  .PRINT
9      ;
10     ; Reserve an arg block area for encryption calls
11     ;
12     025556 177740 EDARGB: .WORD  -32.      ;# OF BYTES TO BE DECRYPTED (EDMTH3)
13     025560 026066' EDADDR: .WORD  DSKBUF    ;POINTER TO BUFFER TO BE DECRYPTED
14     ;
15     ; Recover license number and decrypt disk image of Pro ID to intermed. state
16     ;
17     025562 010146 INSCHK: MOV      R1,-(SP)      ;SAVE REGISTERS
18     025564 010246      MOV      R2,-(SP)
19     025566 010346      MOV      R3,-(SP)
20     025570 010446      MOV      R4,-(SP)
21     025572 010546      MOV      R5,-(SP)
22     025574 012700      MOV      (PC)+,R0      ;DECRYPT LICENSE NUMBER
23     025576 073472      .RAD50  /SCB/        ;WITH THIS CODE
24     025600 074037 026126' XOR      R0,LICNUM      ;BY XORING IT
25     025604 013737 026126' 000000G MOV     LICNUM,TSXSIT   ;MOVE LICENCE NUMBER TO TSGEN CELL
26     025612 012700 025556' MOV     #EDARGB,R0     ;POINT TO ENC/DEC ARG BLOCK (PRESET)
27     025616 004737 026254' CALL    EDMTH3        ;DECRYPT TO INTERMED STATE
28     ;
29     ; Copy Pro ID ROM low bytes into memory, and encrypt 1 step
30     ;
31     173600 IDADDR = 173600      ;ADDRESS OF START OF PRO 350 ID ROM
32     025622 012701 173600 MOV     #IDADDR,R1     ;GET POINTER TO PRO ID ROM
33     025626 012702 026130' MOV     #ROMBUF,R2     ;POINTER TO COPY OF HARDWARE ID
34     025632 010237 025560' MOV     R2,EDADDR      ;SAVE ADDRESS FOR ENCRYPTION
35     025636 005437 025556' NEG     EDARGB        ;MAKE +32. FOR ENCRYPTION
36     025642 013700 025556' MOV     EDARGB,R0     ;ALSO USE AS LOOP COUNTER
37     025646 112122 3#: MOV     (R1)+,(R2)+    ;GET NEXT LOW BYTE
38     025650 005201      INC     R1          ;SKIP ID ROM HIGH BYTES
39     025652 077003      SOB     R0,3#        ;REPEAT THROUGH 32 BYTE ROM
40     025654 012700 025556' MOV     #EDARGB,R0     ;POINT TO ENCRYPTION ARG BLOCK
41     025660 004737 026170' CALL    EDMTH2        ;PERFORM METHOD 2 ENCRYPTION
42     ;
43     ; Have intermediate state of both hardware and disk copies of Pro ID
44     ; in memory. Verify them against each other and correct memory image
45     ; of SCHED at the same time.
46     ;
47     025664 012701 026130' MOV     #ROMBUF,R1     ;POINT TO HARDWARE COPY OF ID
48     025670 012702 026066' MOV     #DSKBUF,R2     ;POINT TO DISK COPY OF ID
49     025674 012704 000000G MOV     #SCHED,R4      ;POINT TO CODE TO BE CORRECTED
50     025700 013703 025556' MOV     EDARGB,R3     ;INIT LOOP COUNTER
51     025704 004737 026060' CALL    GETLIC        ;USE LIC # AS SEED FOR EDPRNW, IN R0
52     025710 122122 4#: CMP     (R1)+,(R2)+    ;VERIFY ID'S ARE THE SAME
53     025712 001014      BNE     5#          ;ABORT IF NO MATCH ON ANY BYTE
54     025714 004737 026456' CALL    EDPRNW        ;RANDOMIZE R0 FOR XOR (LIC# INIT SEED)
55     025720 011405      MOV     @R4,R5       ;GET ENCRYPTED CODE
56     025722 074005      XOR     R0,R5        ;RESTORE FUNCTIONAL CODE
57     025724 010524      MOV     R5,(R4)+    ;PUT DECRYPTED CODE BACK IN MEMORY

```

```

58 025726 077310          SOB      R3,4#          ; REPEAT THROUGH ID TESTS
59 025730 012605          MOV      (SP)+,R5          ; RESTORE REGISTERS
60 025732 012604          MOV      (SP)+,R4
61 025734 012603          MOV      (SP)+,R3
62 025736 012602          MOV      (SP)+,R2
63 025740 012601          MOV      (SP)+,R1
64 025742 000207          RETURN          ; ID CHECKS AND CODE DECRYPTED
65
66 025744          5#:      .PRINT #TSXHD          ; ?TSX-F
67 025752          .PRINT #NOTLIC          ; NOT LICENSED FOR THIS MACHINE
68 025760 000137 004250' .JMP      INISTP          ; ID'S DON'T MATCH, ABORT INIT.
69
70          .NLIST BEX
71 025764          124      150      151 NOTLIC: .ASCIZ /This copy of TSX-Plus not licensed for use on this machine./
72          .LIST BEX
73          .EVEN
74
75          ; Subroutine to recover incremental license number. Assume it has been
76          ; decrypted already by XORing with .RAD50 /SCB/.
77
78 026060 013700 026126' GETLIC: MOV      LICNUM,R0          ; RETRIEVE DECRYPTED LIC # INTO R0
79 026064 000207          RETURN
80
81          ; Reserve room for both disk and hardware copies of the Pro ID number
82          ; and for the incremental license number
83
84 026066          DSKBUF: .BLKB 32.          ; DISK IMAGE OF PRO ID
85 026126 000000          LICNUM: .WORD 0          ; INCREMENTAL LICENSE NUMBER
86 026130          ROMBUF: .BLKB 32.          ; COPY OF ROM ID LOW BYTES

```

EDEXPL -- Comments on encryption methods

```

1          .SBTTL  EDEXPL -- Comments on encryption methods
2          ;
3          ; Encryption and decryption methods used here depend heavily on
4          ; pseudo-random numbers generated by the linear congrutential method.
5          ; See Hull and Dobell, SIAM Review, 4, 230, 1962.
6          ;
7          ; For the linear congruence relation:
8          ;
9          ;   X(I) == ( A * X(I-1) + C ) MOD M
10         ;
11         ;   X(I) is in the range 0 to M-1
12         ;
13         ; The sequence has full period M, provided that:
14         ;   1) C is relatively prime to M
15         ;   2) If p is a prime factor of M, A MOD p == 1
16         ;   3) If 4 is a factor of M, A MOD 4 == 1
17         ;
18         ; In the special case where M is a power of 2, these rules simplify to
19         ;   1) C must be odd
20         ;   2) A MOD 4 == 1
21         ;
22         .SBTTL  EDMTH2 -- Encryption method 2 (XOR with PRN high bytes)
23         ;
24         ; Using the license number as the initial seed, mask out the low 3 bits,
25         ; add 1 and call the PRN generator this many times to form the seed,
26         ; XOR each byte in the input buffer with the high byte of the next PRN
27         ; and replace the result in the input buffer. Decryption is accomplished
28         ; by a second application of the same process.
29         ;
30         ; Inputs:
31         ;   RO      Points to an arg block of the form:
32         ;           RO ----> buff_siz      ; word holding byte length of buffer
33         ;           buff_addr     ; address of buffer to be encrypted
34         ; Outputs:
35         ;   RO      Randomized
36         ;           input buffer encrypted
37         ;
38 026170 EDMTH2:
39 026170 010146      MOV     R1,-(SP)      ; Save registers
40 026172 010246      MOV     R2,-(SP)
41 026174 010346      MOV     R3,-(SP)
42         ;
43         ; Fetch byte count, buffer pointer and initialize PRN seed
44         ;
45 026176 012003      MOV     (RO)+,R3      ; Fetch byte count of input buffer
46 026200 011001      MOV     (RO),R1      ; Fetch pointer to input buffer
47 026202 004737 026060' CALL    GETLIC      ; Use license number as initial PRN seed
48 026206 010002      MOV     RO,R2      ; Copy license number to form repeat count
49 026210 042702 177770 BIC     #^C7,R2     ; No more than 8 repeats
50 026214 005202      INC     R2      ; Make sure there is at least one
51 026216 004737 026456' 2$:    CALL    EDPRNW     ; Get a new PRN
52 026222 077203      SOB     R2,2$     ; Advance the seed between 1 and 8 times
53         ;
54         ; Now sweep the buffer, XORing each byte with the high PRN byte
55         ;
56 026224 004737 026456' 1$:    CALL    EDPRNW     ; With seed in RO, get next random number
57 026230 111102      MOVB   (R1),R2     ; Get next input byte

```

```
58 026232 000300      SWAB   R0           ;Reverse PRN high and low bytes
59 026234 074002      XOR    R0,R2        ;Encrypt the byte
60 026236 000300      SWAB   R0           ;Restore PRN high and low bytes for next seed
61 026240 110221      MOVB  R2,(R1)+      ;Save encrypted bytes back into input buffer
62 026242 077310      SOB   R3,1#        ;Repeat for entire input buffer
63
64 026244 012603      MOV   (SP)+,R3     ;Restore registers
65 026246 012602      MOV   (SP)+,R2
66 026250 012601      MOV   (SP)+,R1
67 026252 000207      RETURN
```

```

1          .SBTTL  EDMTH3 -- Encryption/decryption meth 3 (swap bytes&shift bits)
2          ;
3          ; Using a prn of repeat length same as input string length, select prn
4          ; numbered bytes from the input string, combine them into a word,
5          ; shift the combined bytes a random number of bits, recombine the shifted
6          ; bits and set the confused bytes back into the prn selected string bytes.
7          ; Sign of the byte count indicates: + = encryption; - = decryption.
8          ; If the byte count is 0 or 1, no encryption occurs. If the byte count is
9          ; odd, then one random selected byte will not be encrypted.
10         ;
11         ; Inputs:
12         ;     RO      Points to an arg block of the form:
13         ;     RO ----> buff_siz      ; Word holding byte length of buffer.
14         ;                                     ; Note that buffer must be 512 or less
15         ;                                     ; in length. Flag encryption by using
16         ;                                     ; positive byte count ( 2 to 512. ).
17         ;                                     ; Flag decryption by using negative
18         ;                                     ; byte count (-2 to -512. ).
19         ;
20         ;     buff_addr      ; Address of buffer to be encrypted
21         ; Outputs:
22         ;     RO      Randomized
23         ;     Input buffer encrypted
24         ;
25 026254 010146 EDMTH3: MOV      R1,-(SP)      ; Save registers
26 026256 010246      MOV      R2,-(SP)
27 026260 010346      MOV      R3,-(SP)
28 026262 010446      MOV      R4,-(SP)
29 026264 010546      MOV      R5,-(SP)
30 026266 012003      MOV      (RO)+,R3      ; Save the string length
31 026270 011046      MOV      @RO,-(SP)     ; And save the input buffer pointer
32         ; Initialize prn generator of desired length
33 026272 010300      MOV      R3,R0      ; Recover string length
34 026274 002002      BGE      1$      ; Branch if encryption
35 026276 005400      NEG      R0      ; If decryption, get real repeat
36 026300 005203      INC      R3      ; If neg, correct for ASR round down
37 026302 004737 026506' 1$: CALL      INPRNM      ; Set up for desired repeat length
38         ; Start encryption loop through string
39 026306 006203      ASR      R3      ; Repeat for 1/2 the string length
40 026310 004737 026060'  CALL      GETLIC      ; Get lic. num. for initial seed in RO
41 026314 004737 026532'  CALL      EDPRNM      ; Seed PRN generator (-adjacent pairs)
42 026320 005703 2$: TST      R3      ; Less than 2 bytes left?
43 026322 001446      BEQ      9$      ; Quit if so (odd len -> 1 byte unch.)
44 026324 005004      CLR      R4      ; Clean out shifting registers
45 026326 005005      CLR      R5
46 026330 011601      MOV      @SP,R1      ; Retrieve buffer pointer
47 026332 010102      MOV      R1,R2      ; And second copy
48         ; Select first random byte
49 026334 004737 026532'  CALL      EDPRNM      ; Randomize in range 0 - <strlen-1>
50 026340 060001      ADD      RO,R1      ; Point to first random byte of pair
51         ; Select second random byte
52 026342 004737 026532'  CALL      EDPRNM      ; Randomize again
53 026346 060002      ADD      RO,R2      ; Point to next random byte
54         ; Use part of PRNM as semi-random shift amount
55 026350 010046      MOV      RO,-(SP)     ; Save EDPRNM seed for later
56 026352 042700 177771  BIC      #^C6,RO      ; Get a semi-random shift amount
57         ; Select encryption or decryption
    
```

```

58 026356 005703          TST      R3          ;Positive for encryption
59 026360 100411          BMI      3$          ;Branch if decrypting
60                          ; Do this part for encryption
61 026362 151104          BISB    @R1,R4        ;Get first byte without sign extend
62 026364 000304          SWAB    R4           ;And put it in the high byte
63 026366 151204          BISB    @R2,R4        ;Combine it with first byte
64 026370 000241          CLC           ;Always do at least one shift
65 026372 006004          ROR     R4           ;Shift once
66 026374 006005          ROR     R5           ;Get low bit into r5
67 026376 005400          NEG     R0           ;Right shifts for encryption
68 026400 005303          DEC     R3           ;Reduce count of pairs remaining
69 026402 000407          BR      4$          ;Skip decryption stuff
70                          ; Do this part for decryption
71 026404 151105          3$:  BISB    @R1,R5        ;Get first byte without sign extend
72 026406 000305          SWAB    R5           ;And put it in the high byte
73 026410 151205          BISB    @R2,R5        ;Combine it with the first byte
74 026412 000241          CLC           ;Always do at least one shift
75 026414 006105          ROL     R5           ;Shift once
76 026416 006104          ROL     R4           ;Get high bit into R4
77 026420 005203          INC     R3           ;Reduce count of pairs remaining
78                          ; Shift and recombine the {en|de}cryptd bytes
79 026422 073400          4$:  ASHC    R0,R4        ;Shift combined bytes 0,2,4 or 6 more
80 026424 050504          BIS     R5,R4        ;Recombine bytes
81 026426 012600          MOV     (SP)+,R0     ;Recover EDPRNM seed
82                          ; Now put encrypted bytes back into input string
83 026430 110412          MOVB   R4,@R2        ;Store low byte at second byte place
84 026432 000304          SWAB    R4           ;Get high byte
85 026434 110411          MOVB   R4,@R1        ;Store high byte at first byte place
86 026436 000730          BR      2$          ;Repeat through string
87                          ; Done, restore registers and return
88 026440 012600          9$:  MOV     (SP)+,R0     ;Just pop saved buffer address
89 026442 012605          MOV     (SP)+,R5     ;Restore registers
90 026444 012604          MOV     (SP)+,R4
91 026446 012603          MOV     (SP)+,R3
92 026450 012602          MOV     (SP)+,R2
93 026452 012601          MOV     (SP)+,R1
94 026454 000207          RETURN

```

EDPRNW -- Pseudo random number generator with MOD 2^16

```

1          .SBTTL  EDPRNW -- Pseudo random number generator with MOD 2^16
2          ;
3          ; Linear congruential pseudo-random number generator with maximum repeat
4          ; length of 65536 (2^16). cf. Hull and Dobell and Knuth, vol 2.
5          ;
6          ; Inputs:
7          ;     RO      Seed value
8          ;
9          ; Outputs:
10         ;     RO      New PRN, should be used for next seed
11         ;
12 026456  EDPRNW:
13 026456  010446      MOV     R4,-(SP)      ;Save registers
14 026460  010546      MOV     R5,-(SP)
15 026462  010004      MOV     RO,R4          ;Get seed to be multiplied
16 026464  012700      MOV     (PC)+,RO      ;Fetch multiplier
17 026466  104375      EDPRNA: .WORD 104375      ;Multiplier, can be replaced
18 026470  070400      MUL     RO,R4          ;Multiply by A
19 026472  062705      ADD     (PC)+,R5      ;Add C
20 026474  012705      EDPRNC: .WORD 012705      ;Additive, can be replaced
21 026476  010500      MOV     R5,RO          ;Return result mod 65536. as PRN
22 026500  012605      MOV     (SP)+,R5      ;Restore registers
23 026502  012604      MOV     (SP)+,R4
24 026504  000207      RETURN

```

INPRNM -- Initialize PRN generator with repeat range M

```

1          .SBTTL  INPRNM -- Initialize PRN generator with repeat range M
2          ;
3          ; Using the Hull and Dobell rules, determine acceptable values for
4          ; A and C to get a repeat range of M.
5          ;
6          ; Outputs:
7          ;     EDMULA Set with first acceptable multiplier
8          ;     EDADDC Set with first acceptable additive factor
9          ;     EDMODM Set with desired repeat length
10         ;
11 026506      INPRNM:
12 026506 012737 000040 026574'      MOV     #32,EDMODM      ;Get repeat length to cover Pro ID
13 026514 012737 000005 026562'      MOV     #5,EDMULA       ;Use first valid A
14 026522 012737 000003 026566'      MOV     #3,EDADDC      ;And first valid C
15 026530 000207                      RETURN

```

EDPRNM -- Generate pseudo-random number in specified range M

```

1          .SBTTL  EDPRNM -- Generate pseudo-random number in specified range M
2          ;
3          ; *****
4          ; * INPRNM MUST BE CALLED BEFORE FIRST SEED IS PASSED TO THIS ROUTINE!!!! *
5          ; *****
6          ;
7          ; Using linear congruential method (cf. Hull and Dobell), generate
8          ; pseudo-random number using seed passed in R0. Return new PRN in R0.
9          ;
10         ; Inputs:
11         ;     R0      Seed value, must be in range 0 to M (EDMODM)
12         ;
13         ; Outputs:
14         ;     R0      New pseudo-random number, should be used for next seed
15         ;
16 026532  EDPRNM:
17 026532  010446      MOV     R4,-(SP)      ; Save R4 and R5
18 026534  010546      MOV     R5,-(SP)
19 026536  020037  026574'  CMP     R0,EDMODM    ; Is seed in range 0 to EDMODM?
20 026542  103405      BLO    1$           ; Branch and proceed if so
21 026544  010005      MOV     R0,R5        ; Set up to divide it by EDMODM
22 026546  005004      CLR     R4           ; Set up for divide
23 026550  071437  026574'  DIV     EDMODM,R4    ; Divide it
24 026554  010500      MOV     R5,R0        ; And use remainder as seed
25 026556  010004      1$:  MOV     R0,R4        ; Get current seed ready to be multiplied
26 026560  070427      MUL     (PC)+,R4     ; Multiply by chosen A
27 026562  061125      EDMULA: .WORD 25173. ; Replace at run-time with 5
28 026564  062705      ADD     (PC)+,R5     ; Add in C
29 026566  033031      EDADDC: .WORD 13849. ; Replace at run-time with 3
30 026570  005004      CLR     R4           ; Clear high word for division
31 026572  071427      DIV     (PC)+,R4    ; Perform mod M
32 026574  000400      EDMODM: .WORD 256.  ; Replace at run-time with 32.
33 026576  010500      MOV     R5,R0        ; Return remainder
34 026600  012605      MOV     (SP)+,R5     ; Restore R4 and R5
35 026602  012604      MOV     (SP)+,R4
36 026604  000207      RETURN
37         ;
38         . IFF     ; NE, PROCID      ; Assemble if protection code not included
39 DSKBUF:  ; Define dummy DSKBUF global symbol
40         . ENDC   ; NE, PROCID
41         ;
42         ; Address of real top of TSINIT, including PRO init code
43         ;
44 026606  PROITP:
45 000000      .CSECT  TSXEND
46         . END

```

Errors detected: 0

\*\*\* Assembler statistics

Work file reads: 0  
 Work file writes: 0  
 Size of work file: 11342 Words ( 45 Pages)  
 Size of core pool: 17920 Words ( 70 Pages)  
 Operating system: RT-11

Elapsed time: 00:02:08.36

TSINIT -- TSX startup initializ MACRO V05.04 Thursday 17-Dec-87 08:39 Page 90-1  
EDPRNM -- Generate pseudo-random number in specified range M

DK: PROASM, LP: PROASM=DK: PROASM, TSINIT/C/N: SYM

\$BBIT	2-129	40-72											
\$DEAD	2-122	29-27	38-20										
\$FORM	2-125	40-67											
\$HARD	2-154	22-26	23-30										
\$MEMSZ	2-141	45-102*											
\$NDIN	2-112	22-37											
\$OVRH	2-136	67-25											
\$PHONE	2-109	22-35											
\$SXON	2-90	40-25											
\$TAB	2-125	40-66											
... V1	7-29	7-29	7-45	7-46	24-19	24-23	24-31	24-32	24-33	24-36	24-54	24-55	
	24-61	24-63	24-67	24-78	24-176	25-221	25-226	25-227	25-243	25-243	29-50	29-50	
	29-50	29-57	29-57	29-58	29-58	29-58	29-62	29-62	29-62	29-70	29-70	29-72	
	29-72	29-84	29-95	29-96	29-102	29-103	29-108	29-111	29-116	29-119	30-30	30-30	
	30-30	30-42	30-42	30-43	30-43	30-43	30-47	30-47	30-47	30-55	30-55	30-56	
	30-56	30-68	30-77	30-78	30-84	30-85	31-60	31-61	31-85	31-85	31-85	31-89	
	31-89	31-90	31-90	31-90	31-94	31-94	31-94	31-99	31-99	31-101	31-101	31-110	
	31-131	31-132	31-138	31-139	39-21	39-21	39-21	39-27	39-27	39-62	39-62	39-62	
	39-75	39-76	39-81	39-82	42-22	42-22	42-22	42-29	42-29	42-31	42-31	42-68	
	42-68	42-68	42-72	42-73	42-73	42-73	42-78	42-78	42-78	42-82	42-82	42-83	
	42-83	42-84	42-84	42-84	42-91	42-101	42-102	42-108	42-109	43-43	43-43	43-43	
	43-47	43-48	43-48	43-48	43-52	43-52	43-52	43-55	43-55	43-60	43-65	43-65	
	43-66	43-76	43-77	43-83	43-84	45-151	45-152	47-76	47-77	51-76	51-76	53-25	
	53-25	53-25	53-39	53-39	55-25	55-25	57-21	57-21	57-21	57-29	57-29	57-63	
	57-63	57-111	57-111	59-6	59-7	59-10	60-36	60-36	60-50	60-50	61-35	61-35	
	67-17	67-17	67-51	67-52	69-24	69-24	69-52	69-53	69-58	69-59	72-34	72-35	
	73-30	73-30	73-73	73-74	74-25	74-25	74-25	74-54	74-54	74-81	74-81	74-93	
	74-94	74-99	74-104	74-105	75-78	75-79	78-25	78-25	78-32	78-34	78-53	78-54	
	78-57	79-19	79-24	79-26	79-28	80-27	80-53	81-20	81-21	82-18	83-26	84-26	
	85-66	85-67											
... V2	7-29	7-29	7-29#	7-29#	25-243	25-243	25-243#	25-243#	29-50	29-50	29-50#	29-50#	
	29-57	29-57#	29-58	29-58	29-58#	29-58#	29-62	29-62	29-62#	29-62#	29-70	29-70	
	29-70#	29-70#	29-72	29-72#	30-30	30-30	30-30#	30-30#	30-42	30-42#	30-43	30-43	
	30-43#	30-43#	30-47	30-47	30-47#	30-47#	30-55	30-55	30-55#	30-55#	30-56	30-56#	
	31-85	31-85	31-85#	31-85#	31-89	31-89#	31-90	31-90	31-90#	31-90#	31-94	31-94	
	31-94#	31-94#	31-99	31-99	31-99#	31-99#	31-101	31-101#	39-21	39-21	39-21#	39-21#	
	39-27	39-27	39-27#	39-27#	39-62	39-62	39-62#	39-62#	42-22	42-22	42-22#	42-22#	
	42-29	42-29	42-29#	42-29#	42-31	42-31	42-31#	42-31#	42-68	42-68	42-68#	42-68#	
	42-72	42-72#	42-73	42-73	42-73#	42-73#	42-78	42-78	42-78#	42-78#	42-82	42-82	
	42-82#	42-82#	42-83	42-83#	42-84	42-84	42-84#	42-84#	43-43	43-43	43-43#	43-43#	
	43-47	43-47#	43-48	43-48	43-48#	43-48#	43-52	43-52	43-52#	43-52#	43-55	43-55	
	43-55#	43-55#	43-60	43-60	43-60#	43-60#	43-65	43-65#	51-76	51-76#	53-25	53-25	
	53-25#	53-25#	53-39	53-39	53-39#	53-39#	55-25	55-25	55-25#	55-25#	57-21	57-21	
	57-21#	57-21#	57-29	57-29	57-29#	57-29#	57-63	57-63	57-63#	57-63#	57-111	57-111#	
	60-36	60-36	60-36#	60-36#	60-50	60-50	60-50#	60-50#	61-35	61-35	61-35#	61-35#	
	67-17	67-17	67-17#	67-17#	69-24	69-24	69-24#	69-24#	73-30	73-30	73-30#	73-30#	
	74-25	74-25	74-25#	74-25#	74-54	74-54	74-54#	74-54#	74-81	74-81#	78-25	78-25	
	78-25#	78-25#											
AHEND	2-106	50-21											
ALBFX	25-161	38-12#											
ALCHRB	24-142	27-12#											
ALCOVL	67-34	69-15#											
ALCSLO	24-103	37-12#											
ALCWRK	24-138	26-12#											
ALOCBF	24-102	36-14#											
AREA	2-182#	7-29	24-19	24-23	24-61	24-63	24-78	24-176	25-221	25-243	29-50	29-58	



CL\$STA	2-126	40-83*					
CLDEVX	2-142	32-29	40-94*				
CLEDFS	2-125	40-53	40-54				
CLHEAD	2-142	40-99	40-117				
CLINCP	2-154	23-21					
CLINIT	25-73	40-11#					
CLK100	2-187#	6-165	24-14*				
CLKRTI	2-106	6-71					
CLKVEC	2-157	6-71*	6-72*	6-165*	24-14	25-78*	
CLORSZ	2-111	40-39					
CLOTIR	2-154	23-20					
CLSIZE	2-142	40-100					
CLSTS	2-117	40-96	40-114				
CLTOTL	2-112	25-71	32-18	40-18	40-101	40-108	70-149
CLVEND	4-68#	40-132					
CLVERS	2-110	40-123	40-137*				
CLVTBL	4-60#	40-128					
CO\$BBT	2-129	40-74					
CO\$DEF	2-125	40-62					
CO\$FF	2-125	40-71					
CO\$TAB	2-125	40-68					
CONFQ2	2-104	24-165*	24-166*	24-169*	25-39	25-79	
CONFIG	2-103	24-163*	25-5				
CONSPC	5-13#	29-108					
COSRT	5-34#	74-94					
CRLF	5-9#	29-111	29-119	59-10	74-99	78-57	
CS\$ENT	2-144	77-24					
CS\$NMX	2-88	77-34					
CS\$OPN	2-144	77-24					
CSHALC	2-121	36-71	70-161	75-17	75-18	75-28	75-44
CSHBAS	2-104	8-39	10-37*				
CSHBFP	2-147	75-47*					
CSHBUF	25-206	75-12#					
CSHDEV	2-86	36-63*					
CSHDVN	2-86	36-67*					
CSHOVF	5-39#	75-79					
CSHSIZ	2-147	75-33*					
CSHVEC	2-105	8-42					
CURDEV	2-204#	60-54*	61-40	63-45			
CURNAM	2-205#	52-25*	57-20*	59-8			
CVTDVU	31-37	34-14#	77-29				
CW\$50H	2-103	25-5					
CW\$BTH	2-102	24-161					
CW\$ESP	2-112	24-166					
CW\$FB	2-102	24-162					
CW\$FGJ	2-102	24-162					
CW\$GDH	2-102	24-161					
CW\$LGS	2-102	24-161					
CW\$PRO	2-106	24-116	25-39	25-79			
CW\$QBS	2-114	24-169					
CW\$USR	2-103	24-162					
CW\$XM	2-103	24-162					
CXTALC	25-93	46-12#					
CXTBAS	2-111	46-24					
CXTBUF	2-118	36-134*					
CXTPAG	2-87	29-40	39-41	46-45*	48-31		



Cross reference table (CREF V05.04)

DZOINT	2-135											
EDADDC	89-14*	90-29#										
EDADDR	85-13#	85-34*										
EDARGB	85-12#	85-26	85-35*	85-36	85-40	85-50						
EDMODM	89-12*	90-19	90-23	90-32#								
EDMTH2	85-41	86-38#										
EDMTH3	85-27	87-25#										
EDMULA	89-13*	90-27#										
EDPRNA	88-17#											
EDPRNC	88-20#											
EDPRNM	87-41	87-49	87-52	90-16#								
EDPRNW	85-54	86-51	86-56	88-12#								
EMMAP	2-99	6-123	45-109	57-77	57-82	62-36	62-55	73-47	73-52	74-65	74-71	
EMTENT	2-107	6-89										
ENTVEC	8-35	8-43	8-51	9-11#								
ERHMSG	5-19#	53-41	55-27	57-65								
ERHNDV	5-20#	53-58										
ERRLOG	2-134	58-22										
EXCBUF	2-84	26-17										
EXTLSI	2-245#	45-119	51-54									
FC##SZ	2-134	70-69										
FC#LBN	2-115	38-61										
FETDEV	2-198#	80-23*	80-27	80-53								
FF##SZ	2-115	38-56										
FILBLK	2-203#	57-49*	57-63	57-90*	73-20*	73-30	73-57*					
FMEMHI	2-200#	25-168	45-103*	45-114	47-65*	57-105*	57-107	64-27	64-34*	64-35		
FMEMLO	2-201#	25-167*	47-37	47-62	64-28							
FNDHRB	2-58	63-18#										
FORCEO	31-26	35-8#										
FORK	2-89	58-15										
FPTRAP	2-117	6-95										
FQ##SZ	2-95	36-37										
FREIOQ	2-117	36-18*										
FREPGS	2-118	48-22*										
FRKGEN	2-95	36-34	36-37									
FRKINI	2-95	36-36*										
FSTDL	2-94	38-22										
FSTIOL	2-154	23-11	36-102	37-24								
FW##SZ	2-115	38-58										
GENTOP	2-156	24-27										
GETHNH	25-202	56-13#										
GETHNL	25-97	50-14#										
GETLIC	85-51	85-78#	86-47	87-40								
GETMAP	25-172	68-12#										
GETOVL	68-34	72-12#										
GETSRT	25-180	25-195	74-13#									
GTBYT	2-101	58-25										
GTLIN	2-229#	24-51										
H. CSR	2-95	53-79										
H. DSTS	2-89	53-48	54-46									
H. DVSZ	2-93	54-45										
H. GEN	2-89	53-67	53-71	57-30								
H. INS	2-95	53-88										
H. SIZ	2-93	54-44										
HANDSK	2-83	54-38*										
HANENT	2-89	12-14	14-21*	25-51*	25-66*	40-99*	40-117*	55-33*	55-34*	57-34*	60-21	60-61





















Cross reference table (CREF V05.04)

... CM0	29-84	30-68	31-110	42-91	43-66	80-27	80-53						
... CM1	7-29	25-243	29-50	29-62	29-70	30-30	30-47	30-55	31-85	31-94	31-99	39-21	
	39-27	39-62	42-22	42-29	42-31	42-68	42-78	42-82	42-84	43-43	43-52	43-55	
	43-60	53-25	53-39	55-25	57-21	57-29	57-63	60-36	60-50	61-35	67-17	69-24	
	73-30	74-25	74-54	78-25									
... CM2	7-29	7-29	7-29	7-29	24-19	24-23	24-61	24-63	24-78	24-176	25-221	25-243	
	25-243	25-243	25-243	29-50	29-50	29-58	29-58	29-62	29-62	29-62	29-70	29-70	
	29-70	29-70	30-30	30-30	30-43	30-43	30-47	30-47	30-47	30-55	30-55	30-55	
	30-55	31-85	31-85	31-90	31-90	31-94	31-94	31-94	31-99	31-99	31-99	31-99	
	39-21	39-21	39-27	39-27	39-27	39-27	39-27	39-62	39-62	42-22	42-22	42-29	42-29
	42-29	42-29	42-31	42-31	42-31	42-31	42-68	42-68	42-73	42-73	42-78	42-78	
	42-78	42-82	42-82	42-82	42-82	42-84	42-84	43-43	43-43	43-48	43-48	43-52	
	43-52	43-52	43-55	43-55	43-55	43-55	43-60	53-25	53-25	53-39	53-39	53-39	
	53-39	55-25	55-25	55-25	55-25	57-21	57-21	57-29	57-29	57-29	57-29	57-63	
	57-63	57-63	57-63	60-36	60-36	60-36	60-36	60-50	60-50	60-50	60-50	61-35	
	61-35	61-35	61-35	67-17	67-17	67-17	67-17	69-24	69-24	69-24	69-24	73-30	
	73-30	73-30	73-30	74-25	74-25	74-54	74-54	74-54	74-54	78-25	78-32	78-34	
	79-19	79-24	79-26	79-28									
... CM3	29-57	29-72	30-42	30-56	31-89	31-101	42-72	42-83	43-47	43-65	51-76	57-111	
	74-81												
... CM5	7-29	7-45	7-46	24-19	24-23	24-31	24-32	24-33	24-36	24-54	24-55	24-61	
	24-63	24-67	24-78	24-176	25-221	25-226	25-227	25-243	29-50	29-58	29-62	29-70	
	29-84	29-95	29-96	29-102	29-103	29-108	29-111	29-116	29-119	30-30	30-43	30-47	
	30-55	30-68	30-77	30-78	30-84	30-85	31-60	31-61	31-85	31-90	31-94	31-99	
	31-110	31-131	31-132	31-138	31-139	39-21	39-27	39-62	39-75	39-76	39-81	39-82	
	42-22	42-29	42-31	42-68	42-73	42-78	42-82	42-84	42-91	42-101	42-102	42-108	
	42-109	43-43	43-48	43-52	43-55	43-60	43-66	43-76	43-77	43-83	43-84	45-151	
	45-152	47-76	47-77	53-25	53-39	55-25	57-21	57-29	57-63	59-6	59-7	59-10	
	60-36	60-50	61-35	67-17	67-51	67-52	69-24	69-52	69-53	69-58	69-59	72-34	
	72-35	73-30	73-73	73-74	74-25	74-54	74-93	74-94	74-99	74-104	74-105	75-78	
	75-79	78-25	78-32	78-34	78-53	78-54	78-57	79-19	79-24	79-26	79-28	80-27	
	80-53	81-20	81-21	82-18	83-26	84-26	85-66	85-67					
... CM6	24-19	24-23	24-61	24-63	24-78	24-176	25-221	78-32	78-34	79-19	79-24	79-26	
	79-28												
... CM7	7-29	25-243	29-70	30-55	31-99	39-27	42-29	42-31	42-82	43-55	53-39	55-25	
	57-29	57-63	60-36	60-50	61-35	67-17	69-24	73-30	74-54				
. CLOSE	2-50#	29-57	29-72	30-42	30-56	31-89	31-101	42-83	43-65	51-76	57-111	74-81	
. CSTAT	2-53#	43-60											
. DATE	2-52#	25-222											
. DELET	2-51#	29-58	30-43	31-90	42-73	43-48							
. DSTAT	2-52#	80-27											
. ENTER	2-49#	29-62	30-47	31-94	42-78	43-52							
. EXIT	2-51#	6-168	7-47										
. FETCH	2-52#	80-53											
. GTIM	2-52#	24-19	25-221										
. GVAL	2-49#	24-78	24-176	78-32	78-34	79-19	79-24	79-26	79-28				
. HERR	2-51#	24-57											
. LOCK	2-52#	24-81											
. LOOKU	2-49#	29-50	30-30	31-85	39-21	39-62	42-22	42-68	42-84	43-43	53-25	57-21	
	74-25												
. PRINT	2-50#	7-45	7-46	24-31	24-32	24-33	24-36	24-54	24-55	25-226	25-227	29-95	
	29-96	29-102	29-103	29-108	29-111	29-116	29-119	30-77	30-78	30-84	30-85	31-60	
	31-61	31-131	31-132	31-138	31-139	39-75	39-76	39-81	39-82	42-101	42-102	42-108	
	42-109	43-76	43-77	43-83	43-84	45-151	45-152	47-76	47-77	59-6	59-7	59-10	
	67-51	67-52	69-52	69-53	69-58	69-59	72-34	72-35	73-73	73-74	74-93	74-94	
	74-99	74-104	74-105	75-78	75-79	78-53	78-54	78-57	81-20	81-21	85-8#	85-66	

